# i.MX6 series Firmware

## 1 Introduction

The purposes of the firmware library for the i.MX6 series are to:

- Provide example driver code that enables main features of a peripheral without the need for an operating system.
- Provide example usage for each driver to demonstrate their main features.
- Provide a solid and simple environment for quick board bring-up.

The outcome is finally a test suite where each module can be individually tested. Based on a common initialization of the i.MX6 and the development board, a test for a specific peripheral is launched with the help of a serial console for human interaction.

Included in the release package are Firmware Guide (iMX6FG\_RevC.pdf) and IOMUX Tool application (IOMux.exe).

# 2 Versions Available

	Version	Released Date	Description			
Software (iMX6DQ_platlib_relea se_2011w50.tar.gz)	V2.0.0	16-MAR-2012	Initial Release with following drivers IPU, ESAI, SSI, AUDMUX, I2C, HDMI, GPT, EPIT, GIC, SDMA, UART and USDHC			
Firmware Guide (IMX6FG_RevC.pdf)	Rev C	16-MAR-2012	Detailed driver documentation available with the release			
IOMUX Tool	V3.0.4	16-MAR-2012	IOMUX configuration tool windows application			

# 3 Source code structure of the library:

Description	Location		
Configuration file used to enable what should be	./configs		
supported by a board. e.g mx6dq_sabre_lite.conf	., contrys		
Source code related to the ARM Cortex-A9 core.	./src/cortex_a9		
Common headers which are platform and i.MX independent.	./src/include		
Headers specific to the i.MX6 series.	./src/include/mx6dq ./src/include/mx6sdl		
Global library source file such main, system initialization,	./src/init		
Source code specific to the i.MX6 series.	./src/mx6dq/ ./src/mx6sdl/		
Parent directory for all drivers, includes, and their unit tests and docs.	./sdk/		

#### Example for a driver:

Description	Location		
SDMA driver.	./sdk/sdma/drv		
SDMA driver related includes.	./sdk/sdma/inc		
SDMA driver unit tests.	./sdk/sdma/test		

# 4 Build procedure

# 4.1 System requirements

To build the firmware library a 'Linux host' or 'Windows host + Cygwin' is necessary.

#### (a) Working with Cygwin:

Cygwin can be installed and obtained from here:

http://www.cygwin.com/

The installation procedure is documented on this site.

Some packages are necessary such 'make', 'bash', 'perl', but that list is not thorough.

The cygpath program is a utility that converts Windows native filenames to Cygwin POSIX-style pathnames and vice versa. It can be used when a Cygwin program needs to pass a file name to a native Windows program, or expects to get a file name from a native Windows program.

When using the codesourcery toolchain, it is necessary to set this environment variable: export CYGPATH=cygpath

**Note:** The make package of Cygwin is the version v3.81. The build might fail due to known problem with that version to handle correctly Windows pathname. To avoid this issue, it is recommended to upgrade the version manually to v3.82.

#### (b) Installing the toolchain:

The code is compiled with the ARM-NONE-EABI GCC toolchain obtained here: <a href="https://sourcery.mentor.com/sgpp/lite/arm/portal/subscription3053">https://sourcery.mentor.com/sgpp/lite/arm/portal/subscription3053</a>
Click on "<a href="Download Sourcery G++ Lite 2011.03-42">Download Sourcery G++ Lite 2011.03-42</a>" and download the Windows or Linux tar ball or installer.

Install the package in "/opt" or any other local folder, and make sure that the PATH environment variable allows accessing these executables used for the build process:

```
CC= arm-none-eabi-gcc
AS = arm-none-eabi-as
AR = arm-none-eabi-ar
LN = arm-none-eabi-ld
```

#### Example:

```
export PATH=$PATH:/toolchain_install_path
```

Otherwise, it is possible to re-define the above list in this file

"./firmware\_install\_path/make.def" with the complete path to the executable.

#### 4.2 Source code installation

The firmware source code package can be installed anywhere.

It is not recommended to use a Windows unzip program to extract the sources. Instead, the equivalent command of Cygwin or Linux should be used.

#### Example:

```
tar zxvf mx6series sdk release.tar.qz
```

#### 4.3 Build command

target=mx6sdl boards:
 sabre ai

rev a, b

The build command is ./tools/build sdk.

```
The detailed usage that displays for help is the following:
Usage:
   build_sdk [-t <target>] [-b <board>] [-v <rev>] [-r <test>] [-n] [-
c] [-1]
Generates makefiles for the i.MX SDK project.
Options:
   -t, -target=<target> Specify the target name. Optional, and the
default is all.
   -b, -board=<brd>
                           Specify the board name. Optional, and the
default is all.
   -v, -board_rev=<rev> Specify the board revision. Optional, and
the default is all.
   -r, -test=<name>
                           Optional argument to select a single test,
or ALL for all tests. Defaults to ALL.
  -c, -clean
                            Optional flag to force a clean build.
   -n, -no-build
                            Don't actually run make.
   -l, -list-builds
                            Optional flag to list target, board,
board_rev cominations to be built.
   Short options take the same arguments as their respective long
options.
   The '=' is optional for long options; arguments can be specified as
-arg=value or
   -arg value. Short options require a space between the option and
value.
Generates makefiles for the specified combination of options. If -
clean was specified,
'make clean' will be run. Then a regular build will be executed,
unless the -no-build
argument is present. Makefiles are generated only for packages that
are part of the
selected board.
Valid target and board combinations:
  target=mx6dq boards:
       sabre_ai
                       rev a, b
       evb
                        rev a (MX6QCPUDDR3)
       smart_device
                      rev a, b
       sabre lite
                       reva, b, c
```

```
evb rev a (MX6QCPUDDR3) smart_device rev a, b
```

#### target=all

#### Examples:

```
build_sdk -target mx6dq -board evb
```

Build for the  $\mbox{mx6dq}$  evb board with default board rev a, and all tests.

```
build_sdk -t mx6dq -b sabre_ai -v a -test sdma -clean
```

Build the mx6dq sabre\_ai board with explicit board rev a, but only build the sdma test.

Clean before building.

```
build_sdk -target=mx6dq -board=evb -test=gpmi -no-build
```

Generate the makefiles for mx6dq evb and the gpmi test, but don't actually run make.

```
build_sdk -target=all -test vpu
```

Clean build of vpu\_test for all valid combinations of target, board, and board\_rev.

Note: even though the above example commands generate a makefile in the installation path, this one can't be used to build the SDK.

#### The supported boards are:

- evb (Freescale validation board). Revision A only.
- sabre\_ai (Automotive reference design). Revision A, B.
- sabre\_lite (General purpose lite reference design). Revision A, B, C.
- smart\_device (Smart device reference design). Revision A, B.

The listed tests below are not available for all boards. It is usually due to some functionalities that are not supported, or specific IO configuration not implemented.

#### Available tests:

Module	test	evb	sabre_ai	sabre_lite	smart_device
Audio: SSI and audiomux	audio	Х	Х	Х	X
Camera	camera	-	-	-	Х
CAN	flexcan	-	Х	-	-
ENET for Ethernet	enet	Х	-	-	-
EPIT	epit	Х	Х	Х	X
Generic Interrupt Controller - GIC	gic	X	X	X	X
GPMI	gpmi	Х	-	-	-
GPT	gpt	Х	Х	Х	X
GPU	gpu	Х	-	-	-
HDMI	hdmi	Х	Х	Х	Х
I2C	i2c	Х	Х	Х	Х
IPU for display	ipu	Х	Х	Х	X
LDB for LVDS display	ipu	Х	Χ	X	X
KEYPAD	kpp	X	-	1	-
MIPI	mipi	X	X	1	-
OCOTP for e-fuses	ocotp	X	X	X	X
Low power modes	power_modes	X	Х	X	X
PCle	pcie	Х	-	-	-
PWM	pwm	X	-	-	-
SATA for Hard Drive	sata	Х	Х	X	X
SDMA	sdma	Х	X	X	X
SNVS RTC	snvs_rtc	Х	Х	X	X
SNVS SRTC	snvs_srtc	X	Х	X	X
SPI	spi	Х	-	-	-
UART	uart	Х	Х	Х	Х
USB	usb	-	Х	-	-
USDHC for SD/MMC	usdhc	Х	Х	Х	Х
Video decoder	vpu	Х	-	Х	X
Watchdog	wdog	Х	Х	Х	X
WEIM	weim	Χ	Х	1	-
ALL	ALL	X	-	ı	-

'x' stands for all boards revisions, otherwise the supported revision is specified. The option ALL includes all tests with the possibility to run, re-run, or skip some of tests.

Note: the warnings detected during the build process are considered as errors.

```
Once the build is finished a message should appear such:
```

#### The output binary and ELF files can be found in:

```
./output/mx6dq/sabre_ai_rev_a/bin/mx6dq_sabre_ai_rev_a-vpu-sdk.bin.elf ./output/mx6dq/sabre_ai_rev_a/bin/mx6dq_sabre_ai_rev_a-vpu-sdk.bin
```

The file name contains the targeted CPU and board, as well as the tested module.

It is sometimes required to clean the build to take into account some changes. The following command will remove the directory ./firmware\_install\_path/output/mx6dq before building:

```
./tools/build_sdk -t mx6dq -b sabre_ai -v a -test sdma -clean
```

#### 5 How to run a test

## 5.1 Setup required

An i.MX61 EVB board is necessary to run a test. Please refer to the appropriate board user guide to prepare this setup.

The user interaction and output information are available through the serial port UART4 connected to a host running a terminal such TeraTerm, minicom, HyperTerminal,...

The configuration for the terminal is common: 115200bps, 8-bit, no parity, 1 stop bit.

# 5.2 Download with a JTAG probe

The code can be downloaded and executed through any JTAG tool which supports Cortex-A9.

It is necessary to have a board initialization file that will initialize some of the clocks, the MMDC controller and its interface, as well as the DDR3.

The source package contains the include file used by the ARM Realview Debugger to initialize the board (e.g. mx6dq\_init.inc).

## 5.3 Download with the manufacturing tool

The manufacturing tool can be obtained from the Freescale i.MX Design tool web page: http://www.freescale.com/webapp/sps/site/overview.jsp?code=IMX DESIGN

```
Below is an example of profile that can be added in the ucl.xml:

<LIST name="DWLD_IN_SDP" desc="Download and execute a binary!">

<CMD type="find" body="Recovery" timeout="180"/>

<CMD type="boot" body="Recovery" file ="mx6dqsabre_lite-vpu-sdk.bin" > Loading platlib image </CMD>

<CMD type="jump" > Jumping to platlib image. </CMD>

</LIST>
```

## 5.4 Using SD boot

The SDK binary image can be programmed on a SD card using 2 different methods.

#### **Using a Windows host:**

By using the cfimager-imx.exe provided with the release package under folder ./tools/windows. Command:

```
cfimager-imx.exe -o 0x0 -f
./output/mx6dq/evb_rev_a/bin/mx6dq_evb_rev_a-ALL-sdk.bin -d <your
drive letter for the SD card>
```

For example, if the drive letter of the SD card reader is "f", type the following:

```
cfimager-imx.exe -o 0x0 -f ./output/mx6dq/evb rev_a/bin/mx6dq_evb_rev_a-ALL-sdk.bin -d f
```

There is also the option of formatting the card first before programming it, though be warned it will take more time for this operation to complete due to the card formatting.

To invoke the card formatting, simply append the above command with "-a", as provided in the following example (again, assuming the drive letter is "f"):

```
cfimager-imx.exe -o 0x0 -f ./output/mx6dq/evb rev_a/bin/mx6dq_evb_rev_a-ALL-sdk.bin -d f -a
```

#### Using a Linux host:

By using the command "dd" under Linux.

```
dd if=output/mx6dq/evb rev_a/bin/mx6dq_evb_rev_a-ALL-sdk.bin
of=/dev/sdx seek=2 skip=2 bs=512 && sync
/dev/sdx is the Linux device for your SD card.
```

The seek option allows to not change the first 1kB of the SD card where a partition table could reside.

The skip option is to remove the unnecessary first 1kB from the binary as the seek option is used.

Once the SD card is programmed successfully with a bootable SDK image, follow below instructions on how to run it on the i.MX6DQ EVB board:

- Insert SD card with SDK binary into SD slot.
- Configure boot dip switches for boot from SD SW1: off-off-off-off-off-off-on-off; SW2: off-off-on-on-off-off-off; SW5: all off.
- Power on board, and follow on screen prompt commands on host' terminal window.

### 6 How to add a new driver and test

A new driver should typically be added in the ./sdk/new\_driver/drv and its test into ./sdk/new driver/test.

In the board config file such mx61 evb.conf, the following lines must be added:

```
./src/sdk/new_driver/drv
./src/sdk/new_driver/test
```

Everything contained in these repositories will be compiled.

The main test file should contain a test function named: int32\_t new\_driver\_test(void). Hence, when building, the parameter "- new\_driver" can simply be used.

When using the build ALL option, this test can also be added into the function void ALL\_test (void) available in ./src/mx6dq/mx6dq.c.

#### 7 Firmware Guide

Firmware guide is a detailed documentation of the work done in platform library with pseudo code examples of driver code. The document (IMX6FG\_RevC.pdf) is placed in the root folder of release package.

#### 8 IOMUX tool

IOMUX tool helps generate IOMUX configuration source code in C programming language. The source code generated is used as is in platform library. IOMUX tool is to be used by board designers to generate the design XML file for PINMUX and pad configuration for a board. It is also used to generate source code (.h and .c files) with functions to setup IO configuration to free device driver developers from board design details of each pin and pad.

The IOMUX tool is available with release package in folder "IOMUX Tool", also included is readme file and sample XML IOMUX design file for EVB board type for i.MX6 Series.

## 9 Install the video decoding/encoding demo:

This guide is elaborated to show how to setup and run the dual video + dual HD1080P60 display demo on i.MX6x EVB / Sabre Lite / Smart Device boards.

A single display can be used as well.

Text for user input.

Text for important hints.

- How to build the program?
   ./tools/build-sdk -t mx6dq -b evb -r vpu -c
- 2. How to setup the demo?
  - a) Create the image over SD card. Under linux, using fdisk/mkfs.vfat/dd to create a bootable image together with the FAT32 file system on the same SD card.
    - sudo fidsk /dev/sdx , sdx is the device name of your SD card.

sudo fdisk /dev/sdb

Command (m for help): m

Delete existing partition if there is.

Command (m for help): d

Selected partition 1

```
Command (m for help): n
Command action
  e extended
  p primary partition (1-4)
Partition number (1-4): 1
First cylinder (1-1023, default 1): 256
Here the start address should be larger than 2M (space reserved to program
the test binary). If one cylinder is 4k, then here 1G is reserved.
 Last cylinder or +size or +sizeM or +sizeK (256-1023, default 1023): 1023
Command (m for help): w
 The partition table has been altered!
Calling ioctl() to re-read partition table.
Syncing disks.
Now there's one partition on the SD card.
cat /proc/partitions
 major minor #blocks name
  8 0 78125000 sda
  8 1 104391 sda1
  8 2 78019672 sda2
 253 0 75956224 dm-0
```

- 253 1 2031616 dm-1
- 8 16 3872256 sdb
- 8 17 2904576 sdb1
- Using mkfs.vfat to format the partition:

#### sudo mkfs.vfat /dev/sdb1

- Copy two video clips to the SD card. Note that the filename should be less than 8 characters with ".264" extension. The video should be RAW h264 encoded files with no any container. The program will find the first two valid 264 files for playing.
- Burn the image to the SD card.
  - sudo dd if=output/mx6dq/evb\_rev\_a/bin/mx6dq\_evb\_rev\_a-vpu-sdk.bin of=/dev/sdb seek=2 skip=2 bs=512 && sync

# NOTE that seek=2 skip=2 is mandatory, without them the MBR of the file system would be overwritten!!

- b) Put the SD card into SLOT4.
- c) Set the boot switch to boot from SD4.
- d) Plug the HDMI cable of the first display to J5 of the CPU board.
- e) Optional second display: plug the HDMI expansion board (P/N: MCIMXHDMICARD) based on the SiI9024 chip onto the parallel display connector J19 of the CPU board, and plug the HDMI cable of the second display.
- f) Connect the serial cable for console output and 5V power supply, power on the board.

\*

Diagnostics Suite (1.0) for i.MX6DQ TO1.0 Sabre-Lite Board Rev A

Build: Mar 8 2012, 16:22:20 Freescale Semiconductor, Inc.

\*

====== Clock frequencies(HZ) =======

Cortex A9 core : 792,000,000 DDR memory : 528,000,000 UART2 for debug : 80,000,000

EPIT1 for system timer: 66,000,000

===== DDR configuration =======

data bits: 64, num banks: 8

row: 14, col: 10 DDR type is DDR3

Chip select CSD0 is used

Density per chip select: 1024MB

[INFO] Product Info: i.MX6Q

[INFO] VPU firmware version: 2.1.2

0 - VPU DECODER TEST 1 - VPU ENCODER TEST

x - to exit.

From here, the user is invited to chose what he wants to do.

#### 3. Limitations

- a) There is no resizing on the video output. For example if you are decoding some video clips with resolution other than 1080p, the video will show on the top-left of the screen with its original size.
- b) Video clips must be raw without container.
- c) Currently only H264 video decoding is supported. Other formats such as VC1, H263, MPEG3, MPEG4 will be added in a next release.