

CESAR A. GARCIA

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Electrical engineer with 8 years of electrical design experience in electronics and automotive and 2 years of electrical design verification experience in medical. Currently working in PCB design for enterprise servers.

SUMMARY OF QUALIFICATIONS

- Cadence tools including Design Entry HDL, Allegro PCB Editor, and System Architect
- ECO writing and release using ENOVIA
- Technical documentation writing such as protocol, report, and rework instruction
- Automated testing development
- Defect reporting tools such as ClearQuest
- Microsoft Office (Word, Excel, Power Point)
- C Programming, Java, and Python
- Business-level communication skills in fluent English and Spanish
- Cross-functional team collaboration

PROFESSIONAL EXPERIENCE

Hardware Development Engineer, IBM Systems – IBM, Austin, TX

May 2019 to Present

Responsible for the release of several of the PCBs that are part of the high-end and low-end P10 Systems, regional representative of the Design for Excellence team, and high-level PCB planner.

- Develop PCB layouts considering different elements such as size constraints, height clearances, card function, high-speed signals, among others.
- Perform schematic capture and reviews following set schedules for the release of PCBs part of the P10 Systems to support general availability dates.
- Create and manage ECOs using ENOVIA as part of the product development cycle.
- Assist verification engineers during bring up by keeping track of defects and developing detailed rework instructions for lab and manufacturing use.
- Lead reviews as part of the Design for Excellence team to minimize the probability of manufacturing and field issues.
- Train and mentor the newest team members for the Card Design team with weekly meetings and demos.

Design Verification Engineer, Electrical Engineering – Abbott, Plano, TX

January 2017 to May 2019

Responsible for the development and execution of design verification processes for neuromodulation products.

- Developed and managed the execution of protocols for design verification of electrical requirements of different medical devices including the External Pulse Generator and Internal Pulse Generation for DRG, SCS and DBS therapies.
- Investigated and documented verification of temporary BurstDR program in older generation devices.
- Worked with an outside vendor to assist with the release process of the design and verification documents for the next generation RF generator for the treatment of lower-back pain.
- Developed scripts to automate manual testing processes.

Design Engineer, Electrical Engineering – Peterbilt Motors, Denton, TX

March 2015 to January 2017

Responsible for the integration of the Engine Start Module and the maintenance of the battery cable design.

- System Owner of the remote diagnostics modem ensuring documentation was up to date and hardware and firmware levels were compatible to deliver real-time diagnostics to the customers and users.
- Led the Engine Start Module integration with SmartAir and Auto-Start as a Six Sigma project for an optimized no-idle solution including documenting test plans, risk analyses, and presenting results to upper management.

Hardware Development Engineer, POWER Systems – IBM, Austin, TX

June 2012 to March 2015

Responsible for the release of different PCBs found in POWER systems including OpenPOWER.

- Performed schematic capture and reviews for the release of PCBs for POWER systems.
- Created and managed ECOs using ENOVIA as part of the product development cycle.
- Documented rework procedures and wrote tool instructions making it easier to train new team members.
- Led specific design reviews with cross-functional teams to improve design reliability.
- Wrote scripts used for schematic editing to automate repetitive processes.

Hardware Development Engineer Intern – IBM, Austin, TX

May 2010 to December 2011

EDUCATION

B.S. in Electrical Engineering – St. Mary's University, San Antonio, TX

Aug 2007 to May 2012

- 3.7 GPA, Cum Laude; Dean's List for 3 semesters; Presidential Scholarship; Dr. Ozan Outstanding Student Award.