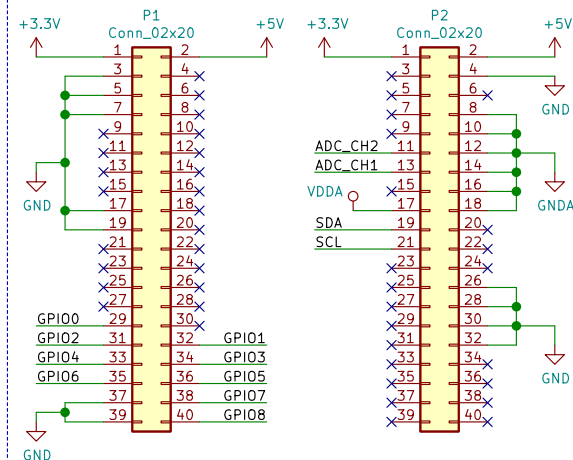
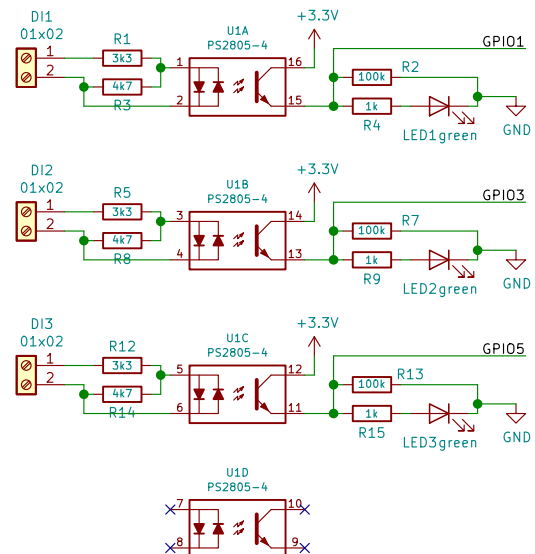


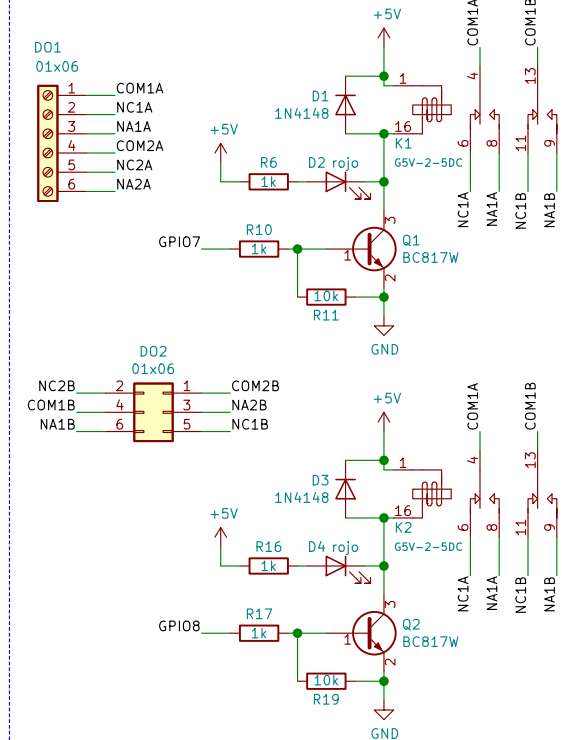
# Puertos de conexión a la EDU-CIAA



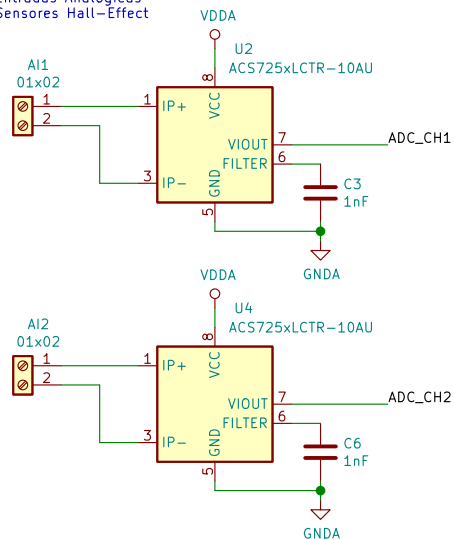
# Entradas Digitales



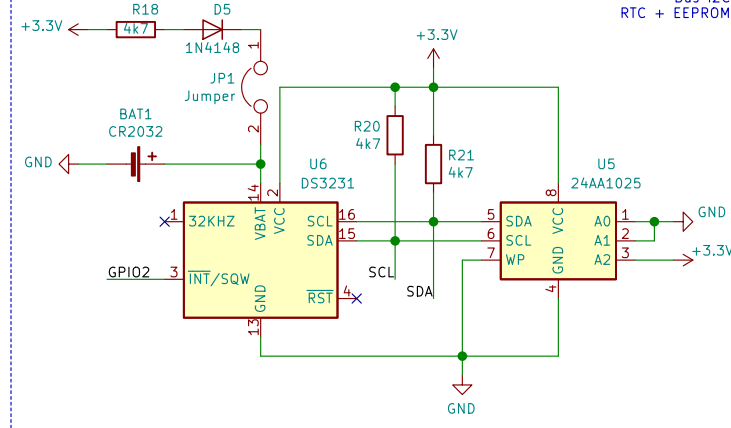
# Salidas Digitales



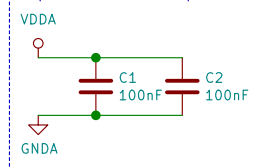
# Entradas Analógicas Sensores Hall-Effect



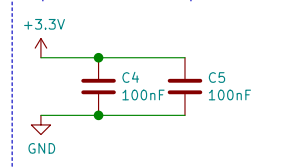
# Bus I2C RTC + EEPROM



# Capacitores de desacople ADC



# Capacitores de desacople Vcc 3.3V



Ignacio Majul  
Poncho Fotovoltaico

Sheet: /  
File: TP\_DCI.sch

Title: TP Diseño de circuitos impresos

Size: A4 Date: 2020-05-21

KiCad E.D.A. kicad 5.1.5-52549c586ubuntu18.04.1

Rev: 1.0

Id: 1/1