Module	Туре	CPSW	Enet	Comments
MDIO	Macro	CPSW_MDIO_PUBLIC_IOCTL		
MDIO	Macro	CPSW_MDIO_PHY_ADDR_MASK	ENET_MDIO_PHY_ADDR_MASK	
MDIO	Macro	CPSW_MDIO_IS_PHY_ADDR_SET		No longer needed as IS_POLL_ENABLED is specific to a given PHY address
MDIO	Macro	CPSW_MDIO_PHY_ADDR_MASK_NONE	ENET_MDIO_PHY_ADDR_MASK_NONE	
MDIO	Macro	CPSW_MDIO_PHY_ADDR_MASK_ALL	ENET_MDIO_PHY_ADDR_MASK_ALL	
MDIO	Macro	CPSW_MDIO_MAX_PHY_CNT	ENET_MDIO_PHY_CNT_MAX	
MDIO	IOCTL	CPSW_MDIO_IOCTL_GET_VERSION	ENET_MDIO_IOCTL_GET_VERSION	
MDIO	IOCTL	CPSW_MDIO_IOCTL_GET_ALIVE_STATUS	ENET_MDIO_IOCTL_IS_ALIVE	
MDIO	IOCTL	CPSW_MDIO_IOCTL_GET_LINK_STATUS	ENET_MDIO_IOCTL_IS_LINKED	
MDIO	IOCTL	CPSW_MDIO_IOCTL_CLAUSE22_READ_REG	ENET_MDIO_IOCTL_C22_READ	
MDIO	IOCTL	CPSW_MDIO_IOCTL_CLAUSE22_WRITE_REG	ENET_MDIO_IOCTL_C22_WRITE	
MDIO	IOCTL	CPSW_MDIO_IOCTL_CLAUSE45_READ_REG	ENET_MDIO_IOCTL_C45_READ	
MDIO	IOCTL	CPSW_MDIO_IOCTL_CLAUSE45_WRITE_REG	ENET_MDIO_IOCTL_C45_WRITE	
MDIO	IOCTL	CPSW_MDIO_IOCTL_CLAUSE22_READ_REG_ASYNC		Not implemented in CPSW LLD, to be added in Enet LLD later
MDIO	IOCTL	CPSW_MDIO_IOCTL_CLAUSE22_WRITE_REG_ASYNC		Not implemented in CPSW LLD, to be added in Enet LLD later
MDIO	IOCTL	CPSW_MDIO_IOCTL_CLAUSE45_READ_REG_ASYNC		Not implemented in CPSW LLD, to be added in Enet LLD later
MDIO	IOCTL	CPSW_MDIO_IOCTL_CLAUSE45_WRITE_REG_ASYNC		Not implemented in CPSW LLD, to be added in Enet LLD later
MDIO	IOCTL	CPSW_MDIO_IOCTL_GET_POLL_ENABLE_MASK	ENET_MDIO_IOCTL_IS_POLL_ENABLED	
MDIO	IOCTL	CPSW_MDIO_IOCTL_PRINT_REGS	ENET_MDIO_IOCTL_PRINT_REGS	
MDIO	Struct	CpswMdio_OperatingMode	CpswMdio_OpMode	Modes: normal, PHY monitoring, manual. Needed by config. CPSW_MDIO_MODE_STATE_CHANGE -> CPSW_MDIO_MODE_STATE_CHANGE_MON
MDIO	Struct	CpswMdio_UserChannel	EnetMdio_Group	Groups: group_0, group_max
MDIO	Struct	CpswMdio_FrameFormat	EnetMdio_FrameFmt	CpswMdio_FrameFormat -> EnetMdio_FrameFmt ENET_MDIO_FRAME_FORMAT_CLAUSE -> ENET_MDDIO_FRAME_FMT_C
MDIO	Struct	CpswMdio_Clause45Mmd	EnetMdio_C45Mmd	
MDIO	Struct	CpswMdio_PhyStatus		CPSW specific - moved to cpsw_mdio_priv.h
MDIO	Struct	CpswMdio_Clause22ReadRegInArgs	EnetMdio_C22ReadInArgs	userCh -> group
MDIO	Struct	CpswMdio_Clause45ReadRegInArgs	EnetMdio_C45ReadInArgs	userCh -> group
MDIO	Struct	CpswMdio_Clause22WriteRegInArgs	EnetMdio_C22WriteInArgs	userCh -> group
MDIO	Struct	CpswMdio_Clause45WriteRegInArgs	EnetMdio_C45WriteInArgs	userCh -> group
MDIO	Struct	CpswMdio_Version	EnetMod_Version	
MDIO	Struct	CpswMdio_Config		clause45EnableMask -> c45EnMask pollEnableMask -> pollEnMask userCh removed
MDIO	Feat		ENET_MDIO_FEAT_CLAUSE45	ENET_MDIO_FEAT_CLAUSE45
MDIO	Feat		ENET_MDIO_FEAT_PHY_MONITOR	
STATS	Macro	CPSW_STATS_PUBLIC_IOCTL	ENET_STATS_PUBLIC_IOCTL	
STATS	IOCTL		ENET_STATS_IOCTL_GET_VERSION	New IOCTL added in Enet LLD
STATS	IOCTL	CPSW_STATS_IOCTL_GET_HOSTPORT_STATS	ENET_STATS_IOCTL_GET_HOSTPORT_STATS	CpswStats_PortStats -> no specific definition
STATS	IOCTL	CPSW_STATS_IOCTL_GET_MACPORT_STATS	ENET_STATS_IOCTL_GET_MACPORT_STATS	CpswStats_GenericMacPortInArgs -> Enet_MacPort CpswStats_PortStats -> no specific definition
STATS	IOCTL	CPSW_STATS_IOCTL_RESET_HOSTPORT_STATS	ENET_STATS_IOCTL_RESET_HOSTPORT_STATS	
STATS		CPSW_STATS_IOCTL_RESET_MACPORT_STATS	ENET_STATS_IOCTL_RESET_MACPORT_STATS	CpswStats_GenericMacPortInArgs -> Enet_MacPort CpswStats_PortStats -> no specific definition
STATS	IOCTL	CPSW_STATS_IOCTL_RESTORE_HOSTPORT_STATS	ENET_STATS_IOCTL_RESTORE_HOSTPORT_STATS	Deprecated. Not used in CPSW LLD.
STATS	IOCTL	CPSW_STATS_IOCTL_RESTORE_MACPORT_STATS	ENET_STATS_IOCTL_RESTORE_MACPORT_STATS	Deprecated. Not used in CPSW LLD.
STATS	IOCTL	CPSW_STATS_IOCTL_PRINT_REGS	ENET_STATS_IOCTL_PRINT_REGS	
STATS	Struct	CpswStats_PortStats		No specific definition enforced. User can assume it's void.

STATS	Struct	CpswStats_HostPort_2g	CpswStats_HostPort_2g	
STATS				
STATS		CpswStats_MacPort_2g	CpswStats_MacPort_2g	
		CpswStats_HostPort_9g	CpswStats_HostPort_9g	
STATS		CpswStats_MacPort_9g	CpswStats_MacPort_9g	
		CpswStats_GenericMacPortInArgs		
STATS		CpswStats_RestoreMacPortStatsInArgs		
PORT		CPSW_PACKET_PRIORITY_NUM	ENET_PACKET_PRI_NUM	
PORT		CpswPort_HeaderPacketPriority	uint32_t	Struct is CPSW specific, will use uint32_t instead.
PORT		CpswPort_SwitchPriority	uint32_t	Struct is CPSW specific, will use uint32_t instead.
PORT	Struct	CpswPort_VlanConfig	EnetPort_VlanCfg	Used in config, not IOCTL.
PORT		CpswPort_TxPriorityType	EnetPort_EgressPriorityType	Used in config, not IOCTL.
PORT	Struct		EnetPort_MaxLen	Replaces uint32_t used for just MRU.
PORT	Struct	CpswPort_TrafficShapingRates	EnetPort_TrafficShapingRates	
PORT	Struct	CpswPort_TrafficShapingConfig	EnetPort_TrafficShapingCfg	
PORT	Struct	CpswPort_TxPriority	EnetPort_PriorityMap	
PORT	Struct	CpswPort_RxPriority	EnetPort_PriorityMap	
PORT	Struct	CpswPort_DscpPriority	EnetPort_DscpPriorityMap	
MACPORT	Macro	CPSW_MACPORT_PUBLIC_IOCTL	ENET_MACPORT_PUBLIC_IOCTL	
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_GET_VERSION	ENET_MACPORT_IOCTL_GET_VERSION	
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_GET_STATUS		Not implemented in CPSW LLD, to be removed.
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_GET_FIFO_STATS		
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_GET_LINK_CONFIG	ENET_MACPORT_IOCTL_GET_LINK_CFG	
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_IS_INBAND		Used for in-band, which is being deprecated.
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_SET_TX_PRIORITY	ENET_MACPORT_IOCTL_SET_EGRESS_QOS_PRI_MAP	CpswMacPort_SetTxPriorityInArgs -> EnetMacPort_SetEgressPriorityMapInArgs
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_GET_TX_PRIORITY	ENET_MACPORT_IOCTL_GET_EGRESS_QOS_PRI_MAP	CpswMacPort_GenericInArgs -> EnetMacPort_GenericInArgs CpswPort_TxPriority -> EnetPort_PriorityMap
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_SET_RX_PRIORITY	ENET_MACPORT_IOCTL_SET_PRI_REGEN_MAP	CpswMacPort_SetRxPriorityInArgs -> EnetMacPort_SetRetaggingMapInArgs
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_GET_RX_PRIORITY	ENET_MACPORT_IOCTL_GET_PRI_REGEN_MAP	EnetMacPort_GenericInArgs -> EnetMacPort_GenericInArgs
				EnetPort_TxPriority -> EnetPort_PriorityMap
MACPORT		CPSW_MACPORT_IOCTL_SET_RX_DSCP_PRIORITY	ENET_MACPORT_IOCTL_SET_INGRESS_DSCP_PRI_MAP	CpswMacPort_SetRxDscpPriorityInArgs -> EnetMacPort_SetIngressDscpPriorityMapInArgs
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_GET_RX_DSCP_PRIORITY	ENET_MACPORT_IOCTL_GET_INGRESS_DSCP_PRI_MAP	CpswMacPort_GenericInArgs -> EnetMacPort_GenericInArgs CpswPort_DscpPriority -> EnetPort_DscpPriorityMap
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_SET_TRAFFIC_SHAPING	ENET_MACPORT_IOCTL_ENABLE_EGRESS_TRAFFIC_SHAPING	CpswMacPort_SetTrafficShapingInArgs -> EnetMacPort_EnableEgressTrafficShapingInArgs
	.00.1	G. G. 1	ENET_MACPORT_IOCTL_DISABLE_EGRESS_TRAFFIC_SHAPING	Spenimen or s_section incompring in a light of section of section of section and section in a light of section
MACPORT	IOCTL	CPSW_MACPORT_IOCTL_GET_TRAFFIC_SHAPING	ENET_MACPORT_IOCTL_GET_EGRESS_TRAFFIC_SHAPING	CpswMacPort_GenericInArgs -> EnetMacPort_GenericInArgs
144.02.5.5	10.0=	CONTRACTOR OF THE PROPERTY OF		CpswPort_TrafficShapingConfig -> EnetPort_TrafficShapingConfig
MACPORT		CPSW_MACPORT_IOCTL_SET_TX_BLOCKS_REM		Will be private CPSW-specific IOCTL or config parameter
MACPORT		CPSW_MACPORT_IOCTL_GET_TX_BLOCKS_REM		Will be private CPSW-specific IOCTL or config parameter
MACPORT		CPSW_MACPORT_IOCTL_PRINT_REGS	ENET_MACPORT_IOCTL_PRINT_REGS	
MACPORT		CPSW_MACPORT_IOCTL_GET_RX_MTU	ENET_MACPORT_IOCTL_GET_MAXLEN	CpswMacPort_GenericInArgs -> EnetMacPort_GenericInArgs uint32_t -> EnetPort_MaxLen (Will return mtu[MAX_PRIORITY] and mru)
MACPORT		CPSW_MACPORT_IOCTL_ENABLE_CPTS_EVENT		
MACPORT		CPSW_MACPORT_IOCTL_DISABLE_CPTS_EVENT		
MACPORT		CpswMac_LayerType	EnetMac_LayerType	
MACPORT		CpswMac_SublayerType	EnetMac_SublayerType	
MACPORT	Struct	CpswMac_VariantType	EnetMac_VariantType	
MACPORT	Struct	CpswMacPort_InterVLANRouteId	CpswMacPort_InterVlanRouteId	
MACPORT	Enum	CpswMacPort_VlanType	EnetMacPort_VlanType	CPSW_MACPORT_VLAN_TYPE> ENET_MACPORT_VLAN_TYPE_
MACPORT	Struct	CpswMacPort_Interface	EnetMacPort_Interface	

MACPORT	Struct	CpswMac_SgmiiModeType	EnetMac_SgmiiMode	
MACPORT	Struct	CpswMacPort_SgmiiConfig		Removed. Will use EnetMac_SgmiiMode directly.
MACPORT	Struct	CpswMacPort_Config	CpswMacPort_Cfg	enableLoopback -> loopbackEn vlanConfig -> vlanCfg sgmiiConfig -> sgmiiMode
MACPORT	Struct	CpswMacPort_GenericInArgs	EnetMacPort_GenericInArgs	
MACPORT	Struct	CpswMacPort_PortStatus		Deprecated
MACPORT	Struct	CpswMacPort_FifoStats		
MACPORT	Struct	CpswMacPort_LinkConfig	EnetMacPort_LinkCfg	
MACPORT	Struct	CpswMacPort_SetTxPriorityInArgs	EnetMacPort_SetEgressPriorityMapInArgs	
MACPORT	Struct	CpswMacPort_SetRxPriorityInArgs	EnetMacPort_SetRetaggingMapInArgs	
MACPORT	Struct	CpswMacPort_SetRxDscpPriorityInArgs	EnetMacPort_SetIngressDscpPriorityMapInArgs	
MACPORT	Struct	CpswMacPort_SetTrafficShapingInArgs	EnetMacPort_EnableEgressTrafficShapingInArgs	trafficShapingConfig -> trafficShapingCfg portNum -> macPort
MACPORT	Struct		EnetMacPort_MaxLen	
MACPORT	Struct	CpswMacPort_SetTxBlocksRemInArgs		Will be private CPSW-specific IOCTL or config parameter
MACPORT	Struct	CpswMacPort_InterVlanRoutingParams	CpswMacPort_InterVlanRoutingCfg	CPSW specific
MACPORT	Struct	CpswMacPort_TxShortGapConfig	CpswMacPort_TxShortIpgCfg	enableTxShortGap -> txShortGapEn enableTxShortGapLimit -> txShortGapLimitEn
MACPORT	Struct	CpswMacPort_PortTxShortGapConfig	CpswMacPort_PortTxShortIpgCfg	portNum -> macPort shortGapCfg -> shortIpgCfg
MACPORT	Struct	CpswMacPort_IpTsConfig	CpswMacPort_IpTsCfg	enable* -> *En
MACPORT	Struct	CpswMacPort_TsEventConfig	CpswMacPort_TsEventCfg	enable* -> *En
MACPORT	Struct	CpswMacPort_EnableTsEventInArgs		
HOSTPORT	Macro	CPSW_HOSTPORT_PUBLIC_IOCTL	ENET_HOSTPORT_PUBLIC_IOCTL	
HOSTPORT	Macro	CPSW_HOSTPORT_RX_MTU_DEFAULT		CPSW specific macro.
HOSTPORT	IOCTL	CPSW_HOSTPORT_IOCTL_ENABLE	ENET_HOSTPORT_IOCTL_ENABLE	
HOSTPORT	IOCTL	CPSW_HOSTPORT_IOCTL_DISABLE	ENET_HOSTPORT_IOCTL_DISABLE	
HOSTPORT	IOCTL	CPSW_HOSTPORT_IOCTL_GET_FIFO_STATS		
HOSTPORT	IOCTL	CPSW_HOSTPORT_IOCTL_SET_TX_PRIORITY	ENET_HOSTPORT_IOCTL_SET_EGRESS_QOS_PRI_MAP	CpswPort_TxPriority -> EnetPort_PriorityMap
HOSTPORT	IOCTL	CPSW_HOSTPORT_IOCTL_GET_TX_PRIORITY	ENET_HOSTPORT_IOCTL_GET_EGRESS_QOS_PRI_MAP	CpswPort_TxPriority -> EnetPort_PriorityMap
HOSTPORT	IOCTL	CPSW_HOSTPORT_IOCTL_SET_RX_PRIORITY	ENET_HOSTPORT_IOCTL_SET_PRI_REGEN_MAP	CpswPort_RxPriority -> EnetPort_PriorityMap
HOSTPORT	IOCTL	CPSW_HOSTPORT_IOCTL_GET_RX_PRIORITY	ENET_HOSTPORT_IOCTL_GET_PRI_REGEN_MAP	CpswPort_RxPriority -> EnetPort_PriorityMap
HOSTPORT	IOCTL	CPSW_HOSTPORT_IOCTL_SET_RX_DSCP_PRIORITY	ENET_HOSTPORT_IOCTL_SET_INGRESS_DSCP_PRI_MAP	CpswPort_DscpPriority -> EnetPort_DscpPriorityMap
HOSTPORT		CPSW_HOSTPORT_IOCTL_GET_RX_DSCP_PRIORITY	ENET_HOSTPORT_IOCTL_GET_INGRESS_DSCP_PRI_MAP	CpswPort_DscpPriority -> EnetPort_DscpPriorityMap
HOSTPORT		CPSW_HOSTPORT_IOCTL_SET_TRAFFIC_SHAPING	ENET_HOSTPORT_IOCTL_ENABLE_INGRESS_TRAFFIC_SHAPING ENET_HOSTPORT_IOCTL_DISABLE_INGRESS_TRAFFIC_SHAPING	CpswPort_TrafficShapingConfig -> EnetPort_TrafficShapingConfig
HOSTPORT		CPSW_HOSTPORT_IOCTL_GET_TRAFFIC_SHAPING	ENET_HOSTPORT_IOCTL_GET_INGRESS_TRAFFIC_SHAPING	CpswPort_TrafficShapingConfig -> EnetPort_TrafficShapingConfig
HOSTPORT		CPSW_HOSTPORT_IOCTL_PRINT_REGS	ENET_HOSTPORT_IOCTL_PRINT_REGS	
HOSTPORT		CPSW_HOSTPORT_IOCTL_GET_RX_MTU	ENET_HOSTPORT_IOCTL_GET_MAXLEN	uint32_t -> EnetPort_MaxLen
HOSTPORT		CPSW_HOSTPORT_GET_FLOW_ID_OFFSET		
HOSTPORT		CPSW_HOSTPORT_IS_CSUM_OFFLOAD_ENABLE	ENET_HOSTPORT_IOCTL_IS_CSUM_OFFLOAD_ENABLED	
HOSTPORT		CpswHostPort_RxPriorityType	EnetHostPort_IngressPriorityType	CPSW_RX_PRIORITY_* -> ENET_INGRESS_PRIORITY_*
HOSTPORT		CpswHostPort_FifoStats		
HOSTPORT		CpswHostPort_Config		enable* -> *En
CPTS		CPSW_CPTS_PUBLIC_IOCTL	ENET_TIMESYNC_PUBLIC_IOCTL CPSW_CPTS_PUBLIC_IOCTL	
CPTS		CPSW_CPTS_HW_PUSH_NORMALIZE	CPSW_CPTS_HWPUSH_NORM	
CPTS	IOCTL	CPSW_CPTS_IOCTL_GET_VERSION	ENET_TIMESYNC_IOCTL_GET_VERSION	

CPTS	IOCTL CPSW_CPTS_IOCTL_REGISTER_STACK		Enet_registerEventCb()
CPTS			
		CDC/W CDTC IOCTI DECISTED HWDHSH CALIDACY	Enet_unregisterEventCb()
CPTS	IOCTL CPSW_CPTS_IOCTL_REGISTER_HW_PUSH_CALLBACK	CPSW_CPTS_IOCTL_REGISTER_HWPUSH_CALLBACK	Enet_registerEventCb()
CPTS	IOCTL CPSW_CPTS_IOCTL_UNREGISTER_HW_PUSH_CALLBACK	CPSW_CPTS_IOCTL_UNREGISTER_HWPUSH_CALLBACK	Enet_unregisterEventCb()
CPTS	IOCTL CPSW_CPTS_IOCTL_PUSH_TIMESTAMP_EVENT	ENET_TIMESYNC_IOCTL_GET_CURRENT_TIMESTAMP	
CPTS	IOCTL CPSW_CPTS_IOCTL_LOAD_TIMESTAMP	ENET_TIMESYNC_IOCTL_SET_TIMESTAMP	
CPTS	IOCTL CPSW_CPTS_IOCTL_SET_TS_NUDGE		
CPTS	IOCTL CPSW_CPTS_IOCTL_SET_TS_PPM	ENET_TIMESYNC_IOCTL_ADJUST_TIMESTAMP	CpswCpts_SetTsPpmInArgs -> EnetTimeSync_TimeStampAdj
CPTS	IOCTL CPSW_CPTS_IOCTL_SET_COMP		
CPTS	IOCTL CPSW_CPTS_IOCTL_SET_COMP_NUDGE		
CPTS	IOCTL CPSW_CPTS_IOCTL_SET_GENF		
CPTS	IOCTL CPSW_CPTS_IOCTL_SET_GENF_NUDGE		
CPTS	IOCTL CPSW_CPTS_IOCTL_SET_ESTF		
CPTS	IOCTL CPSW_CPTS_IOCTL_SET_ESTF_NUDGE		
CPTS	IOCTL CPSW_CPTS_IOCTL_SELECT_TS_OUTPUT_BIT		
CPTS	IOCTL CPSW_CPTS_IOCTL_LOOKUP_EVENT	ENET_TIMESYNC_IOCTL_GET_ETH_RX_TIMESTAMP	CpswCpts_LookUpEventInArgs -> TimeSync_GetEthTimestampInArgs
		ENET_TIMESYNC_IOCTL_GET_ETH_TX_TIMESTAMP	
CDTC	LOCAL COCKY COTC LOCAL CLEAD ALL EVENTS	CPSW_CPTS_IOCTL_LOOKUP_EVENT	
CPTS	IOCTL CPSW_CPTS_IOCTL_CLEAR_ALL_EVENTS	ENET_TIMESYNC_IOCTL_RESET	
CPTS	IOCTL CPSW_CPTS_IOCTL_PRINT_REGS	ENET_TIMESYNC_IOCTL_PRINT_REGS	
CPTS	IOCTL CPSW_CPTS_IOCTL_PRINT_STATS	ENET_TIMESYNC_IOCTL_PRINT_STATS	
CPTS	Struct CpswCpts_RftClkFreq		CPSW specific, member of CpswCpts_Config
CPTS	Struct CpswCpts_EventType		
CPTS	Struct CpswCpts_MsgType	EnetTimeSync_MsgType	
CPTS	Struct CpswCpts_OutputBitSel		
CPTS	Struct CpswCpts_PpmMode	EnetTimeSync_AdjMode	CPSW_CPTS_PPM_DISABLE -> ENET_TIMESYNC_ADJMODE_DISABLE
			CPSW_CPTS_PPM_PARTS_PER_MILLION -> ENET_TIMESYNC_ADJMODE_PPM
CPTS	Struct CpswCpts_PpmDir	EnetTimeSync_AdjDir	CPSW_CPTS_PPM_PARTS_PER_HOUR -> ENET_TIMESYNC_ADJMOD_PPH CPSW_CPTS_PPM_DIR_* -> ENET_TIMESYNC_ADJDIR_*
CPTS	Struct CpswCpts_HwPush		CPSW_CPTS_HW_PUSH_* -> CPSW_CPTS_HWPUSH_*
CPTS	Struct CpswCpts_Event		
CPTS	Macro CPSW_CPTS_HW_PUSH_COUNT_MAX	CPSW_CPTS_HWPUSH_COUNT_MAX	
CPTS	Struct CpswCpts LookUpEventInArgs	CpswCpts_Event	
CPTS		сръмсріз_Еуені	Moved to annu onto a
	· · · -		Moved to cpsw_cpts.c
CPTS	Struct CpswCpts_EventNotifyCb		pEventNotifyCbArg -> eventNotifyCbArg
CPTS	Struct CpswCpts_HwPushNotifyCb		pHwPushNotifyCbArg -> hwPushNotifyCbArg
CPTS	Struct CpswCpts_RegisterStackInArgs		pEventNotifyCb -> eventNotifyCb hEventNotifyCbArg -> eventNotifyCbArg
CPTS	Struct CpswCpts_RegisterHwPushCbInArgs		pHwPushNotifyCb -> hwPushNotifyCb
CPTS	Struct CpswCpts_SetTsPpmInArgs	EnetTimeSync_TimestampAdj	hHwPushNotifyCbArg -> hwPushNotifyCbArg
CPTS	Struct CpswCpts_SetCompValInArgs	, , ,	, , , , , , ,
CPTS	Struct CpswCpts SetFxnGenInArgs		
CPTS	Struct CptsSetFxnGenNudgeInArgs	CpswCpsw_SetFxnGenNudgeInArgs	
CPTS	Struct CpswCpts_Config	sport opon_sett Attachittangerin tigs	hostRxTsEnable -> hostRxTsEn
Cr 13	Struct Tepswepts_comig		tsRxEventsDisable -> tsRxEventsDis
			tsGenfClrEnable -> tsGenfClrEn
ALE	Macro CPSW_ALE_NUM_PORTS		
ALE	Macro CPSW_ALE_NUM_MAC_PORTS		
ALE	Macro CPSW_ALE_MAX_WHITELIST_IP_NXT_HDR		

ALE	Macro CPSW_ALE_ALL_PORT	S MASK		
ALE	Macro CPSW_ALE_ALL_MACF			
ALE	Macro CPSW_ALE_HOST_POF			
ALE	Macro CPSW_ALE_HOST_POF			
ALE	Macro CPSW_ALE_MCAST_IG			
ALE	Macro CPSW_ALE_MACADDR		ENET_MAC_ADDR_LEN	
ALE	Macro CPSW_ALE_OUIADDR_		ENET_OUI_ADDR_LEN	
ALE	Macro CPSW_ALE_IPV4ADDR		ENET_IPv4_ADDR_LEN	
ALE	Macro CPSW_ALE_IPV6ADDR		ENET_IPv6_ADDR_LEN	
ALE	Macro CPSW_ALE_PEAKBITRA	ATE_DISABLE		
ALE	Macro CPSW_ALE_COMMITB	ITRATE_DISABLE		
ALE	Macro CPSW_ALE_THREADID	_INVALID		
ALE	Macro CPSW_ALE_POLICER_N	MATCH_PORT		
ALE	Macro CPSW_ALE_POLICER_N	MATCH_PRIORITY		
ALE	Macro CPSW_ALE_POLICER_N	MATCH_OUI		
ALE	Macro CPSW_ALE_POLICER_N	MATCH_MACDST		
ALE	Macro CPSW_ALE_POLICER_N	MATCH_MACSRC		
ALE	Macro CPSW_ALE_POLICER_N	MATCH_OVLAN		
ALE	Macro CPSW_ALE_POLICER_N			
ALE	Macro CPSW_ALE_POLICER_N			
ALE	Macro CPSW_ALE_POLICER_N			
ALE	Macro CPSW_ALE_POLICER_N			
ALE		ABLEENTRY_DELETE_OUI		
ALE	Macro CPSW_ALE_POLICER_1			
ALE		ABLEENTRY_DELETE_MACDST		
ALE	Macro CPSW_ALE_POLICER_1			
ALE		TABLEENTRY_DELETE_OVLAN		
ALE ALE		ABLEENTRY_DELETE_ETHERTYPE		
ALE	Macro CPSW_ALE_POLICER_1 Macro CPSW_ALE_POLICER_1			
ALE	Macro CPSW_ALE_POLICER_1			
ALE	Macro CPSW_ALE_MACPORT			
ALE	Macro CPSW_ALE_MACPORT			
ALE	Macro CPSW_ALE_ALEPORT_			
ALE	Macro CPSW_ALE_MACPORT			
ALE	Macro CPSW_ALE_PUBLIC_IO			
ALE	IOCTL CPSW_ALE_IOCTL_ADI		CPSW_ALE_IOCTL_ADD_UCAST	CpswAle_AddEntryOutArgs -> uint32_t
ALE	IOCTL CPSW_ALE_IOCTL_ADI		CPSW_ALE_IOCTL_ADD_MCAST	
ALE	IOCTL CPSW_ALE_IOCTL_ADI			
ALE	IOCTL CPSW_ALE_IOCTL_ADI			
ALE	IOCTL CPSW_ALE_IOCTL_ADI			CpswAle_Ipv4AddrInfo -> CpswAle_IPv4AddrInfo
ALE	IOCTL CPSW_ALE_IOCTL_ADI			CpswAle_Ipv6AddrInfo -> CpswAle_IPv6AddrInfo
ALE	IOCTL CPSW_ALE_IOCTL_ADI	D_ETHERTYPE		CpswAle_EtherTypeEntryInfo -> uint16_t
ALE	IOCTL CPSW_ALE_IOCTL_LOG	DKUP_UNICAST	CPSW_ALE_IOCTL_LOOKUP_UCAST	
ALE	IOCTL CPSW_ALE_IOCTL_LOG	DKUP_MULTICAST	CPSW_ALE_IOCTL_LOOKUP_MCAST	
ALE	IOCTL CPSW_ALE_IOCTL_LOG	DKUP_VLAN		
ALE	IOCTL CPSW_ALE_IOCTL_REN	MOVE_ADDR		

ALE	IOCTL	CPSW_ALE_IOCTL_REMOVE_VLAN		
ALE		CPSW_ALE_IOCTL_REMOVE_OUI		
ALE		CPSW_ALE_IOCTL_REMOVE_IPV4ADDR		
ALE		CPSW_ALE_IOCTL_REMOVE_IPV6ADDR		
ALE		CPSW_ALE_IOCTL_REMOVE_ETHERTYPE		
ALE		CPSW_ALE_IOCTL_SET_POLICER		
ALE		CPSW_ALE_IOCTL_GET_POLICER		
ALE		CPSW_ALE_IOCTL_DEL_POLICER		
ALE		CPSW_ALE_IOCTL_DUMP_TABLE		
ALE		CPSW_ALE_IOCTL_DUMP_POLICER_ENTRIES		
ALE		CPSW_ALE_IOCTL_GET_POLICER_STATS		
ALE		CPSW_ALE_IOCTL_SET_PORT_MIRROR_CFG		
ALE		CPSW_ALE_IOCTL_SET_TRUNK_CFG		
ALE		CPSW_ALE_IOCTL_SET_OAMLPBK_CFG		
ALE		CPSW_ALE_IOCTL_DEL_ALL_ENTRIES	CPSW_ALE_IOCTL_REMOVE_ALL_ENTRIES	
ALE		CPSW_ALE_IOCTL_DEL_ALL_ENTRIES CPSW_ALE_IOCTL_SET_RX_FILTER	OF SW_ALL_IOCIL_ILLIVIOVL_ALL_LIVINILS	
ALE		CPSW_ALE_IOCTL_SET_PORT_STATE CPSW_ALE_IOCTL_SET_BCAST_MCAST_LIMIT		
		CPSW_ALE_IOCTL_SET_BCAST_MCAST_LIMIT CPSW_ALE_IOCTL_SET_DEFAULT_THREADCFG		
ALE			CDCW ALE LOCAL ACE ALL ENTINES	
ALE		CPSW_ALE_IOCTL_AGE_ALE_ENTRIES	CPSW_ALE_IOCTL_AGE_ALL_ENTRIES	Canada Catatan II ANCarti-OutAvan > Canada Balinas Fatas OutAvan
ALE		CPSW_ALE_IOCTL_SET_INTERVLAN_CFG		CpswAle_SetInterVLANConfigOutArgs -> CpswAle_PolicerEntryOutArgs
ALE		CPSW_ALE_IOCTL_GET_INTERVLAN_CFG	COCH ALE LOCAL DEALONE LEADNED ENTRIES	
ALE		CPSW_ALE_IOCTL_DEL_LEARNT_ENTRIES	CPSW_ALE_IOCTL_REMOVE_LEARNED_ENTRIES	outArgs: none -> uint32_t
ALE		CPSW_ALE_IOCTL_SET_POLICER_THREADCFG		
ALE		CPSW_ALE_IOCTL_GET_PORT_MACADDR		
ALE		CPSW_ALE_IOCTL_DISABLE_PORT_MIRROR		
ALE		CPSW_ALE_IOCTL_DISABLE_BCAST_MCAST_LIMIT		
ALE		CPSW_ALE_IOCTL_SET_POLICER_GLOBAL_CONFIG		
ALE		CPSW_ALE_IOCTL_GET_BCAST_MCAST_LIMIT		
ALE	IOCTL	CPSW_ALE_IOCTL_GET_PORT_STATE		CpswAle_GetPortStateInArgs -> uint32_t CpswAle_GetPortStateOutArgs -> CpswAle_PortState
ALE	IOCTL	CPSW_ALE_IOCTL_GET_DEFAULT_THREADCFG		Chamber and the state of the st
ALE		CPSW_ALE_IOCTL_PRINT_REGS		
ALE		CPSW_ALE_IOCTL_GET_POLICER_GLOBAL_CONFIG		
ALE		CPSW_ALE_IOCTL_DEL_ALL_POLICER_THREADID		
ALE		CPSW_ALE_IOCTL_BLOCK_CLASSIFIER_HOSTPORT		
ALE		CPSW_ALE_FWDSTLVL_E	CpswAle_FwdStateLevel	CPSW_ALE_FWDSTLVL_FORWARDING -> CPSW_ALE_FWDSTLV_FWD
				CPSW_ALE_FWDSTLVL_BLK_FWD_LRN -> CPSW_ALE_FWDSTLV_BLK_FWD_LRN
				CPSW_ALE_FWDSTLVL_FWD_LRN -> CPSW_ALE_FWDSTLVL_FWD_LRN
ALE		CPSW_ALE_POLICER_YELLOWTHRESH_E	CpswAle_PolicerYellowThresh	
ALE		CPSW_ALE_POLICER_NOMATCH_MODE_E	CpswAle_PolicerNoMatchMode	
ALE	Enum	CPSW_ALE_PORTSTATE_E	CpswAle_PortState	
ALE	Enum	CPSW_ALE_IPADDR_CLASSIFIER_E	CpswAle_lpAddrClassifierType	
ALE	Enum	CPSW_ALE_TBLENTRYTYPE_E	CpswAle_TableEntryType	
ALE	Macro	CPSW_ALE_CONFIG_MASK_ALE_MODULE_ENABLE	CPSW_ALE_CFG_MODULE_EN	
ALE		CPSW_ALE_CONFIG_MASK_ALE_BYPASS_ENABLE	CPSW_ALE_CFG_BYPASS_EN	
ALE	Macro	CPSW_ALE_CONFIG_MASK_UNKNOWN_UNICAST_FLOOD2HOST	CPSW_ALE_CFG_UNKNOWN_UCAST_FLOOD2HOST	

ALE	Enum	CPSW_ALE_RXFILTER_E	CpswAle_RxFilter	
ALE	Struct	CpswAle_MacAddrInfo		
ALE		CpswAle_Ipv4EntryInfo	CpswAle_IPv4EntryInfo	
ALE	Struct	CpswAle_Ipv6EntryInfo	CpswAle_IPv6EntryInfo	
ALE	Struct	CpswAle_McastEntryInfo	superFlag -> super	
ALE		CpswAle_UcastEntryInfo		
ALE		CpswAle_IpAddrClassifierInfo		ipAddrtype -> ipAddrType
				ipv4 ->ipv4Info
				ipv6 -> ipv6Info
ALE		CpswAle_OuiEntryInfo		
ALE	Struct	CpswAle_EtherTypeEntryInfo	uint16_t	
ALE	Struct	CpswAle_VlanIdInfo		
ALE	Struct	CpswAle_VlanEntryInfo		disallowIPFragmentation -> disallowIPFrag
ALE	Struct	CpswAle_MacSrcAddrClassifierInfo	CpswAle_MacAddrClassifierInfo	
ALE		CpswAle_MacDstAddrClassifierInfo	CpswAle_MacAddrClassifierInfo	
ALE	Struct	CpswAle_PolicerMatchParams		policerMatchEnableMask -> policerMatchEnMask
				oui -> ouilnfo
				srcMacAddr -> srcMacAddrInfo dstMacAddr -> dstMacAddrInfo
				dstlp -> dstlpInfo
				egressOpEnabled -> egressOpEn
ALE	Struct	CpswAle_HostRxFilterParams		
ALE	Struct	CpswAle_MirrorMatchParams		macAddrInfo -> dstMacAddrInfo
ALE	Struct	CpswAle_VlanEntryInfo		etherTypeInfo.etherType -> etherType
ALE	Struct	CpswAle_PortBcastMcastRateLimitParams		enableBRateLimitForPort -> bcastRateLimitForPortEn
ALL	Struct	CPSWAIE_r OI tBCastivicastivateEllillitr arailis		enableMRateLimitForPort -> mcastRateLimitForPortEn
ALE	Struct	CpswAle_UnregulatedTrafficPolicer		
ALE	Struct	CpswAle_AddEntryOutArgs	uint32_t	
ALE	Struct	CpswAle_SetUcastEntryInArgs		
ALE	Struct	CpswAle_GetUcastEntryOutArgs	unicastInfo -> info	
ALE	Struct	CpswAle_SetMcastEntryInArgs		
ALE	Struct	CpswAle_GetMcastEntryInArgs		
ALE	Struct	CpswAle_GetMcastEntryOutArgs	outInfo -> info	
ALE	Struct	CpswAle_GetVlanEntryOutArgs		disallowIPFragmentation -> disallowIPFrag
ALE	Struct	CpswAle_SetPolicerEntryInArgs		threadidEnable -> threadidEn
ALE	Struct	CpswAle_SetPolicerEntryOutArgs		*EntryIndex -> *EntryIdx
ALE	Struct	CpswAle_GetPolicerEntryOutArgs	CpswAle_PolicerEntryOutArgs	policerMatchEnableMask -> policerMatchEnMask
				destPortMask -> dstPortMask
				threadidEnable -> threadidEn
ALE	Struct	CpswAle_DelPolicerEntryInArgs		egressOpEnabled -> egressOpEn delAleEntryMask -> aleEntryMask
ALE		CpswAle_SetOAMLoopbackInArgs	uint32_t	des rectiti y rook > dietiri y rook
ALE		CpswAle_SetPortStateInArgs		
ALE	Struct	CpswAle_GetPortStateInArgs	uint32_t	
ALE	Struct	CpswAle_GetPortStateOutArgs	CpswAle_PortState	
ALE		CpswAle_SetPortMirroringConfigInArgs	CpswAle_PortMirroringCfg	destPortNum -> dstPortNum
ALE	Struct	Chamme_actrolitiviii.foriiigcoliiigiiiAiga	CP3WAIC_FOILIVIIITOTINGCIG	enableSrc -> srcEn
				enableDst -> dstEn
				enableMatch -> matchEn

ALE	Struct	CpswAle SetTrunkConfigInArgs	CpswAle_TrunkCfg	enableDstIP -> dstIPEn
ALL	Struct	cpswale_settrunkcomigmalgs	cpswAie_ITUIIKCIg	enableSrcIP -> srcIpEn
				enableInnerVLAN -> innerVlanEn
ALE	Struct	CpswAle_SetDefaultThreadConfigInArgs	CpswAle_DfltThreadCfg	enablePriorityOR -> priorityOrEn
				disableMacPortDefaultThread -> macPortDfltThreadDis
				enablePri -> priEn
ALE	Struct	CpswAle_GetDefaultThreadConfigOutArgs	CpswAle_DfltThreadCfg	enablePriorityOR -> priorityOrEn
ALE	Struct	CpswAle_SetInterVLANConfigInArgs	CpswAle_SetInterVlanCfgInArgs	routeIndex -> routeIdx
				egressTrunkIndex -> egressTrunkIdx
				destPortMask -> dstPortMask
۸۱۲	Ctruct	CocyAle Cetleton// ANConfigOutAge	Chautha PalicarEntryOuthyga	enableTTLCheck -> ttlCheckEn
ALE	Struct	CpswAle_SetInterVLANConfigOutArgs	CpswAle_PolicerEntryOutArgs	policerMatchEnableMask -> policerMatchEnMask
ALE	Struct	CpswAle_BlockClassifierHostPortOutArgs	CpswAle_PolicerEntryOutArgs	*EntryIndex -> *EntryIdx
ALE	Struct	CpswAle_GetPolicerStatsInArgs		
ALE	Struct	CpswAle_GetPolicerStatsOutArgs		
ALE	Struct	CpswAle_SetBcastMcastRateLimitInArgs		
ALE	Struct	CpswAle_GetBcastMcastRateLimitOutArgs		rateLimitEnabled -> rateLimitEn
ALE	Struct	CpswAle_GetPortMacAddrInArgs		macAddr -> addrs
				maxNumAddr -> addrMax
ALE	Struct	CpswAle_GetPortMacAddrOutArgs		macAddr ->addrs
				addrCount -> addrCnt
ALE	Struct	CpswAle_DelLearntEntriesInArgs	uint32 t	totalPortMacAddrCount -> totalAddrCnt
	Struct		_	the condition of the condition
ALE	Struct	CpswAle_SetPolicerThreadConfigInArgs	CpswAle_SetPolicerThreadCfgInArgs	threadIdEnable -> threadIdEn
ALE	Struct	CpswAle_SetPolicerThreadConfigOutArgs	CpswAle_PolicerEntryOutArgs	
ALE	Struct	CpswAle_SetPolicerGlobalConfigInArgs	CpswAle_PolicerGlobalCfg	policingEnable -> policingEn
				yellowDropEnable -> yellowDropEn
ALE	Struct	CpswAle_GetPolicerGlobalConfigOutArgs	CpswAle_PolicerGlobalCfg	redDropEnable -> redDropEn
ALE	Struct	CpswAle_PolicerGlobalConfig	CpswAle_PolicerGlobalCfg	Used in CpswAle_Config and CPSW_ALE_IOCTL_SET_POLICER_GLOBAL_CONFIG
ALE	Struct	CpswAle_AgingConfig	CpswAle_AgingCfg	enableAutoAging -> autoAgingEn
ALE	Struct	CpswAle_InitVlanConfig	CpswAle_InitVlanCfg	autoLearnWithVLAN -> autoLearnWithVlan
ALE	Struct	CpswAle_PortMacModeConfig	CpswAle_PortMacModeCfg	macOnlyCafEnable -> macOnlyCafEn
ALE	Struct	CpswAle_MacAuthConfig	CpswAle_MacAuthCfg	macOnlyEnable -> macOnlyEn enableAuthMode -> authModeEn
ALL	Struct	cpswale_iviacautifcomig	cpswaie_iviacautifeig	macAuthDisableMask -> macAuthDisMask
ALE	Struct	CpswAle_PortVlanSecurityConfig	CpswAle PortVlanSecurityCfg	
ALE	Struct	CpswAle PortLearningSecurityConfig	CpswAle_PortLearningSecurityCfg	noSaUpdateEnable -> noSaUpdateEn
ALE	Struct	CpswAle_IPPktSecurityConfig	CpswAle_IPPktSecurityCfg	enableDefaultNoFragment -> dfltNoFragEn
,				enableDefaultNxtHdrWhitelist -> dfltNxtHdrWhitelistEn
				ipNxtHdrWhitelistCount -> ipNxtHdrWhitelistCnt
ALE	Struct	CpswAle_MalformedPktSecurityConfig	CpswAle_MalformedPktSecurityCfg	disableSrcMcastDrop -> srcMcastDropDis
				enableBadLenPktDrop -> badLenPktDropEn
ALE	Struct	CpswAle_LearningSecurityConfig		Orphan, will remove
ALE	Struct	CpswAle_NetworkSecurityConfig	CpswAle_NetworkSecurityCfg	enableVid0Mode -> vid0ModeEn
ALE	Struct	CpswAle_PortConfig	CpswAle_PortCfg	
ALE	Struct	CpswAle_Config	CpswAle_Cfg	
ALE	Struct	CpswAle_RestartConfig		
CPSW	IOCTL	CPSW_IOCTL_GET_VERSION	ENET_PER_IOCTL_GET_VERSION	Cpsw_GetVersionOutArgs -> Enet_Version
CPSW	IOCTL	CPSW IOCTL OPEN PORT LINK	ENET_PER_IOCTL_OPEN_PORT_LINK	
CPSW	IOCTL	CPSW_IOCTL_CLOSE_PORT_LINK	ENET_PER_IOCTL_CLOSE_PORT_LINK	Cpsw_GenericPortLinkInArgs -> EnetMacPort_GenericInArgs
CPSW	IOCTL	CPSW_IOCTL_IS_PORT_LINK_UP	ENET_PER_IOCTL_IS_PORT_LINK_UP	Cpsw_GenericPortLinkInArgs -> EnetMacPort_GenericInArgs
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OTC	CPSW	IOCTL	CPSW_IOCTL_GET_PORT_LINK_CONFIG	ENET_PER_IOCTL_GET_PORT_LINK_CONFIG	Cosw. Conoris Portlinkly Args. > Engt Mac Port. Conoris In Args.
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CFSN COT. CFSN COT. CFSN COT. CFSN				CDCW DED LOCAL SET INTERVIANI DOLLTE LINI ECDESS	
DPW DCT DPW DCT SPW					Cosus Classiatori/lanDoutallaiFgroscOutAvgs > CosusMacDout lateri/lanDoutald
					Cpsw_ClearIntervianRouteOnlegressOutArgs -> CpswiviacPort_IntervianRouteId
					Constitute Alles Books Malk Tours Outhers a Constitute Alles Books Alles Books Alles
					Cpsw_Getsnort1xiPGConfigOutArgs -> Cpsw_1xsnort1pgCfg
CPSW IOCTL GPSW_IOCTL_REGISTER_RE_DEFAULT_FLOW					
				ENET_PER_IOCIT_DETACH_CORE	
CFSW IOCTL CPSW_IOCTL_UNREGISTER_DSTMAC_INC_HOW					
CPSW Struct CPSW_OPENTURINANGESTER_CRYSTOW CPSW_OPENTURINANGES CPSW_OPENTURINA					
CFSW Struct Gpow_General Continuency Enert Version				_	
Struct Cpsw_GenericPortLinkInArgs EnetPer_PortLinkCrig				Fnot Vorsion	
CPSW Struct Cpsw_GenericPortInishArgs EnetWarPort_GenericArgs					
CPSW Struct Cpsw_InterVlanRouteIngressPktMatchCig					
CPSW Struct Cpsw_InterVlanBouteIngressPktMatchCfg				EnetWacPort_GenericinArgs	
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Struct Cpsw_SetinterVlanRouteUniEgressinArgs CpswAacPort_InterVlanRouteUniEgressinArgs CpswAacPort_InterVlanRouteUniEgressinArgs CpswAacPort_InterVlanRouteUniEgressinArgs CpswAacPort_InterVlanRouteUniEgressinArgs CpswAacPort_InterVlanRouteUniEgressinArgs CpswAacPort_InterVlanRouteUniEgressinArgs CpswAacPort_InterVlanRouteUniEgressinArgs Cpsw_ClearInterVlanRouteUniEgressinArgs Cpsw_ClearInterVlanRouteUniEgressinArgs Cpsw_ClearInterVlanRouteUniEgressinArgs Cpsw_ClearInterVlanRouteUniEgressinArgs CpswAacPort_InterVlanRouteUniEgressinArgs Cpsw_AacPort_InterVlanRouteUniEgressinArgs Cpsw_AacPort					
CPSW Struct Cpsw_SetInterVlanEgressPortCig CPSW Struct Cpsw_SetInterVlanEquet UnliggressInArgs CPSW Struct Cpsw_SetInterVlanEquet UnliggressInArgs CPSW Struct Cpsw_ClearInterVlanEquet UnliggressInArgs CPSW Struct Cpsw_ClearInterVlanEquet UnliggressInArgs CPSW Struct Cpsw_ClearInterVlanEquet UnliggressInArgs CPSW Struct Cpsw_ClearInterVlanEquet UnliggressOutArgs CPSW Struct Cpsw_SetInterVlanEquet UnliggressOutArgs CPSW Struct Cpsw_ClearInterVlanEquet UnliggressOutArgs CPSW Struct Cpsw_DefaultFlowInfo CPSW Struct Cpsw_DefaultFlowInfo CPSW Struct Cpsw_MacDetFlowInfo CPSW Struct Cpsw_SetShortTxIPGConfignArgs Cpsw_TsShortIngCfg configureGapThreshold Cpsw_TsShortIngCfg configureGapThreshold CPSW Struct Cpsw_SetShortTxIPGConfignArgs Cpsw_TsShortIngCfg configureGapThreshold CPSW Struct Cpsw_SetShortTxIPGConfigOutArgs Cpsw_TsShortIngCfg configureGapThreshold Cpsw_					
CPSW Struct CPSW_Setther/VlanRouteUniEgressOutArgs CPSW_Struct CPSW_MGloin/KstateChangeInfo Struct CPSW_MGloin/KstateChangeInfo Struct CPSW_MGloin/KstateChangeCb Struct CPSW_MGloin/KstateChangeCb Struct CPSW_MGloin/KstateChangeCb Struct CPSW_MGloin/KstateChangeCb Struct CPSW_Struct	CDCM	Ctruct	Cocy InterManEgrees PortCfg		srcMacAddr -> srcMacAddrInfo
CPSW Struct Cpsw_GetinterVlanRouteUniEgressOutArgs					
CPSW Struct Cpsw_ClearInterVlanRouteUniEgressInArgs CPSW Struct Cpsw_ClearInterVlanRouteUniEgressOutArgs CPSW Struct Cpsw_SetInterVlanRouteUniEgressOutArgs CPSW Struct Cpsw_SetInterVlanRouteMultiEgressOutArgs CPSW Struct Cpsw_SetInterVlanRouteMultiEgressOutArgs CPSW Struct Cpsw_SetInterVlanRouteMultiEgressOutArgs CPSW Struct Cpsw_SetInterVlanRouteMultiEgressOutArgs CPSW Struct Cpsw_ClearInterVlanRouteMultiEgressOutArgs CPSW Struct Cpsw_ClearInterVlanRouteMultiEgressOutArgs CPSW Struct Cpsw_ClearInterVlanRouteMultiEgressOutArgs CPSW Struct Cpsw_ClearInterVlanRouteMultiEgressOutArgs Cpsw_AttachCoreOutArgs EnetPer_AttachCoreOutArgs CPSW Struct Cpsw_AttachCoreOutArgs EnetPer_AttachCoreOutArgs CPSW Struct Cpsw_MacDstFlowInfo CPSW Struct Cpsw_MacDstFlowInfoCfgInfargs Cpsw_SetTxShortIngCfgInArgs Cpsw_SetTxShortIngCfgInArgs Cpsw_TxShortIngCfgInArgs Cpsw_TxShortIngCfg CPSW_Struct Cpsw_GetShortTxIPGConfigOutArgs Cpsw_VlanCfg CPSW_VlanCfg CPSW_VlanCfg CPSW_VlanCfg CPSW_VlanCfg CPSW_Struct CpswOsal_Prms EnetOsal_Cfg					CoswMacPort InterVLANPouteId > CoswMacPort InterVlanPouteId
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CPSW Struct Cpsw_SetInterVlanRouteMultiEgressInArgs CpswAacPort_InterVLANRouteId -> Cpsw_DefaultFlowInterPoliceId -> Cpsw_DefaultFlowInt	CPSW	Struct	Cpsw_ClearInterVlanRouteUniEgressInArgs		
CPSW Struct Cpsw_SetInterVlanRouteMultiEgressOutArgs	CPSW	Struct	Cpsw_ClearInterVlanRouteUniEgressOutArgs		
CPSW Struct Cpsw_ClearInterVlanRouteMultiEgressInArgs CpswMacPort_InterVlanRouteId CPSW Struct Cpsw_ClearInterVlanRouteMultiEgressOutArgs CpswMacPort_InterVlanRouteId CPSW Struct Cpsw_AttachCoreOutArgs EnetPer_AttachCoreOutArgs EnetPer_AttachCoreOutArgs CPSW Struct Cpsw_MdioLinkStateChangeInfo CPSW Struct Cpsw_DefaultFlowInfo Cpsw_DefaultFlowInfo CPSW Struct Cpsw_MacDstFlowInfo CPSW Struct Cpsw_MacDstFlowInfo CPSW Struct Cpsw_MdioLinkStateChangeCb CPSW Struct Cpsw_MdioLinkStateChangeCb CPSW Struct Cpsw_PortLinkStatusChangeCb CPSW Struct Cpsw_SetShortTxIPGConfigInArgs Cpsw_SetTxShortIpgCfgInArgs ConfigureGapThreshold-> configureGapThreshold (Sick Cpsw_SetShortTxIPGConfigInArgs Cpsw_TxShortIpgCfg PortShortIpgCfg PortShortIpgCf	CPSW	Struct	Cpsw_SetInterVlanRouteMultiEgressInArgs		
CPSW Struct Cpsw_ClearInterVlanRouteMultiEgressInArgs CpswMacPort_InterVlanRouteId CPSW Struct Cpsw_ClearInterVlanRouteMultiEgressOutArgs CpswMacPort_InterVlanRouteId CPSW Struct Cpsw_AttachCoreOutArgs EnetPer_AttachCoreOutArgs EnetPer_AttachCoreOutArgs CPSW Struct Cpsw_MdioLinkStateChangeInfo CPSW Struct Cpsw_DefaultFlowInfo Cpsw_DefaultFlowInfo Cpsw_DefaultFlowInfo Cpsw_DefaultFlowInfo CPSW Struct Cpsw_MacDstFlowInfo CPSW Struct Cpsw_MdioLinkStateChangeCb CPSW Struct Cpsw_PortLinkStateChangeCb CPSW Struct Cpsw_PortLinkStatusChangeCb Cpsw_Struct Cpsw_PortLinkStatusChangeCb Cpsw_SetTxShortIpgCfginArgs Cpsw_SetTxShortIpgCfginArgs Cpsw_SetTxShortIpgCfginArgs Cpsw_SetShortTxIPGConfigInArgs Cpsw_SetTxShortIpgCfginArgs Cpsw_TrshortIpgCfg	CPSW	Struct	Cpsw_SetInterVlanRouteMultiEgressOutArgs		CpswMacPort_InterVLANRouteId -> CpswMacPort_InterVlanRouteId
CPSW Struct Cpsw_ClearInterVlanRouteMultiEgressOutArgs CpswMacPort_InterVlanRouteId CPSW Struct Cpsw_AttachCoreOutArgs EnetPer_AttachCoreOutArgs CPSW Struct Cpsw_MdioLinkStateChangeInfo CPSW Struct Cpsw_DefaultFlowInfo Cpsw_DfltFlowInfo CPSW Struct Cpsw_MacDstFlowInfo CPSW Struct Cpsw_MacDstFlowInfo CPSW Struct Cpsw_MacDstFlowInfo CPSW Struct Cpsw_MdioLinkStateChangeCb CPSW Struct Cpsw_PortLinkStatusChangeCb CPSW Struct Cpsw_SetShortTxIPGConfigInArgs Cpsw_SetShortTxIPGConfigInArgs Cpsw_SetTxShortIpgCfgInArgs Cpsw_TxShortIpgCfg CPSW Struct Cpsw_GetShortTxIPGConfigOutArgs Cpsw_TxShortIpgCfg CPsw_TxShortIpgCfg CPsw_VianCfg Cpsw_VianCfg Cpsw_VianCfg Cpsw_VianCfg Cpsw_VianCfg Cpsw_SetTyshortIpgCfg Cpsw_VianCfg Cpsw_VianCfg Cpsw_SetCosi_Cfg Cpsw_SetCosi_Cfg Cpsw_SetCosi_Cfg Cpsw_SetCosi_Cfg Cpsw_VianCfg Cpsw_VianCfg Cpsw_SetCosi_Cfg Cpsw_S					CpswAle_SetInterVLANConfigOutArgs -> CpswAle_PolicerEntryOutArgs
CPSW Struct Cpsw_AttachCoreOutArgs					
CPSW Struct Cpsw_DefaultFlowInfo Cpsw_DfitFlowInfo Cpsw_DfitFlowInfo Cpsw_DfitFlowInfo Cpsw_DfitFlowInfo Cpsw_DfitFlowInfo Cpsw_Struct Cpsw_MacDstFlowInfo Cpsw_MacDst			· -		
CPSW Struct Cpsw_DefaultFlowInfo				EnetPer_AttachCoreOutArgs	
CPSW Struct Cpsw_MacDstFlowInfo CPSW Callback Cpsw_MdioLinkStateChangeCb CPSW Struct Cpsw_PortLinkStatusChangeCb CPSW Struct Cpsw_SetShortTxlPGConfigInArgs Cpsw_SetShortTxlPGConfigInArgs Cpsw_SetTxShortIpgCfgInArgs Cpsw_SetTxShortIpgCfgInArgs Cpsw_SetTxShortIpgCfgInArgs Cpsw_SetTxShortIpgCfgInArgs Cpsw_TrsShortIpgCfg CPSW Struct Cpsw_GetShortTxlPGConfigOutArgs Cpsw_TxShortIpgCfg Cpsw_TxShortIpgCfg Cpsw_VlanConfig			, = 0		
CPSW Callback Cpsw_MdioLinkStateChangeCb CPSW Struct Cpsw_PortLinkStatusChangeCb CPSW Struct Cpsw_SetShortTxlPGConfigInArgs Cpsw_SetTxShortIpgCfgInArgs Cpsw_SetTxShortIpgCfgInArgs Cpsw_SetTxShortIpgCfgInArgs configureGapThreshold -> configureGapThresh ipgTriggerThresholdBlkCount -> ipgTriggerThreshBlkCnt portlPGCfg -> portShortIpgCfg CPSW Struct Cpsw_GetShortTxlPGConfigOutArgs Cpsw_TxShortIpgCfg Cpsw_VlanConfig Cpsw_VlanConfig Cpsw_VlanConfig Cpsw_VlanConfig EnetOsal_Cfg CPSW Struct CpswOsal_Prms EnetOsal_Cfg				Cpsw_DfltFlowInfo	
CPSW Struct Cpsw_PortLinkStatusChangeCb CPSW Struct Cpsw_SetShortTxIPGConfigInArgs Cpsw_SetShortTxIPGConfigInArgs Cpsw_SetShortTxIPGConfigInArgs Cpsw_SetTxShortIpgCfgInArgs configureGapThreshold -> configureGapThresh ipgTriggerThreshBlkCnt -> ipgTriggerThreshBlkCnt portIPGCfg -> portShortIpgCfg CPSW Struct Cpsw_GetShortTxIPGConfigOutArgs Cpsw_TxShortIpgCfg Cpsw_VlanConfig Cpsw_VlanConfig Cpsw_VlanConfig EnetOsal_Cfg CPSW Struct CpswOsal_Prms EnetOsal_Cfg					
CPSW Struct Cpsw_SetShortTxlPGConfigInArgs Cpsw_SetTxShortlpgCfgInArgs configureGapThreshold -> configureGapThreshold -> configureGapThresholdBlkCount -> ipgTriggerThreshBlkCnt ipgTriggerThreshBlkCnt portlPGCfg -> portShortlpgCfg CPSW Struct Cpsw_GetShortTxlPGConfigOutArgs Cpsw_TxShortlpgCfg portlPGCfg -> portShortlpgCfg CPSW Struct Cpsw_VlanConfig Cpsw_VlanConfig CPSW Struct Cpsw_SetShortTxlPGConfigOutArgs Cpsw_VlanCfg CPSW Struct Cpsw_VlanConfig Cpsw_VlanConfig CPSW_Struct CpswOsal_Prms EnetOsal_Cfg					
ipgTriggerThresholdBlkCount -> ipgTriggerThreshBlkCnt portIPGCfg -> portShortIpgCfg CPSW Struct Cpsw_GetShortTxIPGConfigOutArgs Cpsw_TxShortIpgCfg portIPGCfg -> portShortIpgCfg CPSW Struct Cpsw_VlanConfig Cpsw_VlanCfg CPSW Struct CpswOsal_Prms EnetOsal_Cfg					
CPSW Struct Cpsw_GetShortTxlPGConfigOutArgs Cpsw_TxShortlpgCfg portlPGCfg -> portShortlpgCfg CPSW Struct Cpsw_VlanConfig Cpsw_VlanCfg CPSW Struct CpswOsal_Prms EnetOsal_Cfg	CPSW	Struct	Cpsw_SetShortTxIPGConfigInArgs	Cpsw_SetTxShortIpgCfgInArgs	e i
CPSW Struct Cpsw_VlanConfig Cpsw_VlanCfg CPSW Struct CpswOsal_Prms EnetOsal_Cfg					portIPGCfg -> portShortIpgCfg
CPSW Struct CpswOsal_Prms EnetOsal_Cfg	CPSW			Cpsw_TxShortlpgCfg	portIPGCfg -> portShortIpgCfg
	CPSW				
CPSW Struct CpswOsal_CachePrms				EnetOsal_Cfg	
	CPSW	Struct	CpswOsal_CachePrms		

CPSW	Struct	Cpsw_Config	Cpsw_Cfg	
Types		CPSW_SET_BIT	ENET_BIT	
Types		CPSW_GET_BIT	ENET_GET_BIT	
Types		CPSW_IS_BIT_SET	ENET_IS_BIT_SET	
Types		CPSW_FEXT		
Types		CPSW_FINS		
Types	Macro	C13W_11N3	ENET_VERSION_NONE	
Types		CPSW_MAC_ADDR_LEN	ENET_MAC_ADDR_LEN	
Types		CPSW_PRIORITY_NUM	ENET_PRI_NUM	
Types	Macro		ENET_PRI_MIN	
Types	Macro		ENET_PRI_MAX	
Types		CPSW_TOS_PRIORITY_NUM	ENET_TOS_PRI_NUM	
Types	Macro		ENET_TOS_PRI_MIN	
Types	Macro		ENET_TOS_PRI_MAX	
Types		CPSW_VLAN_ID_MAX	ENET_VLAN_ID_MAX	
Types		CPSW_ETHERTYPE_PTP	ENET_ETHERTYPE_PTP	
Types		CPSW_LINK_CAP_HD10	CPSW_LINK_CAP> ENETPHY_LINK_CAP_	Moved to include/phy/enetphy.h
Types		CPSW_LINK_CAP_FD10	CPSW_LINK_CAP_* -> ENETPHY_LINK_CAP_*	Moved to include/phy/enetphy.h
Types		CPSW_LINK_CAP_HD100	CPSW_LINK_CAP_* -> ENETPHY_LINK_CAP_*	Moved to include/phy/enetphy.h
Types		CPSW_LINK_CAP_FD100	CPSW_LINK_CAP_* -> ENETPHY_LINK_CAP_*	Moved to include/phy/enetphy.h
Types		CPSW_LINK_CAP_HD1000	CPSW_LINK_CAP_* -> ENETPHY_LINK_CAP_*	Moved to include/phy/enetphy.h
Types	Macro	CPSW_LINK_CAP_FD1000	CPSW_LINK_CAP_* -> ENETPHY_LINK_CAP_*	Moved to include/phy/enetphy.h
Types	Macro	CPSW_LINK_CAP_10	CPSW_LINK_CAP_* -> ENETPHY_LINK_CAP_*	Moved to include/phy/enetphy.h
Types	Macro	CPSW_LINK_CAP_100	CPSW_LINK_CAP_* -> ENETPHY_LINK_CAP_*	Moved to include/phy/enetphy.h
Types	Macro	CPSW_LINK_CAP_1000	CPSW_LINK_CAP_* -> ENETPHY_LINK_CAP_*	Moved to include/phy/enetphy.h
Types	Macro	CPSW_LINK_CAP_ALL	CPSW_LINK_CAP_* -> ENETPHY_LINK_CAP_*	Moved to include/phy/enetphy.h
Types	Macro	CPSW_REMOTE_CLIENT_CORES_MAX		
Types	Enum	Cpsw_Type	Enet_Type	
Types	Macro	CPSW_COUNT	ENET_TYPE_NUM	
Types		Cpsw_MacPort	Enet_MacPort	
Types	Macro	CPSW_MAC_PORT_NUM		ENET_MAC_PORT_NUM
Types	Macro	CPSW_NORMALIZE_MACPORT	ENET_NORM_MACPORT	CPSW_MACPORT_MAX
Types		CPSW_NORMALIZE_IVIACPORT CPSW_DENORMALIZE_MACPORT	ENET_DENORM_MACPORT	
Types		CPSW_MAC_PORT_INV	ENET MAC PORT INV	
Types		Cpsw_CrcType		
Types	Struct	Cpsw_Speed	Enet_Speed	Removed 2.5G and 10G
Types		Cpsw_Duplexity	Enet_Duplexity	
Types		Cpsw_VlanSwitch		
Types		Cpsw_RestartType		
Types		Cpsw_Version		
Types		Cpsw_loctlPrms		
Types	Struct	Cpsw_GetVersionOutArgs	Enet_Version	
Types	Struct	-	Enet_VlanTagType	
Types	Struct		Enet_VlanTag	
PHY	Struct	CpswPhy_Drv	EnetPhy_Drv	macMode -> mii
PHY	Macro	CPSW_PHY_EXTENDED_CFG_SIZE_MAX	ENETPHY_EXTENDED_CFG_SIZE_MAX	

PHY	Macro CPSW_PHY_FSM_TICK_PERIOD_MS	ENETPHY_FSM_TICK_PERIOD_MS	
PHY	Macro CPSW_PHY_INVALID_PHYADDR	ENETPHY_INVALID_PHYADDR	
PHY	Macro CPSW_PHY_DEVID_AUTODETECT	Removed	
PHY	Macro CPSW_PHY_USE_DEFAULT	Removed	
PHY	Macro CPSW_PHY_NWAY_AUTOMDIX_ENABLE	Removed	
PHY	Macro CPSW_PHY_MIIMDIO_MDIXFLIP	Removed	
PHY	Macro CPSW_PHY_EVENT_NOCHANGE	Removed	
PHY	Macro CPSW_PHY_EVENT_LINKDOWN	Removed	
PHY	Macro CPSW_PHY_EVENT_LINKUP	Removed	
PHY	Macro CPSW_PHY_EVENT_PHYERROR	Removed	
PHY	Macro CPSW_PHY_EVENT_LPBK	Removed	
PHY	Macro CPSW_PHY_IS_ADDR_VALID	ENETPHY_IS_ADDR_VALID	
PHY	Macro CPSW_PHY_PUBLIC_IOCTL	ENET_PHY_PUBLIC_IOCTL	
PHY	IOCTL CPSW_PHY_IOCTL_PHY_TICK	Removed	
PHY	IOCTL CPSW_PHY_IOCTL_PHY_GET_ID	ENET_PHY_IOCTL_GET_ID	
PHY	IOCTL CPSW_PHY_IOCTL_PHY_GET_SUPPORTED_MODES	ENET_PHY_IOCTL_GET_SUPPORTED_MODES	
PHY	IOCTL CPSW_PHY_IOCTL_PHY_GET_LOOPBACK_STATE	ENET_PHY_IOCTL_GET_LOOPBACK_STATE	
PHY	IOCTL CPSW_PHY_IOCTL_PHY_GET_LINK_STATE	ENET_PHY_IOCTL_IS_LINKED	
PHY	IOCTL CPSW_PHY_IOCTL_PHY_GET_LINK_MODE	ENET_PHY_IOCTL_GET_LINK_MODE	
PHY	IOCTL CPSW_PHY_IOCTL_PHY_SET_MODE		
PHY	IOCTL CPSW_PHY_IOCTL_PHY_RESET	ENET_PHY_IOCTL_RESET	
PHY	IOCTL CPSW_PHY_IOCTL_RUN_COMPLIANCE_TEST	Removed	
PHY	IOCTL CPSW_PHY_IOCTL_PHY_IS_ALIVE	ENET_PHY_IOCTL_IS_ALIVE	
PHY	IOCTL CPSW_PHY_IOCTL_PHY_IS_LINKED	ENET_PHY_IOCTL_IS_LINKED	
PHY	IOCTL CPSW_PHY_IOCTL_READ_REG	ENET_PHY_IOCTL_READ_REG	
PHY	IOCTL CPSW_PHY_IOCTL_WRITE_REG	ENET_PHY_IOCTL_WRITE_REG	
PHY	IOCTL CPSW_PHY_IOCTL_READ_REG_EXT	ENET_PHY_IOCTL_READ_EXT_REG	
PHY	IOCTL CPSW_PHY_IOCTL_WRITE_REG_EXT	ENET_PHY_IOCTL_WRITE_EXT_REG	
PHY	IOCTL CPSW_PHY_IOCTL_READ_CLAUSE45_REG	ENET_PHY_IOCTL_C45_READ_REG	
PHY	IOCTL CPSW_PHY_IOCTL_WRITE_CLAUSE45_REG	ENET_PHY_IOCTL_C45_WRITE_REG	
PHY	IOCTL CPSW_PHY_IOCTL_PRINT_REGS	ENET_PHY_IOCTL_PRINT_REGS	
PHY	Struct CpswPhy_GenericInArgs	EnetPhy_GenericInArgs	portNum -> macPort
PHY	Struct CpswPhy_GetModeOutArgs	Removed	
PHY	Struct CpswPhy_SetModeInArgs	Removed	
PHY	Struct CpswPhy_ComplianceTestInArgs	Removed	
PHY	Struct CpswPhy_ReadRegInArgs	EnetPhy_ReadRegInArgs	portNum -> macPort
PHY	Struct CpswPhy_WriteRegInArgs	EnetPhy_WriteRegInArgs	portNum -> macPort
PHY	Struct CpswPhy_Clause45ReadRegInArgs	EnetPhy_C45ReadRegInArgs	portNum -> macPort
PHY	Struct CpswPhy_Clause45WriteRegInArgs	EnetPhy_C45WriteRegInArgs	portNum -> macPort
PHY	Struct CpswPhy_Version	EnetPhy_Version	
PHY	Struct CpswPhy_FsmTimeoutConfig	EnetPhy_FsmTimeoutCfg	
PHY	Struct CpswPhy_Config	EnetPhy_Cfg	mdixEnable -> mdixEn
DUV	Typedef CpswPhy_Handle	EnatDhy Handle	enableLoopback -> loopbackEn
PHY	Typeder Charling Landie	EnetPhy_Handle	