Sistemas Operativos Avanzados

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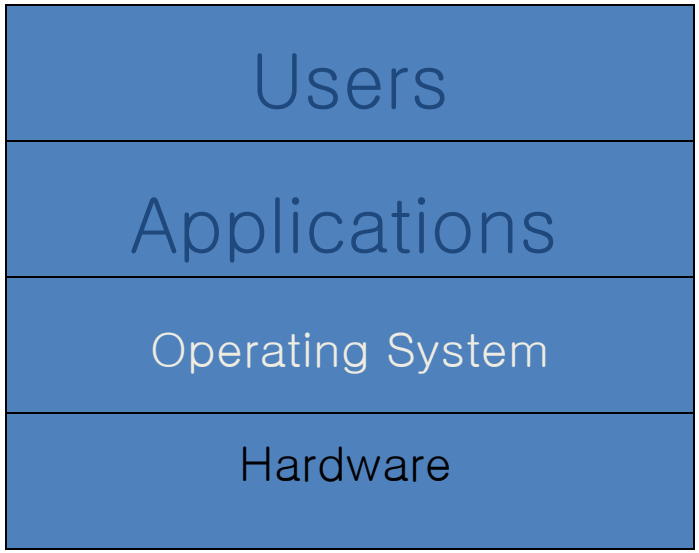
What is an Operating System?

Operating System (OS):

Software that converts hardware into a useful form for applications

Not easy to define precisely…



What DOES OS Provide?

- Role #1: Abstraction - Provide standard library for resources
- What is a resource?
  - Anything valuable (e.g., CPU, memory, disk, I/O device)
- What abstraction does modern OS typically provide for each resource?
  - CPU:
    - process and/or thread
  - Memory:
    - address space
  - Disk:
    - files
- Advantages of OS providing abstraction?
  - Allow applications to reuse common facilities
  - Make different devices look the same
  - Provide higher-level or more useful functionality
- Challenges
  - What are the correct abstractions?
  - How much of hardware should be exposed?

What DOES OS Provide?

- Role #2: Resource management – Share resources well

- Advantages of OS providing resource management?
  - Protect applications from one another
  - Provide efficient access to resources (cost, time, energy)
  - Provide fair access to resources

- Challenges
  - What are the correct mechanisms?
  - What are the correct policies?

OS Organization

- How to cover all the topics relevant to operating systems?

Three PIECES

- Virtualization:
  - Make each application believe it has each resource to itself
- Concurrency:
  - Events are occurring simultaneously and may interact with one another
- Persistence: Access information permanently
  - Lifetime of information is longer than lifetime of any one process
  - Machine may be rebooted, machine may lose power or crash unexpectedly

Advanced Topics (beyond our reach)

- Current systems
  - Multiprocessors
  - Networked and distributed systems
  - Virtual machines
  - Containers
  - …

- Many of the pushed by the explosive demand (a.k.a. Massive complexity under constrained cost)

- This is the support of the world: it will keep changing …

- Some of them covered in SVS (M1679)

Why study Operating Systems?

- Build, modify, or administer an operating system
- Understand system performance
  - Behavior of OS impacts entire machine
  - Tune workload performance
  - Apply knowledge across many layers
    - Computer architecture, programming languages, data structures and algorithms, and performance modeling
- Fun and challenging to understand large, complex systems

Approach

- We will follow “Operating System: Three Easy Pieces” (OSTEP) style
  - From the bottom concepts to state-of-the-art approaches
  - Eminently practical style: all supported by ”simulators” and simple coding examples
  - Assumes some basic knowledge in architecture, C, assembler and system administration
  - More than just a text book…

- Structure
  - The three parts are split in small *pieces* (~40 in the book)
  - Each chapter is build over the previous one (can’t miss the beat)
  - Each chapter has attached a ”Homework” to reinforce the : from using python simulators to write small pieces of code (C)
  - 5+1 Labs, developed on top of xv6

Lecture/Lab structure

- We mix dynamically both
  - The real thing is that there is no separation between "theory" and "lab"

- Sessions of:
  - 1st hour: Introduction to the topic
  - 2nd hour: Introduce/develop of Labs
  - Personal work (out the lab): 6 hours (labs and homework)
  - 10 hours/week
  - Strict schedule

- Although the original course/book is designed for 15 week semester (150h work), we will need to drop some details or advanced topics (and half of the labs)

Material

- Available in http://www.ce.unican.es/

- All written material will be in "English"
  - Lecture notes, Homework/Lab guides, etc…

- *Git* as communication "device": all material will be delivered via http://gitlab.com (lab work) and http://github.com (lecture notes)
  - An e-mail inviting to join the course project will be sent to unican account
  - Slides, labs, other reference material is there
  - It uses "git" to have a "time-track"
    - Lecture notes updates
    - Additional material

- Use git to allow you (& me) to "track" the personal work

Book (ostep.org)

pages.cs.wisc.edu/~remzi/OSTEP/

Operating Systems: Three Easy Pieces

Remzi H. Arpaci-Dusseau and Andrea C. Arpaci-Dusseau

Blog: Why Textbooks Should Be Free

Quick: Free Book Chapters - Buy Hardcover - Buy Softcover - Buy PDF - Buy from EU - Buy T-shirt - For Teachers - Homework - News - Acknowledgements - Other Books

Welcome to **Operating Systems: Three Easy Pieces** (now version 0.91 -- see book news for details), a free online operating systems book! The book is centered around three conceptual pieces that are fundamental to operating systems: **virtualization, concurrency,** and **persistence.** In understanding the conceptual, you will also learn the practical, including how an operating system does things like schedule the CPU, manage memory, and store files persistently. Lots of fun stuff!

This book **is and will always be free** in PDF form, as seen below. For those of you wishing to **BUY** a copy, please consider the following:

- A wonderful hardcover edition (v0.91) - this may be the best printed form of the book (it really looks pretty good), but it is also the most expensive way to obtain *the black book* of operating systems (a.k.a. *the comet book* or *the asteroid book* according to students). Now just: **$36.00**
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- An alpha version for Kindle - Really, this is just the PDF and does not (yet) include all the bells and whistles common in e-pub books.

Sale on print books: Save 30% using code THEBIG30 (until September 19).

New: Can't bear to go out in public without an operating system? How about an Operating Systems: Three Easy Pieces T-shirt ? The t-shirt and printed/electronic books are both brought to you by the demand of various students and professors, and are a nice way to show your appreciation.

Another way to help the book out: cite it! Here is the BiBTeX entry (seen below); you can also link to the site of the best free operating systems book on the market.

Operating Systems: Three Easy Pieces
Remzi H. Arpaci-Dusseau and Andrea C. Arpaci-Dusseau
Arpaci-Dusseau Books
March, 2015 (Version 0.90)

And now, the free online form of the book, in chapter-by-chapter form (now with chapter numbers!):

Intro	Virtualization		Concurrency	Persistence	Appendices
Preface	3 *Dialogue*	12 *Dialogue*	25 *Dialogue*	35 *Dialogue*	*Dialogue*
TOC	4 Processes	13 Address Spaces	26 Concurrency and Threads code	36 I/O Devices	Virtual Machines
1 *Dialogue*	5 Process API code	14 Memory API	27 Thread API	37 Hard Disk Drives	*Dialogue*
2 Introduction code	6 Direct Execution	15 Address Translation	28 Locks	38 Redundant Disk Arrays (RAID)	Monitors
	7 CPU Scheduling	16 Segmentation	29 Locked Data Structures	39 Files and Directories	*Dialogue*
	8 Multi-level Feedback	17 Free Space Management	30 Condition Variables	40 File System Implementation	Lab Tutorial
	9 Lottery Scheduling code	18 Introduction to Paging	31 Semaphores	41 Fast File System (FFS)	Systems Labs
	10 Multi-CPU Scheduling	19 Translation Lookaside Buffers	32 Concurrency Bugs	42 FSCK and Journaling	xv6 Labs
	11 *Summary*	20 Advanced Page Tables	33 Event-based Concurrency	43 Log-structured File System (LFS)	Flash-based SSDs
		21 Swapping: Mechanisms	34 *Summary*	44 Data Integrity and Protection	
		22 Swapping: Policies		45 *Summary*	
		23 Case Study: VAX/VMS		46 *Dialogue*	
		24 *Summary*		47 Distributed Systems	
				48 Network File System (NFS)	
				49 Andrew File System (AFS)	
				50 *Summary*	

Homeworks

- Some chapter (most) include homework
  - Homeworks can be used to solidify your knowledge of the material in each of the chapters
  - Most homeworks are based on running little **simulators,** which mimic some aspect of an operating system: For example, a disk scheduling simulator could be useful in understanding how different disk scheduling algorithms work:
    - Most of them provides the solution
  - Some home-works are just short programming exercises, allowing you to explore how real systems work and complement Lab work.

- Homework are done in personal-time
  - Will be "evaluated" in the exams

Labs: C and xv6

- Refresh C knowledge
- Use a “toy” kernel to dig into implementation details
  - It is a clean and beautiful little kernel, and thus a perfect object for our study and usage.
  - It was developed by OS Eng. In MIT as a port of K&R original Unix R6/PDP11
  - Use al real kernel (such as linux) will be certainly overkill

```
cigal xv6-wisc (master)*$ cloc *
     145 text files.
     143 unique files.
      15 files ignored.

http://cloc.sourceforge.net v 1.64  T=1.34 s (99.9 files/s, 8217.7 lines/s)
-------------------------------------------------------------------------------
Language                     files          blank        comment           code
-------------------------------------------------------------------------------
C                               45            946            621           5855
Assembly                         9             58            124           1748
C/C++ Header                    20            177            138            955
D                               57              0              0            154
make                             1             40             47             90
Perl                             2             11             22             33
-------------------------------------------------------------------------------
SUM:                           134           1232            952           8835
-------------------------------------------------------------------------------
```

Prerequisites

- All OS and architecture previous subjects(ugh!)

Evaluation

- The subject is evaluated by 2 exams (both theory and lab) and:
  - *"Es condición necesaria e indispensable entregar en plazo las prácticas de laboratorio correspondientes a cada uno de los parciales de la asignatura para poder ser evaluados".*
  - Like in real life, there is no such thing as `theory!= lab` : both are tightly integrated

- Recuperacion:
  - Might be harder…

Schedule

Date	Chapter	Lab	Homework
28-sep.	1 Intro	P0 Lab Intro and review C	
2-oct.	4. The Abstraction: The Process/ 5. Interlude: Process API		Process Intro / Process API
5-oct.	6. Mechanism: Limited Direct Execution		Direct Execution
9-oct.	7. Scheduling: Introduction	P0 Due, P1 System Calls	Scheduler
16-Oct	8: Scheduling: The Multi-Level Feedback Queue		MLFQ Scheduling
19-oct.	9: Scheduling: Proportional Share/10. Multiprocessor Scheduling		Lottery Scheduling
23-oct.	13. The Abstraction: Address Space / 14. Memory API	P1 Due, P2 Scheduling	VM API
26-oct.	15. Address Translation		Relocation
30-oct.	16. Segmentation		Segmentation
2-nov.	17. Free-Space Management/18 Pagin Intro.	*P2 Due*	Free Space
6-nov.	19. Translation Lookaside Buffers	*P3 Memory*	Paging
9-nov.	20. Paging: Smaller Tables		TLBs
13-Nov	21. Swapping: Mechanisms/22. Swaping: Policies		Multi-level Paging/Paging Mechanism
16-Nov	26. Concurrency: An Introduction / 27. Interlude: Thread API		Threads (Intro)/Threads (API)
20-Nov	28. Locks		Threads (Locks)
23-Nov	29. Lock-based Concurrent Data Structures		
27-Nov	30. Condition Variables	P3 Due, P4 Threads	Threads (CVs)
???	Mid Term Exam I (Processes & Memory)		
30-Nov	31. Semaphore		
04-Dec	32. Common Concurrency Problems.		Threads (Bugs)
07-Dec	33. Event-based Concurrency (Advanced)		
11-Dec	36. I/O Devices		
14-Dec	37. Hard Disk Drives		Disks
18-Dec	39. File and Directories	P4 Due, P5 File systems	39. File and Directories
21-Dec	40. File system Implementation.		FS Implement
08-Jan	41. Fast File System / 42. Crash Consistency: FSCK		FFS
11-Jan	42. Crash Consistency: Journaling	P5 Due	
	43. Log-structured File Systems		
	44. SSD Disks		
???	Mid Term Exam II (Concurrency & Persistence)		