



## Features

- Generates the control signals needed by the APC-128s
- 32 ADC Channels to read out all APC128s in parallel, minimizing dead time
- FPGA Board with associated USB hardware enables high-speed communication with online software
- Handles external triggering from a variety of sources via an translator mezzanine card

## Components

- 1×**Opal Kelly ZEM4310** with
  - Cyclone IV FPGA
  - 128MB RAM
  - USB-3.0
  - 2×HSMC Connectors
- 8×AD9219 40MHz/ 10 bit ADCs
- 8×RJ-45 Ports
- 2×40-Pin 0.1" Header
- Bias-voltage control relay
- On-board power regulation