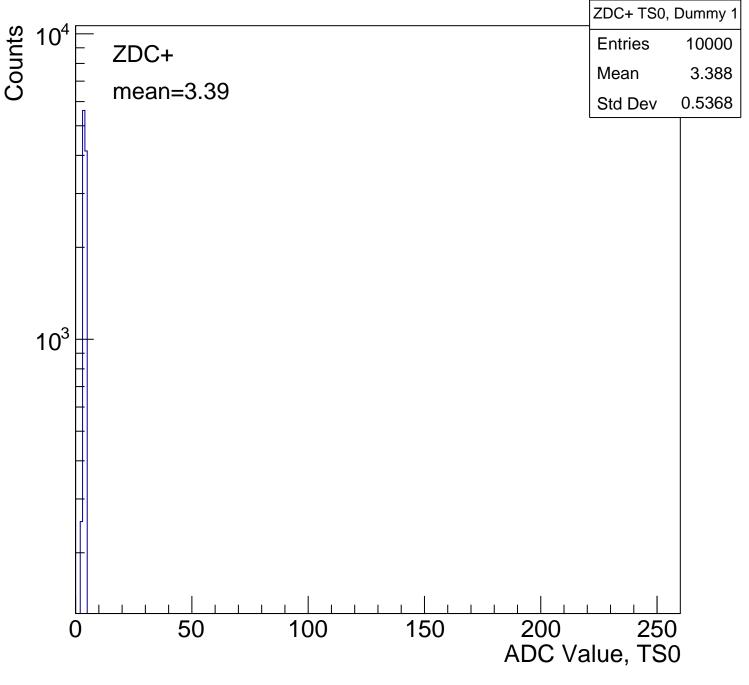
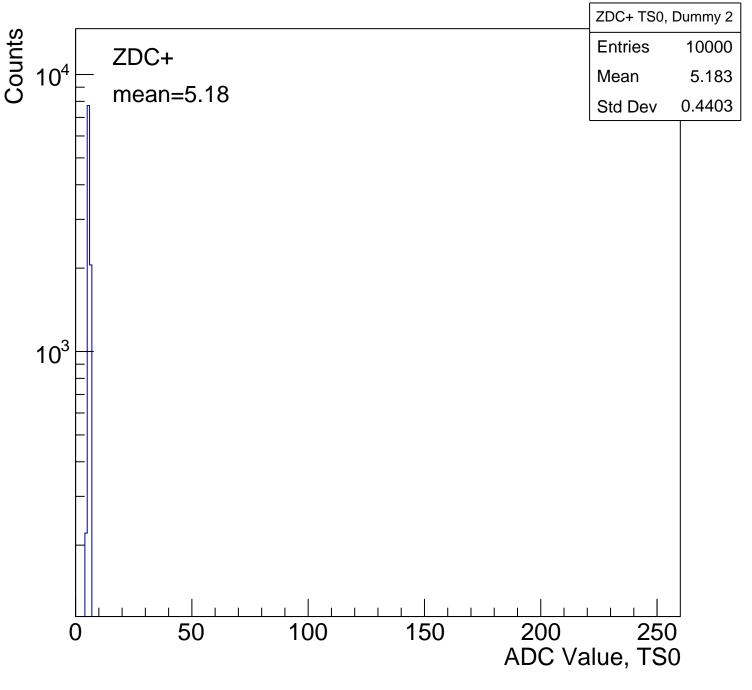


# ZDC+ TS0, Dummy 1



# ZDC+ TS0, Dummy 2



# ZDC+ TS0, Dummy 3

