

Room	Morning	Afternoon
Mees I	YArch	YArch
Mees II	FPGAs in the datacenter	XiangShan tutorial
Penn I	ASPLOS 2025 Contest Tracks	ASPLOS 2025 Contest Tracks
Penn II	MCCSys	MCCSys
Leeuwen I	GreenSys	GreenSys
Leeuwen II	xDSL	AIBrix
Goudriaan I	HCDS	Understanding CXL
Goudriaan II	GenAI Catalyst	ILLIXR
J.F. Staal	LATTE	LATTE
New York I	Agile hardware tutorial	Quantum Circuit
New York II	MLBench	MLBench
Tokyo	SIGARCH Visioning: Sustainable Computing	SIGARCH Visioning: Sustainable Computing