

编码:

OP(ins[31:26])	base(ins[25:21])	rt(ins[20:16])	offset(ins[15:7])	ins[6]	LWE(ins[6:0])
011111	xxxxx	xxxxx	xxxxxxxxx	0	101111

格式: lwe rt, offset(base)

描述: $GPR[rt] \leftarrow memory[GPR[base] + offset]$

The contents of the 32-bit word at the memory location specified by the aligned effective address are fetched, signextended to the GPR register length if necessary, and placed in GPR rt. The **9-bit** signed offset is added to the contents of GPR base to form the effective address.

操作: $Addr = sign_extend(offset) + GPR[base]$

$GPR[rt] \leftarrow memory[Addr]$

示例: lwe \$t1, 8(\$t0)

其他:

1. $sign_extend(offset) + GPR[base]$ 必是 4 的倍数。
2. $sign_extend(offset) + GPR[base]$ 不会越界。