1. More on interrupts
   a. Last time – discussing changes we need to make to support interrupts

2. Classification of interrupts
   a. Examples of possible interrupts

   b. How to classify interrupts
      i. By timing

      ii. Type of user

      iii. Masking

      iv. Location in instruction

      v. Result

3. Memory overview
    a. Terminology

    b. Characteristics of memory systems
        i. Location

        ii. Capacity

        iii. Units of transfer

        iv. Access methods
            1. Sequential

            2. Direct access

            3. Random access

            4. Associative

        v. Performance

vi.   Physical type

vii.   Volatility

viii.   Erasability