-Implemented Full Adder using Half Adder
-Two Half adders are used with OR gate to produce a full adder.
-First two numbers are added using a half adder, it generates a sum and carry1.
-That sum is again taken as one input and carry in as another input to half adder which calclates Final sum and carry2.
-Both carry1 and carry2 are feeded to OR gate to get the final carry.

Inputs			Outputs	
A	B	C_{in}	Carry	Sum
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

Table 3.7 Truth table for full-adder

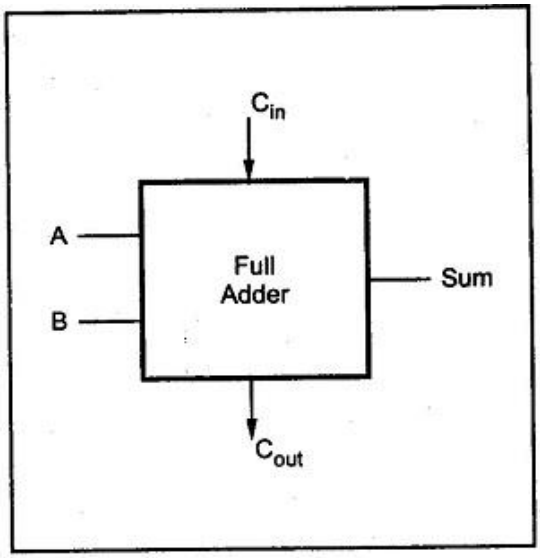

Fig. 3.14 Block schematic of full-adder