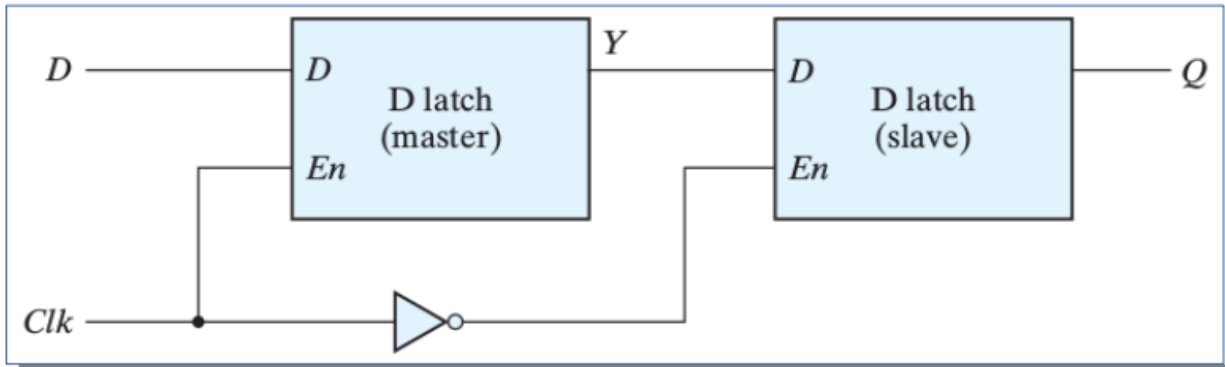
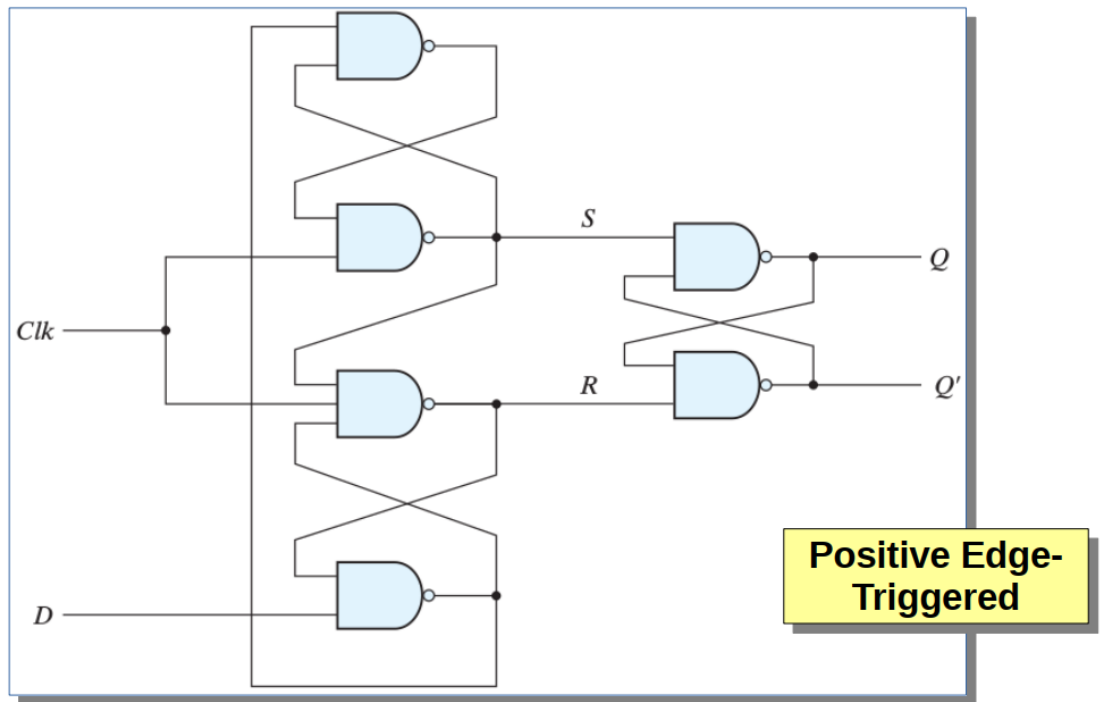


Implemented D flip flop by three methods

1.dffms.v contains code for D Flip flop implemented using D-latches(master slave).This is a negative edge triggered Flip Flop.It will change at negative edge of the clock.It is represented in wave form by Qms(output) .



2.dffdr.v contains code for D Flip Flop implemented using gate level implementation. This is a positive edge triggered flipflop. It will change at positive edge of the clock.It is represented in wave form by Qsr(output) .



3.dffbehav.v contains code for D Flip Flop implemented using behavioral implementation. This is a positive edge triggered flipflop. It will change at positive edge of the clock.It is represented in wave form by Q(output) .