

CHAITANYA AGARWAL

COLLEGE STUDENT

PERSONAL PROFILE

Enthusiastic and energetic, eager to contribute to team's success through hard work, attention to detail and excellent organizational skills. Clear understanding of algorithms and classifiers and knowledge of C++, python and knowledge of Machine learning. Motivated to learn grow and excel.

WORK EXPERIENCE

Undergrad Engineering Virtual Internship program

Microsoft | June 2020 - August 2020

- Learned about strategizing, personal branding, presenting yourself
- Organizing and planning, team building and learned about Microsoft culture

Microsoft Engage 2021

Microsoft | June 2021 - July 2021

- Challenge to make a Microsoft teams clone with any framework, APIs of our choice

EDUCATIONAL HISTORY

J M International School

High school graduate | April 2009 - May 2019

- CGPA-10 in X class(English,French,Social science,Science)
- Scored 96.5% in XII class

Delhi Technological University

Electrical Engineering | August 2019 - Present

- CGPA: 9.67

PROJECTS

1.Sentiment Analysis on Netflix data

- Extracted the useful data, cleaned it
- Did the Analysis of the Releases, Genre
- Predicting the sentiment of a movie through its description using BLOB module

2. Interactive Maps in Global Temperature

- PLotted dynamic plots on global temperature from year 1973 to 2013
- Interactive plots using Plotly and ipywidgets

3. Movies Similarity

- Extracted useful features from the dataset and performed feature engineering using nltk module
- Using Kmeans to group similar movies
- Calculated the similarity distance using Cosine similarity and plotted Dendrogram to visualize

POSITION OF RESPONSIBILITY

1. Electronic design engineer at RAFTAAR tech team
- Worked on the landing gear of the vehicle and control it using Arduino uno



CONTACT ME AT



Address: Winter hills apartments, New delhi-110059



chaitanyaagarwal3323@gmail.com



chaitanyaagarwal08@gmail.com

SKILLS SUMMARY

•••• Machine Learning

•••• Data analytics

•••• Python

•••• Sql

•• Tableau

•••• C++

ACOMPLISHMENTS



Electronic design engineer at RAFTAAR tech team DTU



Participated in HPVC competition organized by ASME

PORTFOLIO

<https://chaitanya3323.github.io/>