

Poluru Sai Chaithanya

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OBJECTIVE

Enthusiastic Engineer with a strong foundation in fundamental concepts and a drive to excel in the field. Ready to embark on a career path that offers challenges, growth opportunities, and the chance to make a positive impact and being optimistic in life.

EDUCATION

B.Tech Electronics and Communication Engineering Gitam University - 8.47CGPA	2021 – present Bengaluru
Intermediate-MPC Narayana Junior College - 96.6%	2019 – 2021 Hyderabad
10th-State Board Sri Chaitanya Techno School - 9.8GPA	2018 – 2019 Anantapur

SKILLS

Technical Skills

VLSI, Digital Logic Circuits, C, SQL(Basics) and Python Data Structures and Algorithms

Soft Skills

Communication, Decision Making, Work Ethic, Teamwork

EXPERIENCE

Summer Unnati Training Intern- Intel • I worked as an intern tasked with optimizing large language model for deployment on Intel AI hardware using OpenVINO. • Developed an AI chatbot for the allotted problem statement.	05/2024 – 07/2024 Hybrid
VLSI Internship - iStop.ai • Developed and implemented a traffic light controller for a cross-shaped intersection using Verilog as part of an internship project. • Promoted open and constructive communication among team members.	10/2023 – 11/2023 Remote

PROJECTS

Artificial Intelligence Chatbot

- This project involves creating a customized chatbot to address queries and FAQs for Gitam University.
- The chatbot leverages fine-tuned pre-trained Large Language Models (LLMs) using Intel AI Tools and Intel® OpenVINO™.

Traffic Light Controller

- Implementation of a 6-state finite state machine traffic light controller for cross intersection in Verilog.
- Performed a rich number of simulations to validate correct behavior on traffic light controller for smooth state transitions and with 100% accurate lighting configurations.

CERTIFICATES

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| • Python Data Structures issued by University of Michigan - Coursera | • Gmail authorized by Google Cloud – Coursera | • TCS ION Carrer Edge – Young Professional skills |
| • VLSI course for beginners participation – NIELIT Calicut | • VLSI intern and project completion certificate by Apsis Solutions(iStop.ai). | • Basic Environmental Engineering and Pollution Abatement (Elite) – NPTEL |

INTERESTS

Photography | Gardening | Sports | Music