

DESIGN AND IMPLEMENTATION OF UNSIGNED BINARY MULTIPLIER

Review I presentation
presented by

Chaitra alur - 01FE21MVE006

Professor Name : DR. Saroja V Siddamal
School of Electronics and Communication Engineering
KLE Technological University ,Hubballi

Contents

- Problem statement
- Flow chart
- Block diagram
- example

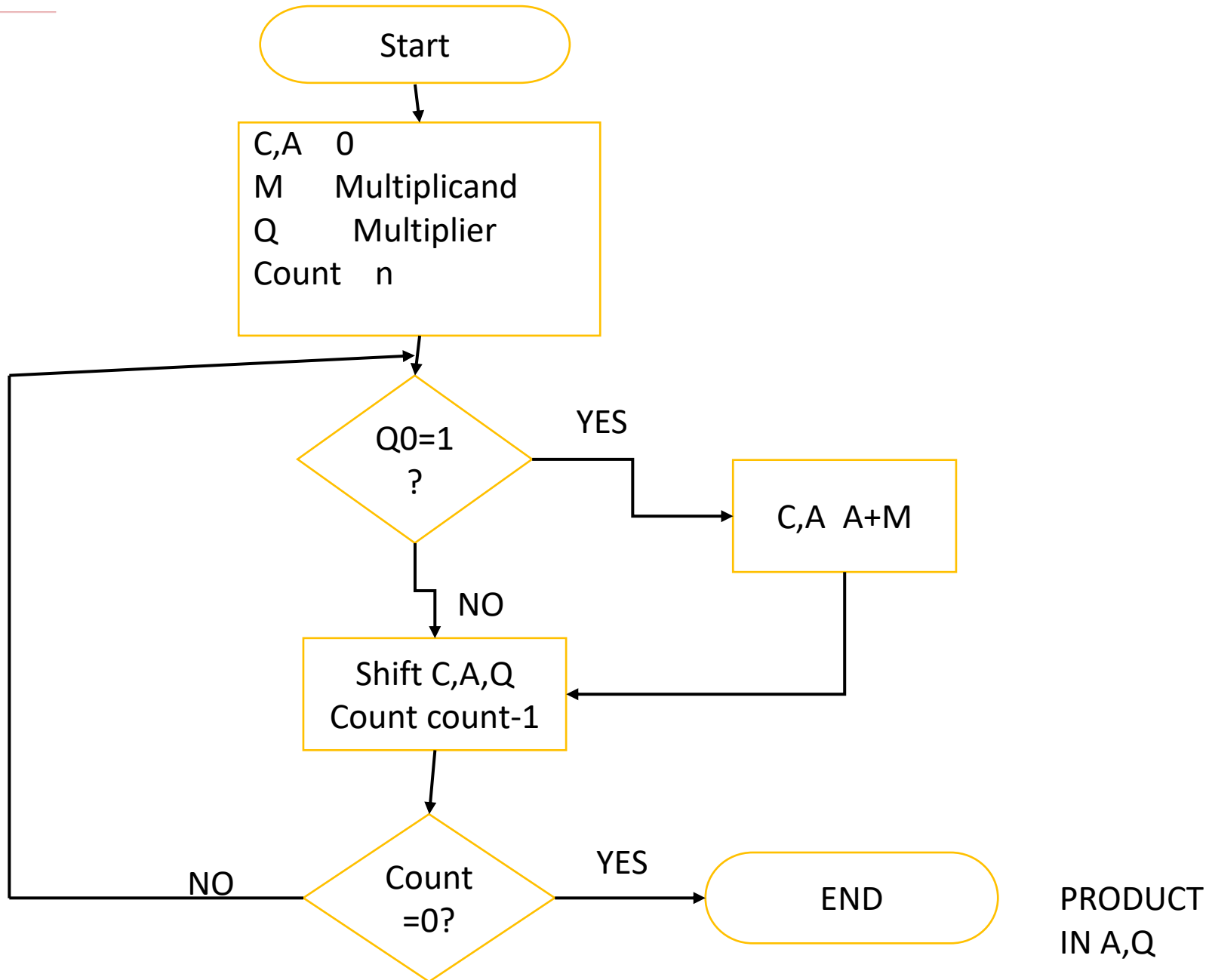
Problem statement

design and implementation of unsigned binary multiplier

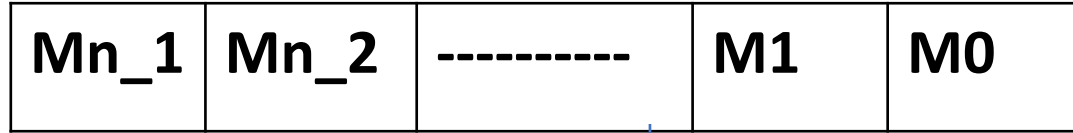
- Variables such as integers can be represent in two ways, i.e., signed and unsigned. Signed numbers use sign flag or can be distinguish between negative values and positive values. Whereas unsigned numbers stored only positive numbers but not negative numbers.
- Unsigned numbers don't have any sign, these can contain only magnitude of the number. So, representation of unsigned binary numbers are all positive numbers only. For example, representation of positive decimal numbers are positive by default. We always assume that there is a positive sign symbol in front of every number.

○

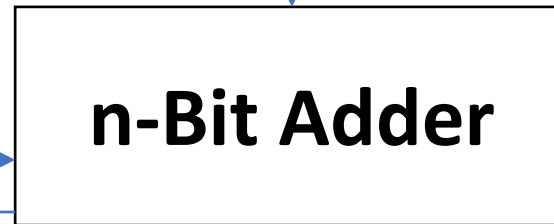
Flow Chart Of Unsigned Binary Multiplication



Block Diagram

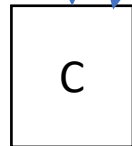


n-bit Bus

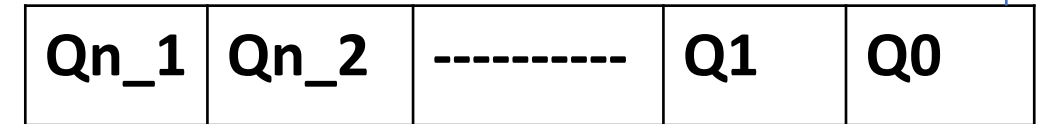
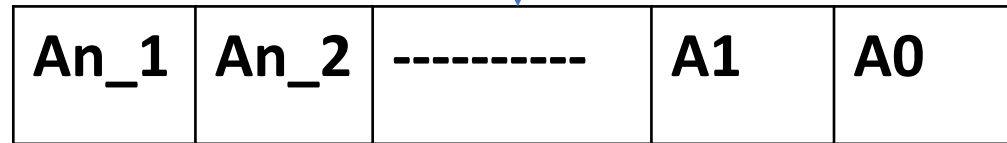


Add

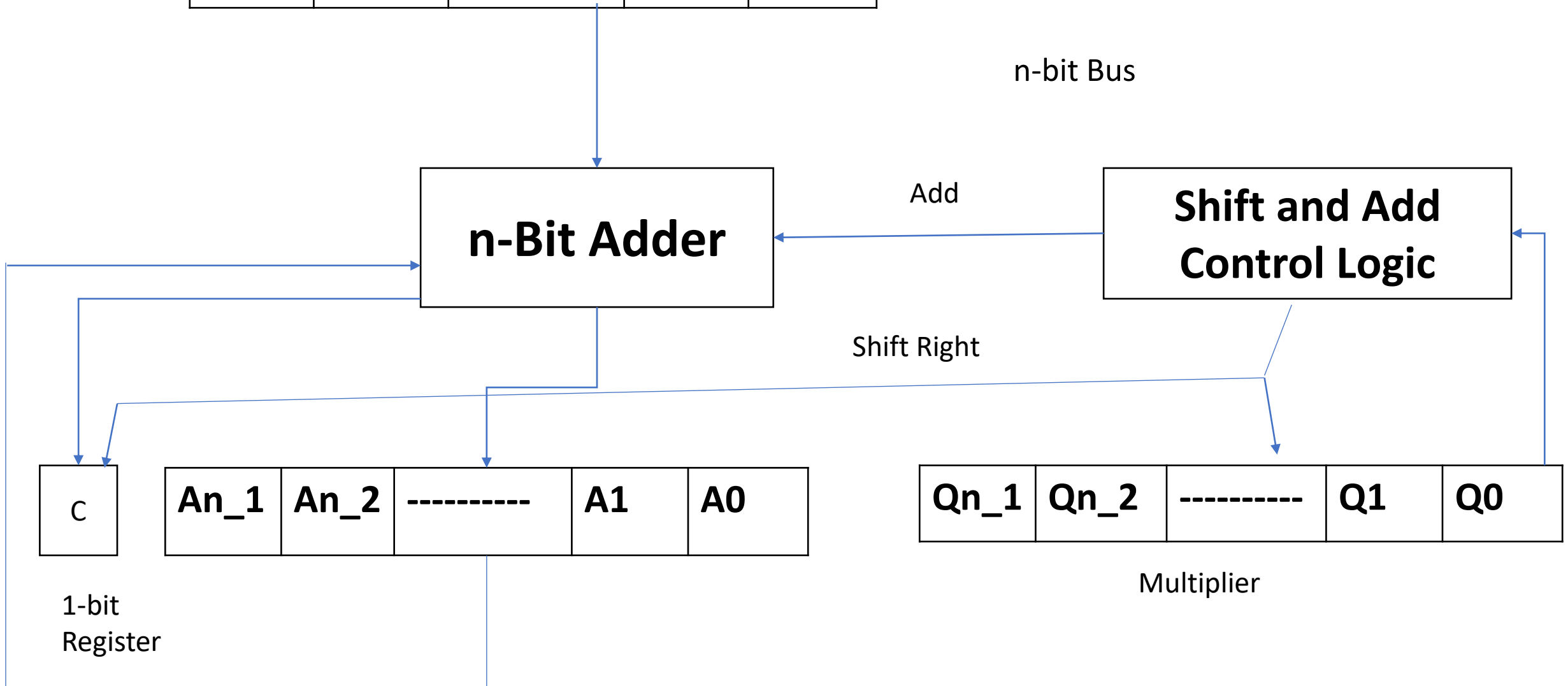
Shift Right



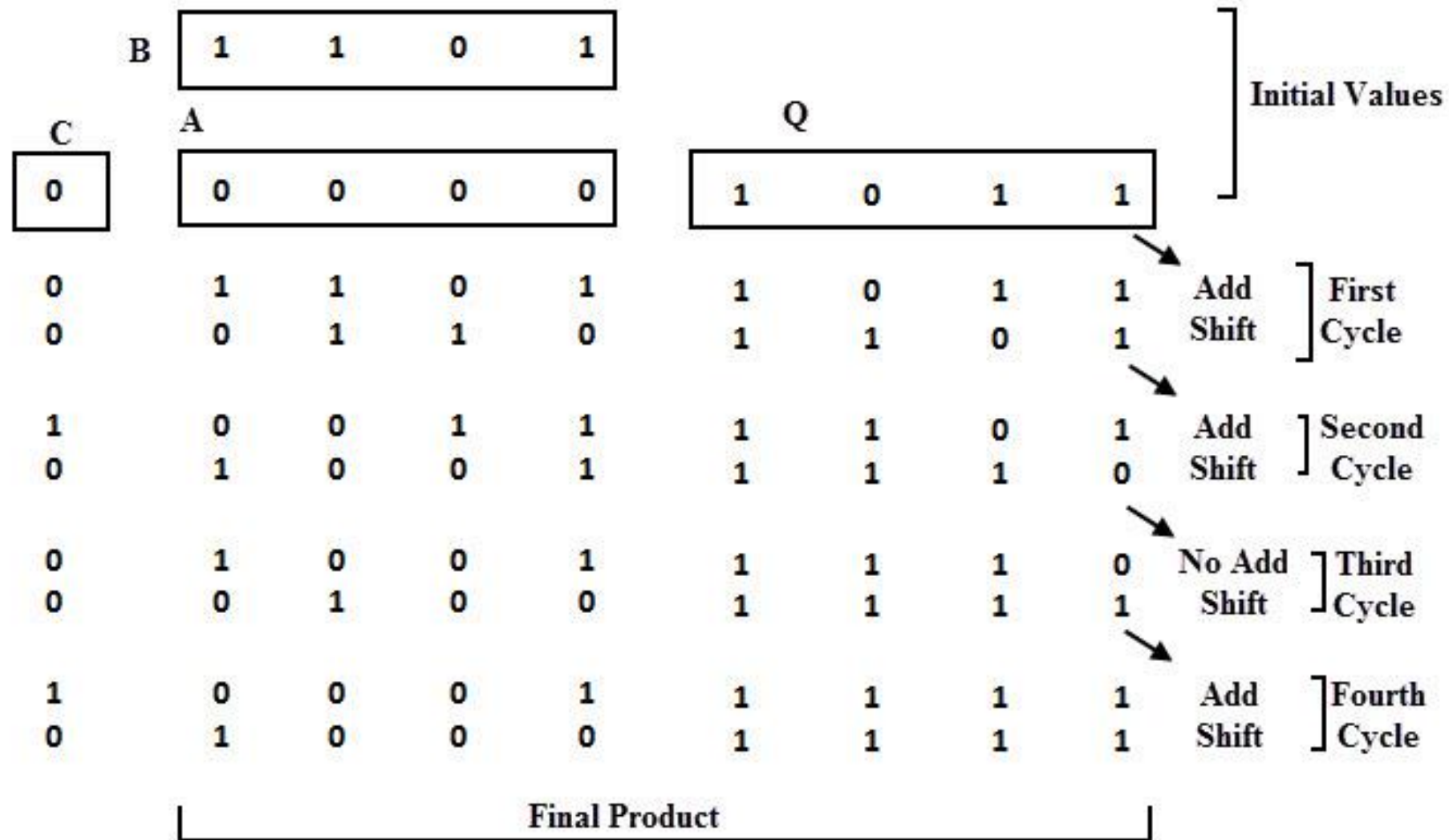
1-bit
Register



Multiplier



Example for unsigned binary multiplication



THANK
YOU