San Jose State University Department of Computer Engineering

CMPE 127 Lab Report

Final Project Report

Title: The Line Follower Robot

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By

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Abstract— Making a robot follows a given black line on a white given platform and searches the path to the treasure.

I. OBJECTIVE

The final project required to design a *Line* Follower Maze Robot using TI Robotics System Learning Kit by leveraging the skills learned in different phases of the lab throughout the semester. The goal of the project was to integrate the line sensor and the bump sensor to solve a maze that searches for a treasure. The project was a combinational lab with six modules: Module 6 for GPIO interface the line sensor, Module 10 for SysTick interrupts, Module 12 and 13 for bump sensor, motors and PWM control, and Module 13 for line follower building. To achieve the goal of the project, the robot should start from the start point on the platform, self-drive via the line, know how to find the right path to the treasure and how to back up for lost, and stop at the treasure spot.

II. REQUIRED COMPONENTS

- •Robot chassis, DC motors, and wheels.
- •Motor drive and power distribution board. Line IR sensors
- Bump sensors
- MSP432P401R MCU LaunchPad
- Rechargeable battery, pack of 6, metal hydride, 1300mAh, 1.2V, AA

III. DESIGN METHODOLOGY

The first part of the lab was to test the reflectance of the sensor in module 6. At this step, there are nine cases need to be recorded:

- 1. The robot stayed at the center: Position >-47 && Position <47.
- 2. Slightly off to the left: Position <= 47 && Position > -142

- 3. Slightly off to the right: Position >= 47 && Position <142
- 4. Off to the left: Position <= -142 && Position >-237
- 5. Off to the right: Position >= 142 && Position < 237
- 6. Way off to the left: Position <= -237 && Position > -332
- 7. Way off to the right: Position >= 237 && Position < 332
- 8. Lost: Data == 0b11111111 && Position == 0
- 9. At the treasure's position: Data == 0b000000000 && Position == 333

Theoretically, when the robot stayed at the center position, it should go forward until the next instruction came. When the robot was not at the center, it should fix its path back to the center.

The second part of the lab was to test the bump interruption. Theoretically, when any of the bumps were activated, meaning there was a wall exist, the robot should back up and search for a new path.

The third part of the lab was to set the same PWM on both motors when the robot ran, but the time delay is different for each of the 9 cases that should be different. This design was to easily control the robot.

Combine 3 parts above, we have flow chart as Figure 1.

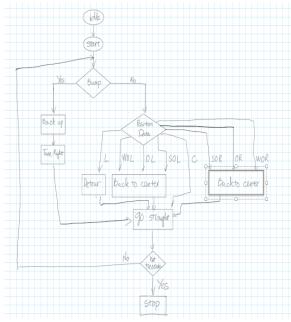


Figure 1: Flow chart of design.

IV. TESTING RESULT

The demonstration is performed on the platform as figure 2.

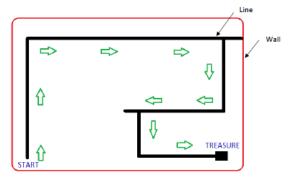


Figure 2: Robot explorer path to the treasure.

The successfully rate after 20 tests is 95%. During the failed test, the robot was encountered a situation where the sensors array was perpendicular to the track (Figure 3), so the robot thought that it hit the treasure so it stopped. This is a room for improvement.

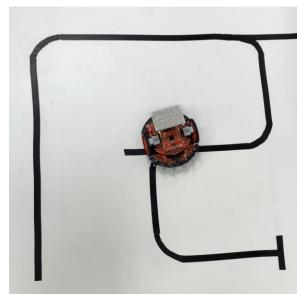


Figure 3: The sensors array is perpendicular to the track.

V. CHALLENGE ENCOUNTERED

There were two major challenges existed during the designing process and testing processing: reading line sensor error and design robot path with different time delay and PWM motors.

The first challenge was reading the line sensors error. Before the lab started, the line sensors worked perfectly, they captured data immediately. However, after calling this function in module 13, all sensors stopped working. Later, all sensors can finally capture data, but the responding time was too slow. When the robot was testing on the board, the line sensors stopped working again. We assumed that the problem came from our coding design, but after we changed to the new batteries, the sensors were working fine again.

The second challenge was to design the robot to take a detour when it is on the white space. The solution for this was to set

the condition as Data = 0b1111 1111, Position = 0 in the if statement in if-else statement set. If we do not do this, the robot will keep running straight instead of take detour as Figure 4.

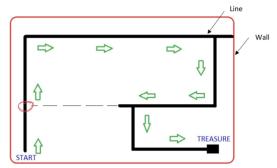


Figure 4: Wrong Path Detecting.

VI. CONCLUSION & IMPROVEMENTS
The project is very fun and helpful. The
robot can run on the given platform with
95% successful rate. All the challenges have
been solved.

The project provided a chance to practice:

- Periodic SysTick interrupts to measure the line sensor.
- Periodic Timer A1 interrupts to run the high-level strategy.
- Edge triggered interrupts for collisions.
- Main program for debugging and low priority tasks.

Beside that, the robot all so need two improvements:

• Try to reach 99.99% successful rate. Figure has shown the failed case. We should add another array of sensor (Figure 5), so the robot can distinguish the failed case and the treasure. Failed case: QTR-8RC is seeing the black line while QTR-2RC is seeing the white space, then the robot keep running forward. Treasure case, both QTR-

8RC and QTR-2RC are seeing black line, the robot stops. By doing this, we can eliminate the encountered failed case.

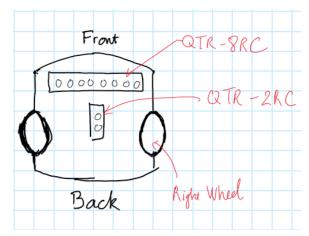


Figure 5: Robot after adding QTR-2RC sensor.

• Currently, the average time to reach the treasure is 60 seconds. Therefore, another key improvement will be speed up the robot by modifying the algorithm and delay time, so it can finish the task faster than the present version.

There were many challenges and problems existed during lab time, but during the demonstration, the robot passed all the tests within the first trial. The robot followed the line onboard correctly and stopped on the treasure spot perfectly.

VII. APPENDICES AND REFERENCES

Header files:

#include "msp.h"
#include "..\inc\bump.h"
#include "..\inc\Clock.h"
#include "..\inc\SysTick.h"
#include "..\inc\CortexM.h"
#include "..\inc\LaunchPad.h"
#include "..\inc\Motor.h"
#include "..\inc\TimerA1.h"
#include "..\inc\TexaS.h"
#include "..\inc\Reflectance.h"

```
Motor Stop();
Driver Module:
                                                              } else if (Position >= 47 &&
void TimedPause(uint32_t time) {
                                                      Position <142) { //slightly off to the right
 Clock Delay1ms(time);
                                 // run for
                                                                  Motor Right (speed, speed);
a while and stop
                                                                  Clock Delay1ms(time1);
 Motor Stop();
                                                                  Motor Stop();
 while(LaunchPad Input() == 0); // wait for
                                                              } else if (Position <= -142 \&\&
                                                      Position >-237) \{ //off to the left
 while(LaunchPad Input());
                                // wait for
                                                                  Motor_Left(speed, speed);
release
                                                                  Clock_Delay1ms(time2);
                                                                  Motor_Stop();
                                                              } else if (Position \geq 142 &&
uint8 t Data; // QTR-8RC
                                                      Position < 237) { // off to the right
int32 t Position; // 332 is right, and -332
                                                                  Motor Right (speed, speed);
is left of center
                                                                  Clock Delay1ms(time2);
                                                                  Motor Stop();
int main(void){
                                                              } else if (Position \leftarrow -237 &&
    TExaS_Init(LOGICANALYZER P2);
                                                      Position > -332) { // way off left
    Clock Init48MHz();
                                                                  Motor Left(speed, speed);
    LaunchPad Init(); // built-in switches
                                                                  Clock Delay1ms(time3);
and LEDs
                                                                  Motor Stop();
    TimerAl Init(&bump interrupt,50000); //
                                                              } else if (Position >= 237 &&
                                                      Position < 332) { // way off right
    EnableInterrupts();
                                                                  Motor Right (speed, speed);
    Bump Init();
                      // bump switches
                                                                  Clock Delay1ms(time3);
    Motor Init();
                      // your function
                                                                  Motor Stop();
    Reflectance_Init();
                                                              } else if (Data == 0b00000000 &&
                                                      Position == 333) { // goal
    int speed = 1500, backup speed = 4000;
                                                                 Motor Stop();
    int time1 = 50,
                                                                  //break:
        time2 = 100,
                                                              } else {
        time3 = 150.
        time backup = 300;
                                                      Motor_Backward(backup_speed,backup_speed);
                                                                  Clock Delay1ms(time backup);
   TimedPause(1000);
                                                                  Motor_Stop();
                                                              }
    while(1){
                                                          }
        Data = Reflectance_Read(1000);
        Position =
Reflectance Position(Data);
        Clock Delay1ms(10);
                                                      Bump Module:
        if (Data == 0b111111111 && Position
                                                      #include <stdint.h>
                                                      #include "msp.h"
== 0) { // 2nd t-join}
            Motor Right (2000, 2000);
                                                      void Bump Init(void){
            Clock Delay1ms(1500);
                                                             P4->SEL0 \&= \sim 0 \times ED;
                                                              P4->SEL1 &= \sim 0 \times ED;
            Motor Stop();
                                                              P4->DIR &= ~0xED;
            Motor Forward (4000, 4000);
                                                              P4->REN |= 0xED;
            Clock Delay1ms(500);
                                                              P4->OUT \mid = 0xED;
            Motor_Stop();
        else if (Position >-47 && Position
                                                          uint8_t result =0;
<47) { //center
            Motor Forward(speed, speed);
                                                          result = ~P4->IN & 0xED;
            Clock Delay1ms(time3);
                                                          return result;
            Motor Stop();
            //break;
        } else if (Position <= -47 &&
                                                      Motor Module:
Position > -142) { //slightly off to the
                                                      #include <stdint.h>
                                                      #include "msp.h"
            Motor Left(speed, speed);
                                                      #include "../inc/CortexM.h"
            Clock Delay1ms(time1);
                                                      #include "../inc/PWM.h"
```

```
// -----Motor_Init-----
void Motor Init(void){
   P3 -> SELO &= ~0xC0;
                                                    PWM module:
   P3 -> SEL1 &= \sim 0 \times C0;
                                                    #include "msp.h"
   P3 \rightarrow DIR \mid = 0xC0;
                                                    void PWM Init1(uint16 t period, uint16 t
   P5 -> SEL0 &= \sim 0 \times 30;
   P5 -> SEL1 &= \sim 0 \times 30;
                                                     if(duty >= period) return;  // bad
   P5 -> DIR |= 0x30;
                                                    input
                                                    P2->DIR |= 0x10;
                                                                                    // P2.4
   PWM Init34(15000, 0, 0);
                                                    output
   P3 -> OUT &= ~0xC0;
                                                    P2 - > SEL0 \mid = 0 \times 10;
                                                                                   // P2.4
                                                    TimerOA functions
   return;
                                                     P2->SEL1 &= ~0x10;
                                                                              // P2.4
}
                                                    TimerOA functions
                                                     TIMER A0->CCTL[0] = 0 \times 0080; // CCI0
// ----- Motor Stop-----
                                                    toggle
   P5->OUT &= \sim 0 \times 30;
                                                     TIMER A0->CCR[0] = period; // Period
   P2->OUT &= \sim 0 \times C0; // off
P3->OUT |= 0 \times C0; // low current sleep
                                                    is 2*period*8*83.33ns is 1.333*period
                                                    TIMER A0->EX0 = 0 \times 0000; //
                                                    divide by 1
   PWM Init34(15000, 0, 0);
                                                    TIMER_A0 - > CCTL[1] = 0 \times 0040; // CCR1
   return;
                                                    toggle/reset
                                                     TIMER A0->CCR[1] = duty; // CCR1
                                                    duty cycle is duty1/period
// -----Motor Forward-----
                                                    TIMER A0->CTL = 0 \times 0230;
   P3 \rightarrow OUT |= 0xC0; // nSleep = 1
                                                    SMCLK=12MHz, divide by 1, up-down mode
   P5 -> OUT &= \sim 0 \times 30; // PH = 0
   PWM Duty3(rightDuty);
                                                    void PWM_Init12(uint16_t period, uint16_t
   PWM Duty4(leftDuty);
                                                    duty1, uint16 t duty2){
   return;
                                                     if(duty1 >= period) return; // bad input
                                                     if(duty2 >= period) return; // bad input
                                                     P2->DIR \mid = 0x30; // P2.4, P2.5
// -----Motor Right-----
                                                   output
void Motor Right(uint16 t leftDuty, uint16 t
                                                    P2->SELO |= 0x30; // P2.4, P2.5
rightDuty) {
                                                   TimerOA functions
   P3 \rightarrow OUT \mid= 0xC0; // nSleep = 1
                                                    P2->SEL1 &= ~0x30; // P2.4, P2.5
   P5 -> OUT &= \sim 0 \times 10; // P5.4 PH = 0
                                                   TimerOA functions
   P5 -> OUT \mid = 0x20; // P5.5 PH = 1
                                                    TIMER A0->CCTL[0] = 0 \times 0080; // CCI0
   PWM Duty4(leftDuty);
   PWM Duty3(rightDuty);
                                                     TIMER_A0->CCR[0] = period;  // Period
                                                    is 2*period*8*83.33ns is 1.333*period
                                                     TIMER_AO -> EXO = OxOOOO; //
// -----Motor Left-----
                                                   divide by 1
void Motor_Left(uint16_t leftDuty, uint16_t
                                                    TIMER\_A0 -> CCTL[1] = 0 \times 0040;
                                                                                     // CCR1
rightDuty){
                                                   toggle/reset
   P3 -> OUT |= 0xC0; // nSleep = 1
                                                     TIMER A0->CCR[1] = duty1;
                                                                                      // CCR1
   P5 -> OUT |= 0x10; // P5.4 PH = 1
                                                  duty cycle is duty1/period
   P5 -> OUT &= \sim 0 \times 20; // P5.5 PH = 0
                                                    TIMER A0->CCTL[2] = 0 \times 0040;
                                                                                      // CCR2
   PWM Duty4(leftDuty);
                                                   toggle/reset
   PWM Duty3(rightDuty);
                                                                                     // CCR2
                                                     TIMER A0->CCR[2] = duty2;
                                                   duty cycle is duty2/period
                                                    TIMER A0->CTL = 0 \times 02F0;
// -----Motor_Backward-----
                                                    SMCLK=12MHz, divide by 8, up-down mode
void Motor_Backward(uint16_t leftDuty,
uint16_t rightDuty){
   P3 \rightarrow OUT |= 0xC0; // nSleep = 1
                                                   void PWM Duty1(uint16_t duty1) {
   P5 -> OUT |= 0x30; // PH = 1
                                                     if(duty1 >= TIMER A0->CCR[0]) return; //
   PWM Duty3 (rightDuty);
                                                    bad input
   PWM Duty4(leftDuty);
```

return;

```
TIMER A0->CCR[1] = duty1;
                                   // CCR1
duty cycle is duty1/period
                                                           P5->SEL0 &= \sim 0 \times 08; //P5.3 set to GPIO
                                                           P5->SEL1 &= \sim 0 \times 08; //P5.3 set to GPIO
                                                           P5->DIR \mid= 0x08; //P5.3 set to output
void PWM Duty2(uint16 t duty2) {
                                                           P5->OUT &= \sim 0 \times 08; //P5.3 set to low
 if(duty2 >= TIMER A0->CCR[0]) return; //
bad input
                                                           P9->SEL0 &= \sim 0 \times 04;
 TIMER A0->CCR[2] = duty2;
                                    // CCR2
                                                           P9->SEL1 &= ~0x04;
duty cycle is duty2/period
                                                           P9->DIR |= 0x04;
                                                           P9->OUT &= \sim 0 \times 04;
void PWM Init34 (uint16 t period, uint16 t
                                                           P7->SEL0 &= \sim 0 \times FF; //P7.0 set to GPIO
duty3, uint16 t duty4) {
                                                           P7->SEL1 &= \sim 0 \times FF; //P7.0 set to GPIO
    if(duty3 >= period) return; // bad input
                                                           //P7->DIR &= ~0xFF; //P7.0 set to input
    if(duty4 >= period) return; // bad input
    P2->DIR \mid = 0xC0;
                              // P2.6, P2.7
output
   P2->SEL0 \mid = 0xC0;
                              // P2.6, P2.7
                                                      uint8 t Reflectance Read(uint32 t time) {
TimerOA functions
   P2->SEL1 \&= \sim 0 \times C0;
                              // P2.6, P2.7
                                                           uint8 t result;
TimerOA functions
                                                           //P5->SEL0 &= ~0x08;
   TIMER A0->CCTL[0] = 0 \times 0080;
                                     // CCI0
                                                           //P5->SEL1 &= ~0x08;
togale
                                                           //P5->DIR \mid = 0x08; //set 5.3 as output
   TIMER A0->CCR[0] = period;
                                                           P5->OUT |= 0x08; //set 5.3 as high
Period is 2*period*8*83.33ns is 1.333*period
                                                           //P9->DIR |= 0x04; //set 9.2 as output
  TIMER A0->EX0 = 0 \times 00000;
                                                           P9->OUT \mid= 0x04; //set 9.2 as high
                                 //
divide by 1
                                                           //P7->SELO &= ~0xFF;
   TIMER A0->CCTL[3] = 0 \times 0040;
                                     // CCR3
                                                           //P7->SEL1 &= ~0xFF;
toggle/reset
   TIMER A0->CCR[3] = duty3;
                                      // CCR3
                                                           //Clock Delaylus(10);
duty cycle is duty3/period
                                                           P7->DIR \mid = 0xFF; //set P7.7-P7.0 as
   TIMER A0->CCTL[4] = 0 \times 0040;
                                      // CCR4
                                                       output
toggle/reset
                                                           P7->OUT \mid= 0xFF; //set P7.7-P7.0 as high
  TIMER A0->CCR[4] = duty4;
                                     // CCR4
                                                           Clock Delaylus(10);
duty cycle is duty4/period
                                                           P7->DIR = 0x00; //set P7.7-P7.0 as input
   TIMER A0->CTL = 0 \times 02F0;
SMCLK=12MHz, divide by 8, up-down mode
                                                           Clock Delaylus(time);
                                                           result = (P7->IN \& OxFF);
void PWM Duty3(uint16 t duty3) {
                                                           P5->OUT &= \sim 0 \times 08; //turn off IR LED
   if(duty3 >= TIMER A0->CCR[0]) return; //
                                                           P9->OUT &= \sim 0 \times 04; //turn off IR LED
bad input
                                                           return ~result;
   TIMER A0->CCR[3] = duty3;
                                      // CCR3
duty cycle is duty3/period
                                                       int32 t Reflectance Position(uint8 t data) {
void PWM Duty4(uint16 t duty4) {
                                                           if(duty4 >= TIMER A0->CCR[0]) return; //
                                                       142, 237, 332};
                                                           uint8_t Mask[8] = \{1, 2, 4, 8, 16, 32,
bad input
   TIMER A0->CCR[4] = duty4;
                                      // CCR4
                                                       64, 128};
duty cycle is duty4/period
                                                           int sum, count, result;
                                                           sum = 0;
                                                           count = 0;
Reflectance Module:
                                                           result = 0;
#include <stdint.h>
                                                           for (int i = 0; i < 8; i++) {
#include "msp432.h"
                                                               if(data & Mask[i]){
#include "..\inc\Clock.h"
                                                                   count++;
                                                                   sum = sum + W[i];
void Reflectance_Init(void){
                                                               }
   // write this as part of Lab 6
    //Clock Init48MHz();
                                                           }
```

```
return 333;
                                                   (NVIC->IP[2]&0xFF00FFFF) | 0x00400000; //
   else{
                                                      NVIC - > ISER[0] = 0x00000400; // enable
      result = sum / count;
                                                   interrupt 10 in NVIC
                                                      TIMER A1->CTL \mid= 0x0014;
                                                                                    // reset
       return result;
                                                   and start Timer Al in up mode
   }
TimerA1 Module:
                                                   // -----TimerA1 Stop-----
#include <stdint.h>
                                                   // Deactivate the interrupt running a user
#include "msp.h"
                                                   task periodically.
                                                   // Input: none
void (*TimerAlTask)(void); // user
                                                   // Output: none
function
                                                   void TimerA1 Stop(void) {
void TimerAl Init(void(*task)(void),
                                                    // write this as part of Lab 13
uint16_t period){
                                                      TIMER A1->CTL &= \sim 0 \times 0030;
                                                                                      // halt
   TimerAlTask = task;
                                  // user
                                                   Timer A1
function
                                                      NVIC -> ICER[0] = 0x00000400;
   TIMER A1->CTL &= \sim 0 \times 0030; // halt
                                                   disable interrupt 10 in NVIC
   // bits15-10=XXXXXX, reserved
   // bits9-8=10, clock source to
SMCLK
                                                   void TA1_0_IRQHandler(void){
   // bits7-6=10, input clock divider
                                                    // write this as part of Lab 13
                                                      TIMER A1->CCTL[0] &= ~0x0001; //
                     stop mode
   // bits5-4=00,
                                                   acknowledge capture/compare interrupt 0
   // bit3=X,
                       reserved
                                                                                   // execute
                                                      (*TimerAlTask)();
   // bit2=0,
                       set this bit to
                                                   user task
clear
   // bit1=0,
                      no interrupt on
timer
                                                   Bump Interrupt Module:
   TIMER A1->CTL = 0 \times 0280;
                                                   void bump interrupt(void) {
   // bits15-14=00, no capture mode
   // bits13-12=XX,
                      capture/compare
                                                       uint8 t bumpResult = Bump Read();
input select
                                                       if (bumpResult != 0x00) {
   // bit11=X,
                      synchronize capture
source
                                                           int backup speed = 2500, backup time
   // bit10=X,
                      synchronized
                                                   = 300:
capture/compare input
   // bit9=X,
                    reserved compare mode
                                                   Motor Backward(backup speed, backup speed);
   // bit8=0,
                                                           Clock Delay1ms (backup time);
   // bits7-5=XXX,
                      output mode
                                                           Motor Stop();
   // bit4=1,
                      enable
                                                           int turn speed = 3000, turn time =
capture/compare interrupt on CCIFG
                                                   900:
   // bit3=X,
                      read
capture/compare input from here
                                                           Motor Right (turn speed, turn speed);
   // bit2=0, output this value
                                                           Clock Delay1ms(turn time);
in output mode 0
                                                           Motor_Stop();
   // bit1=X,
                      capture overflow
status
                                                           Motor_Forward(4000,4000);
   // bit0=0,
                                                           Clock Delay1ms(500);
                       clear
capture/compare interrupt pending
                                                           Motor Stop();
   TIMER A1->CCTL[0] = 0 \times 0010;
   TIMER A1->CCR[0] = (period - 1); //
compare match value
                                                      return;
   TIMER\_A1 -> EXO = 0x0005;
                           // configure
for input clock divider /6
 // interrupts enabled in the main program
after all devices initialized
```

 $NVIC \rightarrow IP[2] =$

if(data == 0){