

P Flag question

One of your friends has come to you with following K-Map that he developed after carefully going through a Truth Table to represent input-output relationships of a <u>real world</u> problem with the aim of designing a combinational circuit.

		(CD	
7-	0	1	0	0
AB	0	0	0	0
A	1	1	0	0
	1	0	0	0

A. Write the minterm numbers that will be in the Boolean equation represented by the K-Map above. (Ex: use m1 to represent minterm 1)

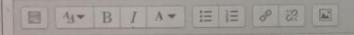
B. After carefully studying the problem again, you found out that it is not a problem even <u>minterms</u> 4, 5, and 9/being 0 or 1. By taking your <u>new findings into consideration</u>, simplify above K-map. Write the simplified Boolean equation in <u>SoP</u> form. (Write ABCD as AB'CD' in your answer. <u>Don't keep spaces between letters</u>)

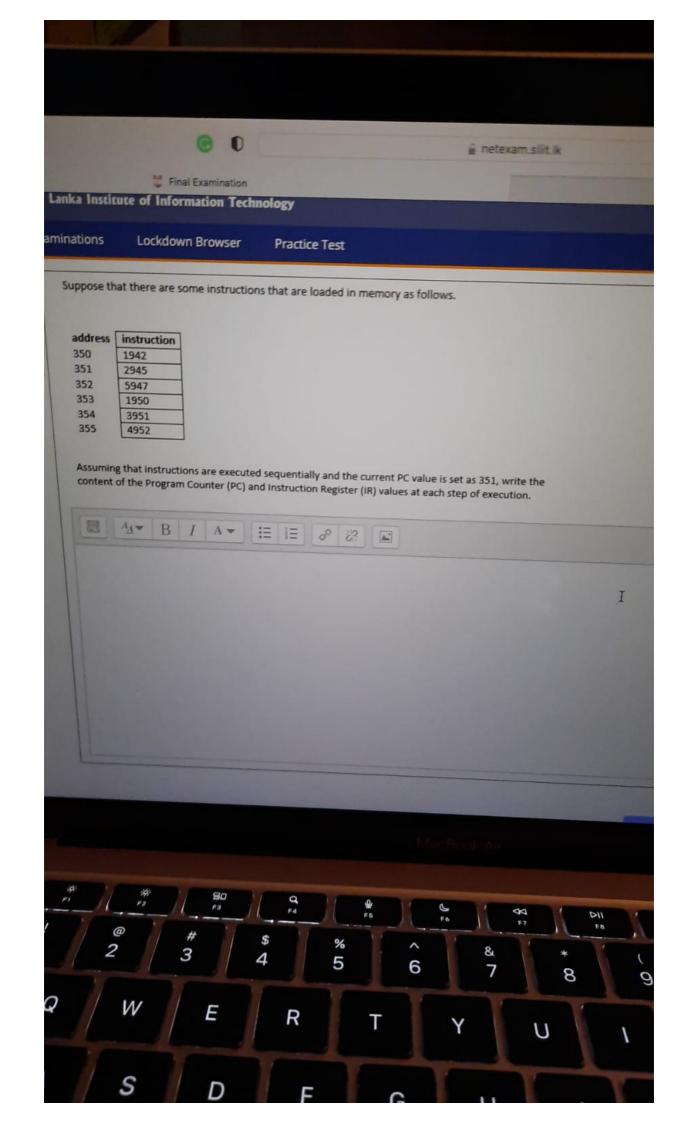
28

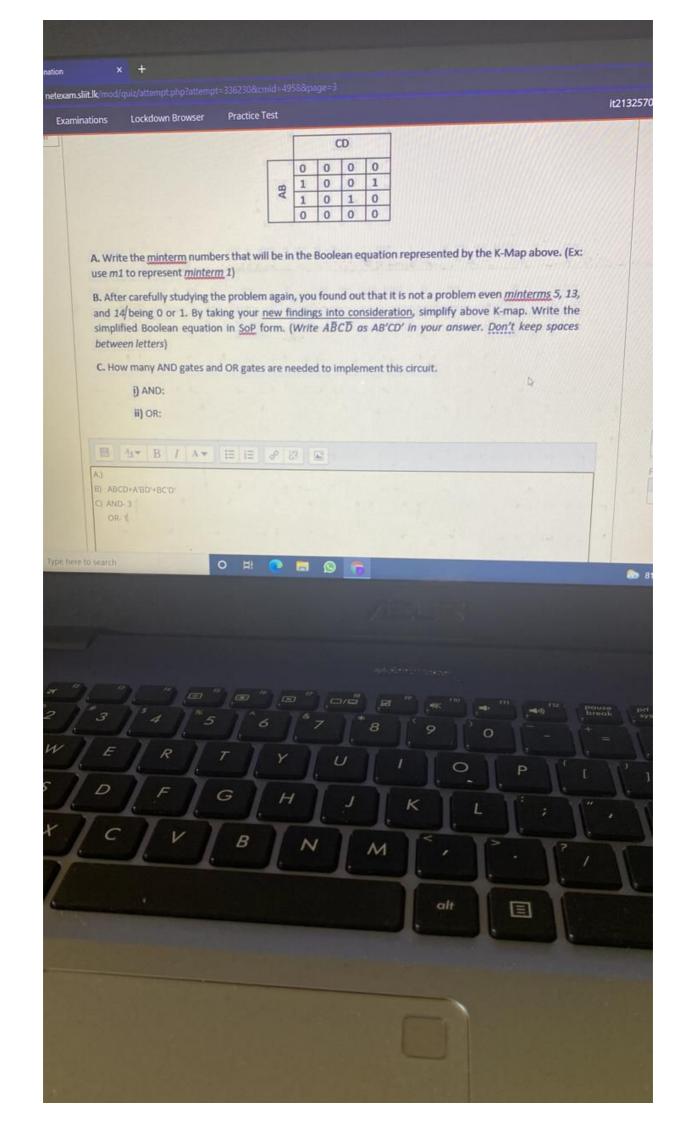
C. How many AND gates and OR gates are needed to implement this circuit.

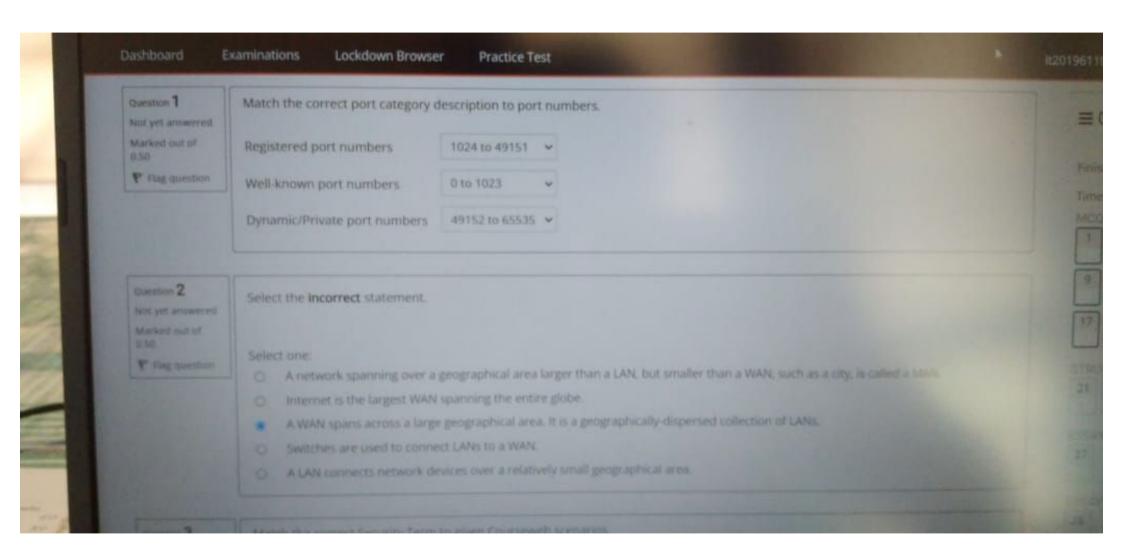
i) AND:

ii) OR:









29

inswered

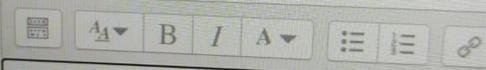
out of

question

Suppose that there are some instructions that are lo

address	instruction
350	1942
351	2945
352	5947
353	1950
354	3951
355	4952

Assuming that instructions are executed sequentially are content of the Program Counter (PC) and Instruction Re



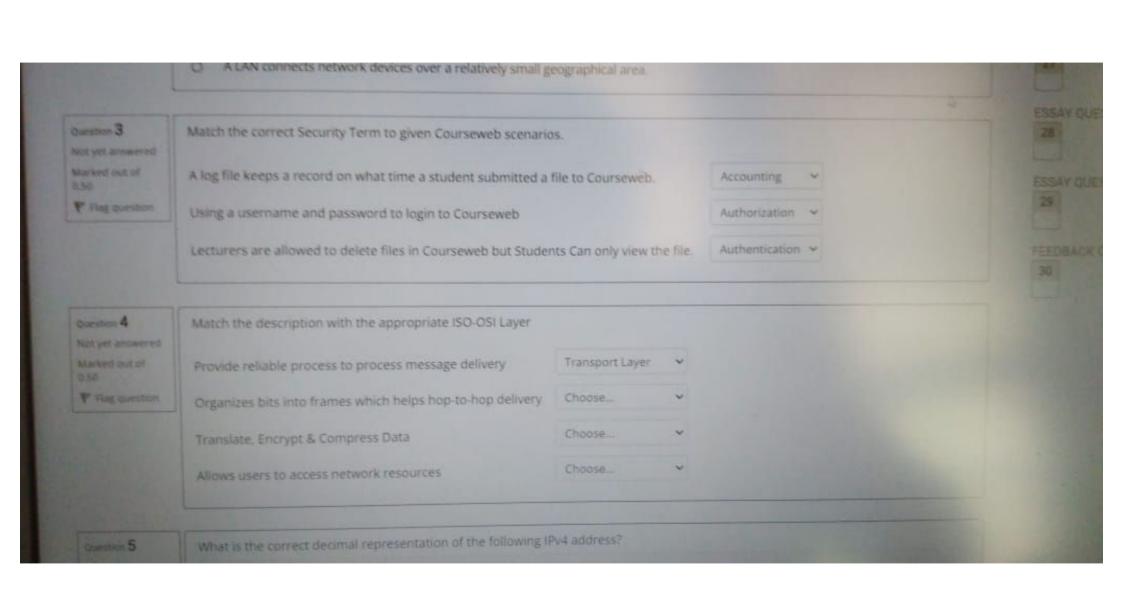
PC-351 IR-1942

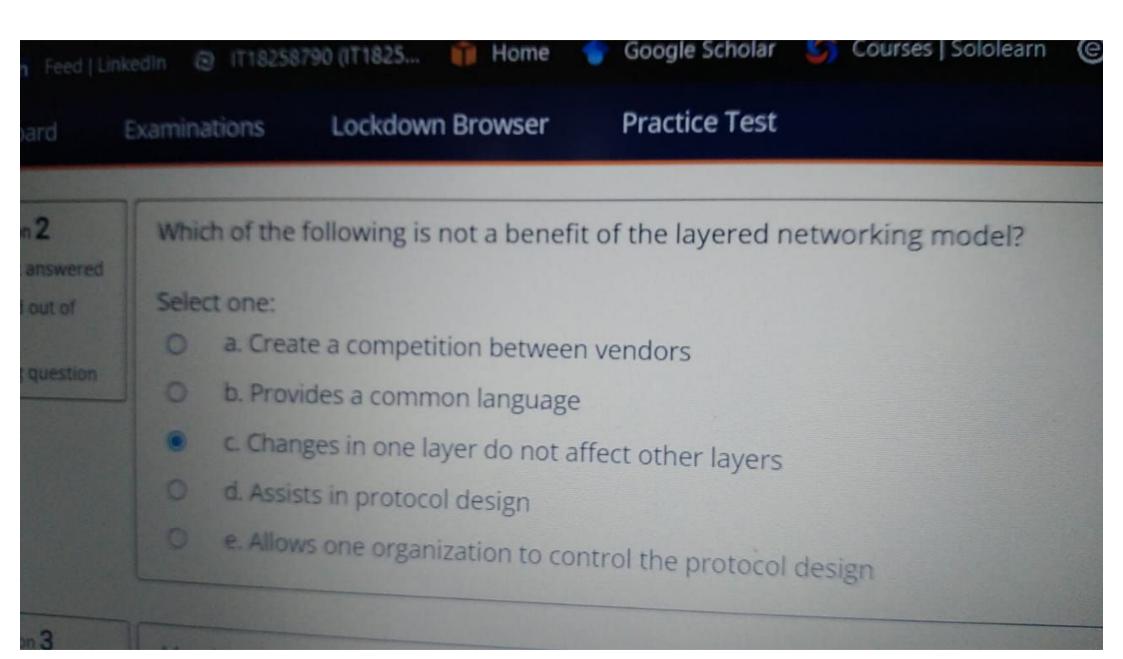
PC-352 IR-2945

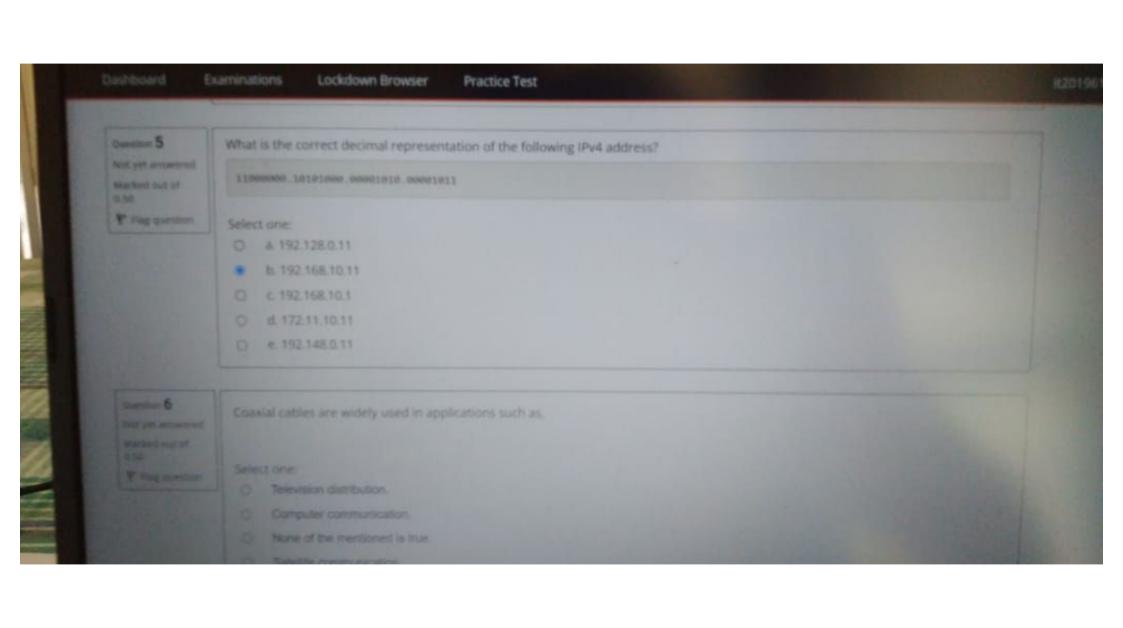
PC-353 IR-5947

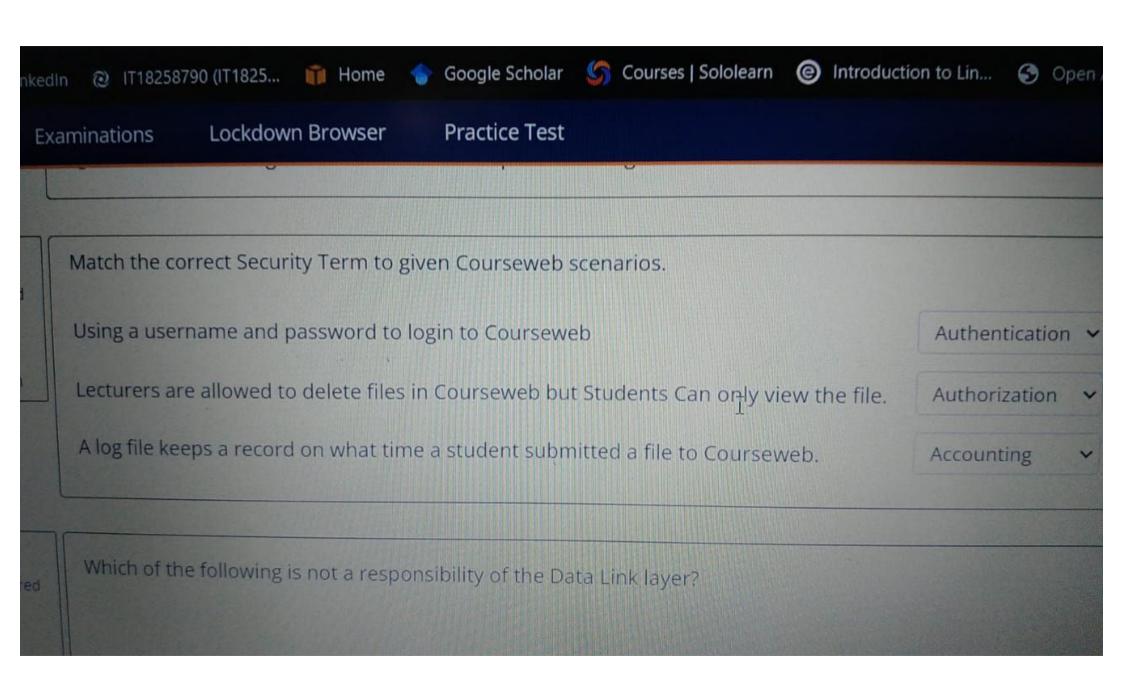
PC-354 IR-1950

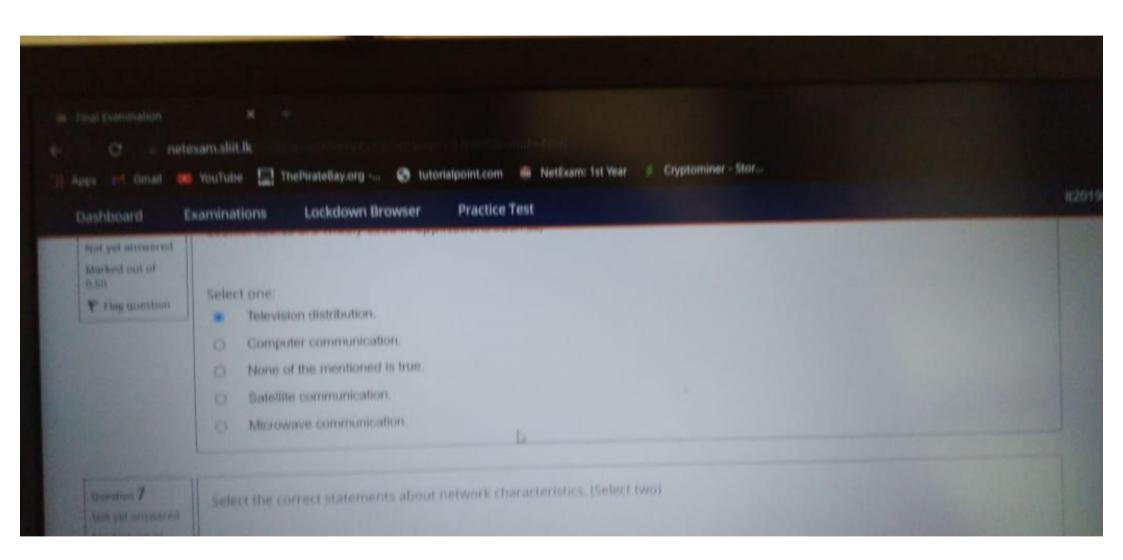
PC-355 IR-3951











uestion 4 ot yet answered tarked out of 50

Flag question

Which of the following is not a responsibility of the Data Link layer?

Select one:

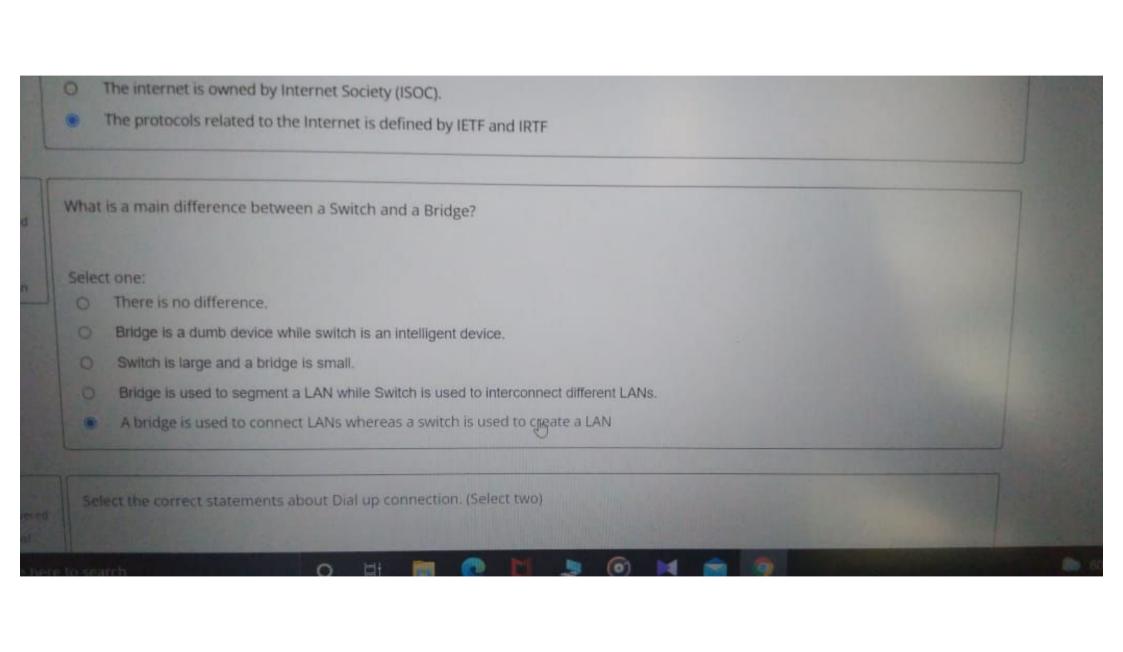
- a. Access control
- O b. Routing
- O c. Framing Ethernet frame
- d. Physical addressing (MAC address)
- O e. Flow control

Question 5 Not yet answered

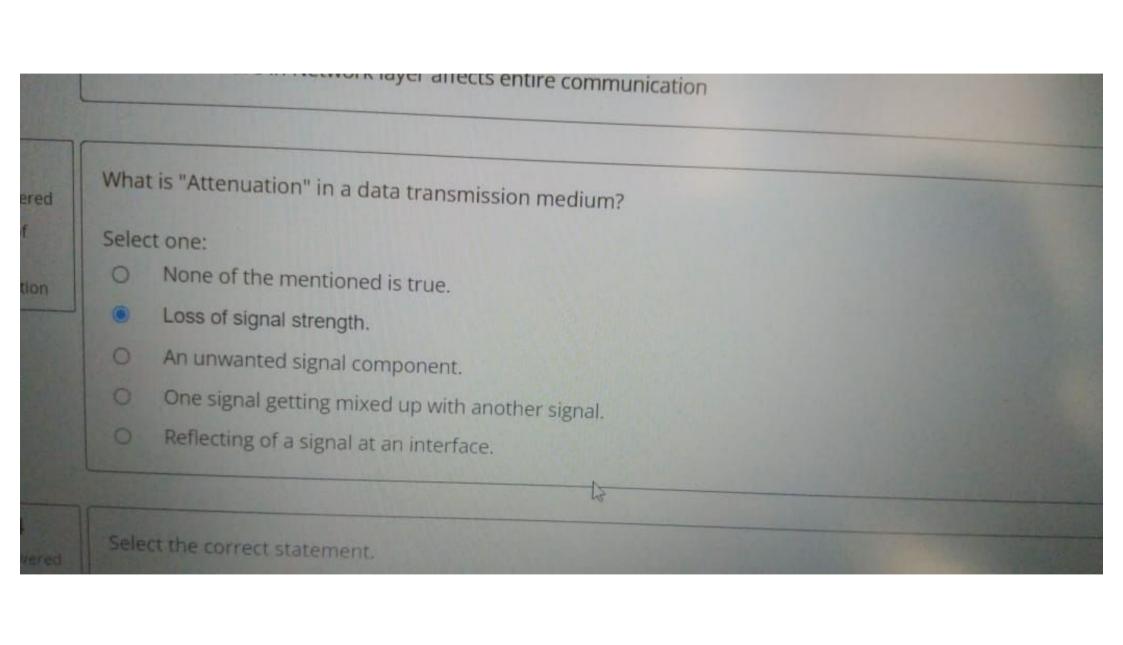
Marked out of 0.50 What is the correct decimal representation of the following IPv4 address?

11000000.10101000.00001010.00001011

	One signal getting mixed up with another signal. Loss of signal strength.
Question 7 Not yet answered Marked out of	What are the correct statements about "MAC address table"? (select two)
P Flag question	Select one or more: MAC address table is used by switches None of the given answers are true MAC address table is used by routers MAC address table is a collection of MAC addresses with the respective connected ports MAC address table is created for each computer
Question 8 Not yet answered Marked out of 0.50	Which of the following statements describe the network shown in the graphic? (Select two)



Select the statement which is not true. ered Select one: stion The internet architecture standards are defined by Internet Architecture Board. The internet is a public network. The internet is defined as a global mesh of interconnected networks. The internet is owned by Internet Society (ISOC). The protocols related to the Internet is defined by IETF and IRTF What is a main difference between a Switch and a Bridge? Select one: There is no difference.



What is the correct statement about a Broadcast Domain? Select one: A collection of two or more computers in which, when one sender sends a message, it is received by one receiver in the same domain. A collection of two or more computers in which, when one sender sends a message, it is received by a group of computers in domain. A collection of two or more computers in which, when one sender sends a message, it is received by all the others in the same domains.	ender sends a message, it is received by a group of computers in the same
 A collection of two or more computers in which, when one sender sends a message, it is received by one receiver in the sandomain. A collection of two or more computers in which, when one sender sends a message, it is received by all the others in the sandomain. A collection of two or more computers in which, when one sender sends a message, it is received by all the others in the sandomain. A collection of two or more computers in which, when one sender sends a message, it is received by all the others in a different sends a message, it is received by all the others in a different sends a message. 	ender sends a message, it is received by a group of computers in the same
A collection of two or more computers in which, when one sender sends a message, it is received by a group of computers in domain. A collection of two or more computers in which, when one sender sends a message, it is received by all the others in the sand of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which, when one sender sends a message, it is received by all the others in a different configuration of two or more computers in which is a different configuration of two or more computers in which it is a different configuration of two or more computers in which it is a different configuration of two or more computers in which	ender sends a message, it is received by a group of computers in the same
A collection of two or more computers in which, when one sender sends a message, it is received by all the others in the same of two or more computers in which, when one sender sends a message, it is received by all the others in a different computers in which, when one sender sends a message, it is received by all the others in a different computers.	ender sends a message, it is received by all the others in the same domain
A collection of two or more computers in which, when one sender sends a message, it is received by all the others in a different	
	ender sends a message. It is received by all the others in a different
	and the second of the second of a different
None of the mentioned is true.	
Which of the following statements is correct?	

Question 18

Not yet answered

Marked out of

0.50

P Flag question

ADSL is the abbreviation of,

Select one:

- None of the mentioned is true.
- Asymmetric Digital Subscriber Line
- Asymmetric Dual System Line
- Asymmetric Dual Subscriber Line
- Asymmetric Digital System Line

Question 19

Not yet answered

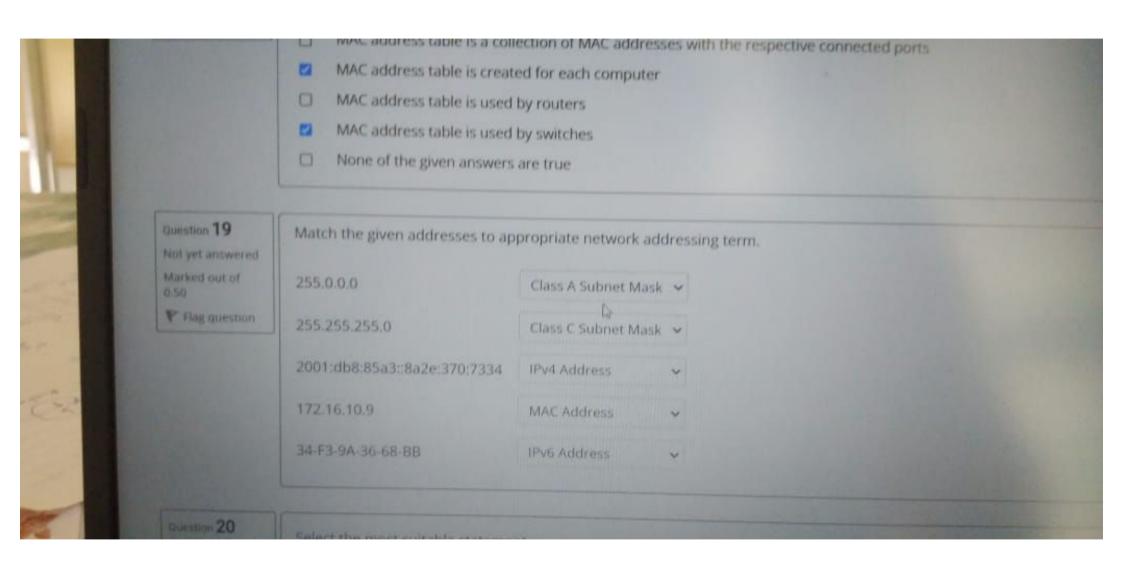
Marked out of 0.50

P Flag question

What is not an advantage of a computer network?

Select one:

- User Communication
- O Social Engineering
- O Interaction among cooperative application programs
- O Remote Information Access
- O Resource Sharing



Marked out of 0.50

P Flag question

Select one:

- O 10 Gigabit Ethernet
- Token ring
- Virtual Private Network
- Ethernet
- Fast Ethernet

Question 12

Not yet answered

Marked out of 0.50

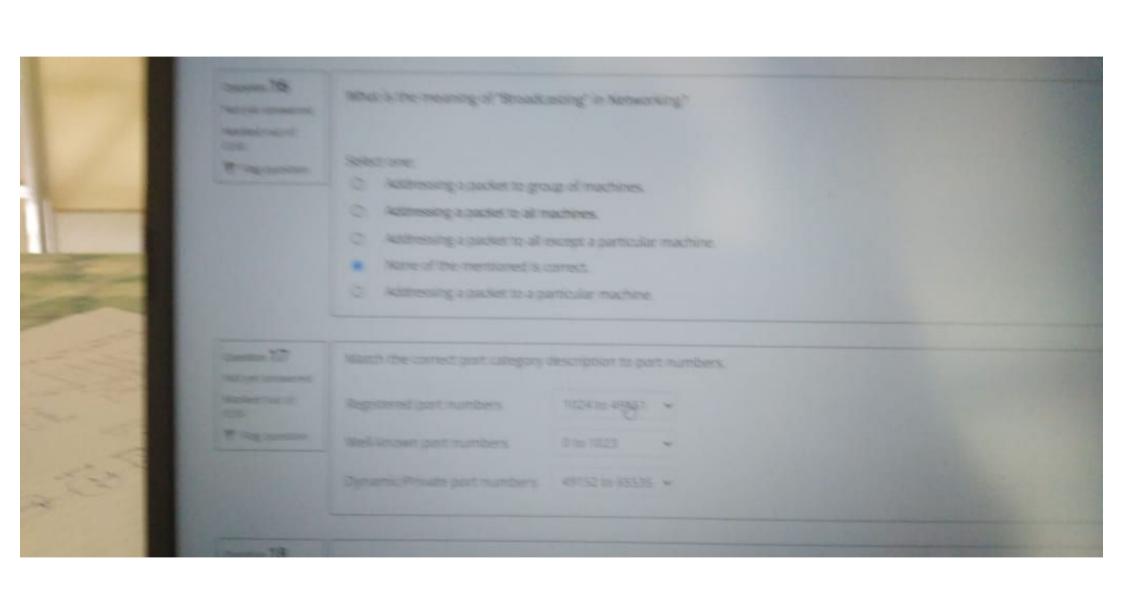
P Flag question

Select the correct elements/components that make up a network.

Select one:

- O Service, Medium, Packet, Rules.
- Service, Medium, Packet, Protocols.
- Device, Wire, Message, Rules.
- Device, Medium, Message, Rules.
- O Device, Medium, Message, Protocols.

Dynamic/Private port numbers 49152 to 65535 V Question 18 What are the correct statements about "MAC address table"? (select two) Not yet answered Marked out of Select one or more: P Flag question MAC address table is a collection of MAC addresses with the respective connected ports MAC address table is created for each computer MAC address table is used by routers MAC address table is used by switches None of the given answers are true Question 19 Match the given addresses to appropriate network addressing term. Class A Subnet Mask >



Question 23

Not yet answered

Marked out of 3.00

P Flag question

For the given IP address find the following.

IP address: - 172,18,10,1

- . 18 Network address: 172
- Subnet mask: 255 . 255 . 255
- Broadcast address: 172 . 18 . 10
- Last usable IP address: 172 . 18 . 10 . 255

Question 24

Not yet answered

Marked out of 2.00

P Flag question

What is the subnet mask of the following IP address?

IP address: - 192.168.105.4

Write your answer in dotted decimal form

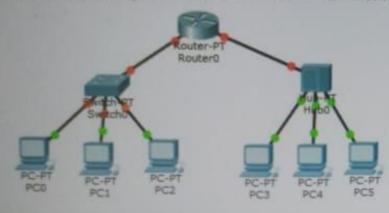
Question 15

Not yet answered

Marked out of

P Flag question

Which of the following statements describe the network shown in the graphic? (Select two)



Select one or more:

- There are total number of seven collision domains in the network.
- There are total number of four collision domains in the network.
- There are total number of two broadcast domains in the network.
- There are total number of five collision domains in the network.
- There are total number of four broadcast domains in the network,
- There are total number of six broadcast domains in the network.

wered of

estion

IP address: - 192.168.105.4

Write your answer in dotted decimal format (e.g. 192.168.10.1)

What is the subnet mask of the following IP address?

Answer: 255.255.255.0

Wered it of

estion

Complete the missing parts of the binary form of following IPv4 address:

IPv4 Address: 173.18.10.253

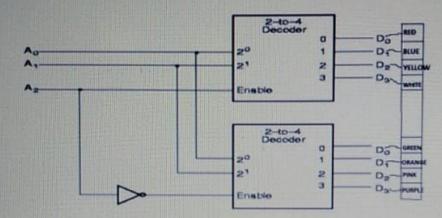
- IPv4 Address in Binary: 10101101 . 00010010 . 00001010 . 11111101
- IP Address Class: B

Question **27**Not yet answered

Marked out of 10.00

P Flag question

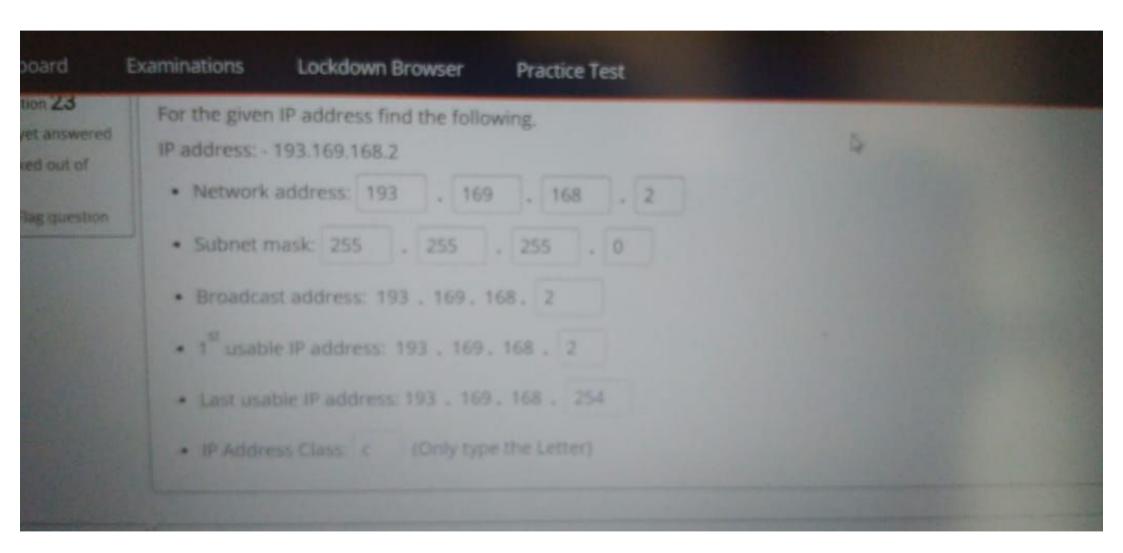
Following is the block diagram of a Combinational circuit which is made up of two 2-4 Decoders. Assume that each of the output lines of a Decoder is connected to a colored LED bulb as given in the diagram.



The functionality of the 2-4 decoder is given below.

Inputs		Outputs				
A2	A1	AO	D0	D1	D2	D3
1	0	0	1	0	0	0
1	1	0	0	1	0	0
1	0	1	0	0	1	0
1	1	1	0	0	0	1
0	X	X	0	0	0	0

- a. What are the input values that must be provided for A2, A1, A0 respectively, to have the ORANGE light ON?
- b. What are the input values that must be provided for A2, A1, A0 respectively, to have the RED light ON?
- c. If you need to have both the BLUE and ORANGE lights ON, what is the modification you need to do to the above circuit?



29

inswered

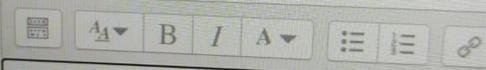
out of

question

Suppose that there are some instructions that are lo

address	instruction
350	1942
351	2945
352	5947
353	1950
354	3951
355	4952

Assuming that instructions are executed sequentially are content of the Program Counter (PC) and Instruction Re



PC-351 IR-1942

PC-352 IR-2945

PC-353 IR-5947

PC-354 IR-1950

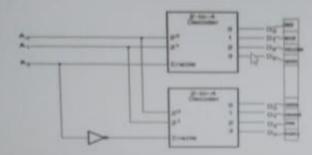
PC-355 IR-3951

Not yet answered
Marked out of

F Hag question

Following is the block diagram of a Combinational circuit which is made up of two 2-4 Decoders.

Assume that each of the output lines of a Decoder is connected to a colored LED bulb as given in the diagram.



The functionality of the 2-4 decoder is given below.

Inputs			Outputs			
A2	A1	AD	DO	D1	D2	D3
1	0	0	1	0	0	0
1	1	0	0	1	0	0
1	0	1	0	0	1	0
1	1	1	0	0	0	1
0	×	×	0	0	0	-0

- a. What are the input values that must be provided for A2, A1, A0 respectively, to have the BLUE light ON?
- b. What are the input values that must be provided for A2, A1, A0 respectively, to have the GREEN light ON?
- e. If you need to have both the RED and GREEN lights ON, what is the modification you need to do to the above circuit?

Finish attempt
Time left 0:28-5
MCQ QUESTIO
1 2 3
9 10 11

THE BIAT BE FOR

Question 29

Not yet answered

Marked out of 10.00

P Flag question

Suppose that there are some instructions (16 bits) are loaded in memory as follows. The memory addresses are given in hexadecimal values and the current PC value is set as 351.

address	instruction
350	1942
351	2945
352	5947
353	1950
354	3951

Most significant four bits indicate the opcode and other bits represent the operand reference. What are the memory addresses of above instructions pointing at to fetch instruction/data ()?

B A B / A F E E & Z E

