odle

NetExam

Sri Lanka Institute of Information Technology

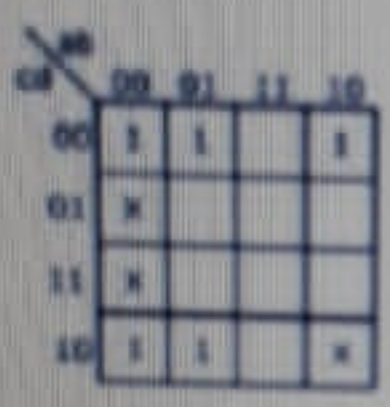
on 4

et answered

ed out of 1

ag question

Select the simplified boolean expression of the function g



Select one:

- a. a'b'+b'd'+a'bd'
- b. a'b'+b'd'+a'b'd'
- c. b'd'+a'bd'
- d. b'd'+a'd'
- e. a'b'+b'd'+a'd'

Which rule is incorrect related to K-Maps?

Select one:

- a. Can consider 1 for grouping
- b. Wrapping is possible for don't care conditions
- c. Grouping of 1s considering power of 2's
- d. Diagonal grouping is not possible
- e. Overlapping is impossible

NetExam

Sri Lanka Institute of Information Technology

Select the simplified boolean expression of the function given by the K-Map

cd \ ab	00	01	11	10
00	1	X	X	1
01	X			1
11				
10	1			X

Select one:

- a. b'c'+ab'c'd
- b. bd'+b'c'
- c. b'd'+b'c'
- d. b'd'
- e. b'd'+b'c'+c'd'

$\overline{b'c'}$

ASCII code for character "h" is 104. Character "h" is transmitted over a network. Parity scheme used by the sender is Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11010000
- b. 01101000
- c. 11101000
- d. 11010001
- e. 11010011

dle

NetExam

Sri Lanka Institute of Information Technology

5

answered

out of 1

question

Select the correct simplification of the function given by the K-Map

YZ \ WX	00	01	11	10
00	0	X	0	X
01	X	1	X	1
11	0	X	1	0
10	0	1	X	0

Select one:

- a. XY+Y'Z
- b. W'X+Y'Z
- c. XZ+Y
- d. W'X+Y'Z+XY
- e. X'Z+Y

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question **3**

Not yet answered

Marked out of 1

Flag question

Write the simplified answer of the following K-Map?

cd \ ab	00	01	11	10
00				
01		1	1	
11		1	1	1
10			1	1

Select one:

- a. b'd+ac
- b. bd+ac
- c. bd+ac'
- d. bd'+ac
- e. bd+a'c

Identify the most suitable answer related to Address Bus?

Select one:

- a. Carries instructions
- b. Control and timing information
- c. Carry data and instructions
- d. Identify the source and destination of data
- e. Carries data

NetExam

Sri Lanka Institute of Information Technology

What is the functionality of PC register?

Select one:

- a. Contains the address of the next instruction to be fetched
- b. Contains the address of the last instruction to be fetched
- c. Instruction most frequently fetched
- d. Contains the address of the first instruction to be fetched
- e. Contains a word of data to be written to memory or the word most recently re

Question **31**

Not yet answered

Marked out of 1

Flag question

A combinational circuit that adds two Boolean variable at a time

Select one:

- a. Shifter
- b. Full adder
- c. Comparator
- d. Quarter adder
- e. Half adder

NetExam

Sri Lanka Institute of Information Technology

ered

f 1

tion

Which one of the following consists of a sequence of operations performs by CPU?

Select one:

- a. Execute Cycle
- b. Instruction Cycle
- c. Fetch Cycle
- d. Decoding
- e. OS

Question 23

Not yet answered

Marked out of 1

Flag question

What is the operating system service that belongs to memory management?

Select one:

- a. Allocating memory for incoming processes
- b. Creating and deleting files and directories
- c. Creating user and system processes
- d. Deadlock handling
- e. Allocate disk space for programs

Next page

red

1

on

What is the register that uses to store the memory addresses?

Select one:

- a. ALU
- b. PC
- c. Stack
- d. AX
- e. MAR

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question **16**

Not yet answered

Marked out of 1

Flag question

Which of the following register is used to keep the memory address of the next instruction to be executed is located?

Select one:

- a. Memory Data Register
- b. Program Status Register
- c. Memory Address Register
- d. Program Counter
- e. Instruction Register

Next page

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question 22

Not yet answered

Marked out of 1

Flag question

Built-in set of machine code instruction of computer (processor) are called:

Select one:

- a. Code set
- b. Instruction set
- c. Object code
- d. Source code
- e. Logical operations

What is the maximum memory size that can be addressed directly in a computer which has a processor with 24 bit memory addresses?

Select one:

- a. 2 MB
- b. 4 GB
- c. 16 KB
- d. 16 MB
- e. 8 MB

Finish attempt

NetExam

Sri Lanka Institute of Information Technology

How many AND gates and OR gates you need to draw a PLA for the following two expressions.

F1-A'B'C'+ABC+AB'C'

F2-A'B'C+ABC+AB'C'

Select one:

- a. 4 AND gates and 4 OR gates
- b. 4 AND gates and 1 OR gate
- c. 4 AND gates and 2 OR gates
- d. 6 AND gates and 2 OR gates
- e. 6 AND gates and 1OR gate

stion 23
yet answered
rked out of 1
Flag question

A bit that shows the current mode of the operating system is called;

Select one:

- a. Overflow bit
- b. System bit
- c. Mode bit
- d. Kernel bit
- e. User bit

NetExam

Sri Lanka Institute of Information Technology

Process is a:

Select one:

- a. passive entity
- b. contents of main memory
- c. program in execution
- d. job in secondary memory
- e. program in High Level language kept on disk

vered
of 1
stion

In instruction format, data or address of any data location is said to be:

Select one:

- a. Op code
- b. Function code
- c. Instruction code
- d. Logical code
- e. Operand

ion 29

et answered

ed out of 1

ag question

Operating System is,

Select one:

- a. Combination of Hardware and Software
- b. A Interface Card
- c. A Cable
- d. Hardware Component
- e. Software Program

NetExam

Sri Lanka Institute of Information Technology

Built-in set of machine code instruction of computer (processor) are called:

Select one:

- a. Logical operations
- b. Instruction set
- c. Code set
- d. Object code
- e. Source code

A three input NOR gate gives logic high output only when

Select one:

- a. Two inputs are high
- b. two input are low
- c. one input is high
- d. one input is low
- e. all input are low

How many AND gates and OR gates you need to draw a PLA for the following two expressions.

F1-A'B'C'+ABC+AB'C'

F2-A'B'C+ABC+AB'C'

Select one:

- a. 4 AND gates and 1 OR gate
- b. 6 AND gates and 1OR gate
- c. 4 AND gates and 2 OR gates
- d. 4 AND gates and 4 OR gates
- e. 6 AND gates and 2 OR gates

29
swered
t of 1
uestion

What is not a component of a process management?

Select one:

- a. Process Communication
- b. Deletion of Processes
- c. Backup of files on stable storage media
- d. Suspend processes
- e. Resume Processes

red
f 1
ion

When a computer is first turned on or restarted, a special type of absolute loader called _____ is executed.

Select one:

- a. Boot Loader Program
- b. Relegation Loader Program
- c. Basic OS Program
- d. Bootstrap Program

What is the functionality of PC register?

Select one:

- a. Contains a word of data to be written to memory or the word most recently read
- b. Instruction most frequently fetched
- c. Contains the address of the next instruction to be fetched
- d. Contains the address of the first instruction to be fetched
- e. Contains the address of the last instruction to be fetched

NetExam

Sri Lanka Institute of Information Technology

How many AND gates and OR gates you need to draw a PLA for the following two expressions.

F1-A'B'+AB+AB'

F2-A'B'+AB+A'B

Select one:

- a. 6 AND gates and 1 OR gate
- b. 6 AND gates and 2 OR gates
- c. 4 AND gates and 2 OR gates
- d. 4 AND gates and 4 OR gate
- e. 4 AND gates and 1 OR gate

ered
f 1
tion

What is the register that uses to store the memory addresses?

Select one:

- a. AX
- b. MAR
- c. Stack
- d. ALU
- e. PC

red
1
ion

What is the incorrect statement related to 4 bit parallel adder?

Select one:

- a. Carry out of one adder is inserted as carry in of next adder
- b. Combination of half adder and full adders
- c. It is a sequential circuit
- d. Produces the arithmetic sum in parallel
- e. Consist with 4 adders

NetExam

Sri Lanka Institute of Information Technology

red

1

ion

What is not a service of Operating System?

Select one:

- a. Memory Management
- b. Information Management
- c. Process Management
- d. Disk Management
- e. Program Management

NetExam

Sri Lanka Institute of Information Technology

ASCII code for character "n" is 1101110. Character "n" is transmitted over a network. Parity scheme used by the sender is Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11011101
- b. 11101010
- c. 11101110
- d. 11011100
- e. 01101110

Ne

NetExam

Sri Lanka Institute of Information Technology

This is **not** a task performed by an operating system:

Select one:

- a. Efficiently manage the computer hardware
- b. Coordinate the use of hardware among applications
- c. Performs a specific function for an end user
- d. Provide a convenient user interface
- e. Execute user programs

NetExam

Sri Lanka Institute of Information Technology

Find the **incorrect** statement about BIOS program:

Select one:

- a. BIOS handles requests comming from application software
- b. BIOS is loaded at power up or reboot
- c. BIOS is typically stored in an EPROM
- d. BIOS initializes all aspects of system
- e. BIOS comes in a factory fitted firmware

NetExam

Sri Lanka Institute of Information Technology

A bit that shows the current mode of the operating system is called:

Select one:

- a. Mode bit
- b. System bit
- c. User bit
- d. Kernel bit
- e. Overflow bit

The Final Carry out of the full adder represented by

Select one:

- a. By merging two intermediate carry out results using and XOR gate
- b. By merging two intermediate carry out results using and OR gate
- c. By merging two intermediate carry out results using and NOR gate
- d. By merging two intermediate carry out results using and AND gate
- e. By merging two intermediate carry out results using and XNOR gate

NetExam

Sri Lanka Institute of Information Technology

4

swered

t of 1

estion

8X1 mux consist with

Select one:

- a. 8 inputs and 1 output
- b. 2^8 inputs and 1 outputs
- c. 1 inputs and 8 output
- d. 8^2 inputs and 1 outputs
- e. 8 inputs and 4 output

Question 32
Not yet answered
Marked out of 1
Flag question

A combinational circuit that adds two Boolean variable at a time

Select one:

- a. Full adder
- b. Quarter adder
- c. Comparator
- d. Shifter
- e. Half adder

NetExam

Sri Lanka Institute of Information Technology

This is **not** a task performed by an operating system:

Select one:

- a. Coordinate the use of hardware among applications
- b. Execute user programs
- c. Efficiently manage the computer hardware
- d. Performs a specific function for an end user
- e. Provide a convenient user interface

NetExam

Sri Lanka Institute of Information Techno

Question **34**

Not yet answered

Marked out of 1

Flag question

4X1 mux consist with

Select one:

- a. 1 inputs and 4 output
- b. 4^2 inputs and 1 outputs
- c. 4 inputs and 2 output
- d. 4 inputs and 1 output
- e. 2^4 inputs and 1 outputs

NetExam

Sri Lanka Institute of Information Technology

How many AND gates and OR gates you need to draw a PLA for the following two expressions.

F1-A'B'+AB+AB'

F2-A'B'+AB+A'B

4

Select one:

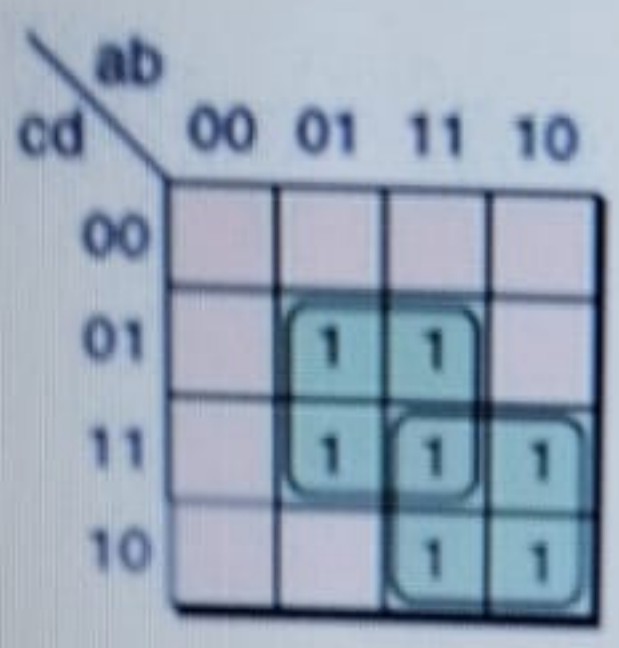
- a. 4 AND gates and 1 OR gate
- b. 6 AND gates and 1 OR gate
- c. 4 AND gates and 4 OR gate
- d. 6 AND gates and 2 OR gates
- e. 4 AND gates and 2 OR gates

What is the control unit's main fuction in the CPU?

Select one:

- a. To transfer data to primary storage
- b. To bring data from I/O devices to memory
- c. To perform arithmatic and logic operations
- d. To decode program instruction
- e. To store program instruction in memory

Write the simplified answer of the following K-Map?



Select one:

- a. b'd+ac
- b. bd+ac
- c. bd+ac'
- d. bd+a'c
- e. bd'+ac

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question 2

Not yet answered

Marked out of 1

Flag question

Write the simplified answer of the following K-Map?

cd \ ab	00	01	11	10
00		1	1	
01		1	1	
11		1		
10		1		

Select one:

- a. a'b+b'c'
- b. a'b'+bc
- c. a'b+bc'
- d. ab+b'c
- e. ab+bc'

NetExam

Sri Lanka Institute of Information Technology

When a computer is first turned on or restarted, a special type of absolute loader called _____ is executed.

Select one:

- a. Boot Loader Program
- b. Relegation Loader Program
- c. Basic OS Program
- d. Bootstrap Program

Identify the most suitable answer related to Address Bus?

Select one:

- a. Control and timing information
- b. Carry data and instructions
- c. Identify the source and destination of data
- d. Carries data
- e. Carries instructions

NetExam

Sri Lanka Institute of Information Technology

wered

t of 1

estion

What are one time passwords?

Select one:

- a. Password which consist with one word
- b. Unique password is required every time user tries to login into the system
- c. Password which change hourly
- d. Same password is required every time user tries to login into the system
- e. Password which change hourly

Question **30**

Not yet answered

Marked out of 1

Flag question

_____ is built directly on the hardware.

Select one:

- a. User environment
- b. Computer environment
- c. Application system
- d. Database system
- e. Operating system

Next page

NetExam

Sri Lanka Institute of Information Technology

What is the operating system service that belongs to memory management?

Select one:

- a. Allocating memory for incoming processes
- b. Creating user and system processes
- c. Allocate disk space for programs
- d. Deadlock handling
- e. Creating and deleting files and directories

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question 20

Not yet answered

Marked out of 1

Flag question

The instructions like MOV and ADD are called as:

Select one:

- a. Commands
- b. Byte code
- c. Op code
- d. Operators
- e. Mnemonics

NetExam

Sri Lanka Institute of Information Technology

1

swered

t of 1

uestion

Select the **incorrect** feature of a CISC system:

Select one:

- a. Transistors used for handling complex instructions
- b. Low cycles per second, large code sizes
- c. High cycles per second, small code sizes
- d. Includes multi-clock complex instructions
- e. Emphasis on hardware

ASCII code for character "d" is 100 (i.e. hundrad). Character "d" is transmitted over a network. Parity scheme used by the sender is Odd Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11010011
- b. 11010000
- c. 11010001
- d. 11101000
- e. 01100100

Next page

NetExam

Sri Lanka Institute of Information Technology

11

nswered

out of 1

question

What is the register that uses to store the memory addresses?

Select one:

- a. AX
- b. Stack
- c. PC
- d. MAR
- e. ALU

NetExam

Sri Lanka Institute of Information Technology

ASCII code for character "h" is 104. Character "h" is transmitted over a network. P
sender is Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is
the network?

Select one:

- a. 11010011
- b. 11010000
- c. 01101000
- d. 11101000
- e. 11010001

NetExam

Sri Lanka Institute of Information Technology

Built-in set of machine code instruction of computer (processor) are called:

Select one:

- a. Logical operations
- b. Source code
- c. Instruction set
- d. Object code
- e. Code set

Tools

nand gates

logical opera

Inputs			outputs
W	X	Y	Z = W. X. Y
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

Universal gates-NOR Gate

electronicshub.org

	Output
C	Y
0	1
1	0
0	0
1	0
0	0
1	0

Projection123.com

Question **40**
Not yet answered
Marked out of 1
Flag question

What is the maximum memory size that can be addressed directly in a computer which has a processor with 8 bit memory addresses?

Select one:

- a. 32 KB
- b. 4 GB
- c. 32 Bytes
- d. 256 Bytes
- e. 256 KB

NetExam

Sri Lanka Institute of Information Technology

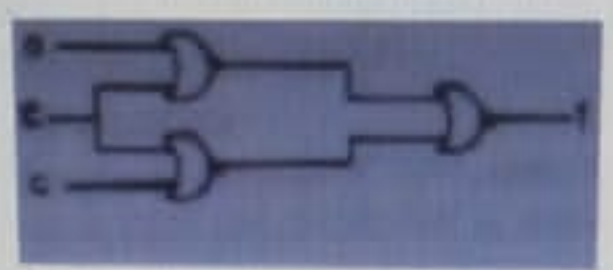
Select the correct order in execution.

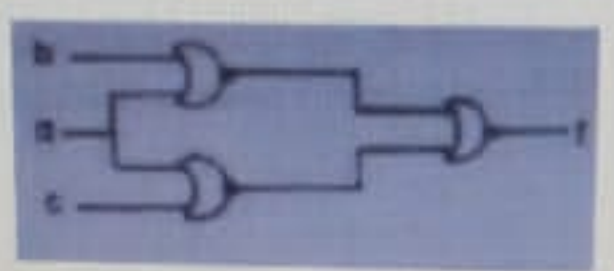
1. CPU checks to see whether the next instruction it requires is in cache
2. If not, the CPU has to fetch next instruction from main memory
3. If it is, then the instruction is fetched from the cache
4. Cache fetches data from next to current addresses in main memory

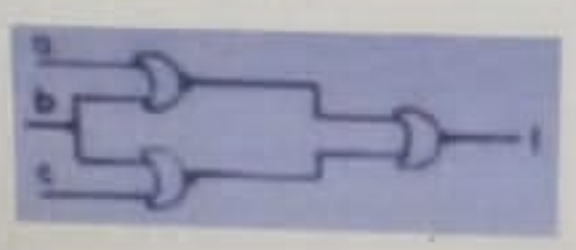
Select one:

- a. D, A, C, B
- b. C, D, B, A
- c. B, A, C, D
- d. D, C, A, B
- e. B, C, A, D

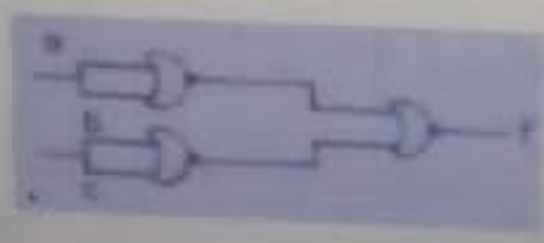
Select one:

a. 

b. 

c. 

d.

e. 

ASCII code for character "n" is 1101110. Character "n" is transmitted over a network. Parity scheme used by the sender is Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11101110
- b. 11011100
- c. 11100010
- d. 11011101
- e. 01101110

Next page

NetExam

Sri Lanka Institute of Information Technology

What is embedded system?

Select one:

- a. The computer which is the part of a big computer
- b. The programme which is the permanent part of the computer
- c. The computer and software system that control the machine
- d. None of the above
- e. The programme which arrives by being wrapped in box

NetExam

Sri Lanka Institute of Information Technology

A three input NOR gate gives logic high output only when

Select one:

- a. one input is low
- b. two input are low
- c. one input is high
- d. all input are low
- e. Two inputs are high

ed
1
on

ASCII code for character "p" is 112. Character "p" is transmitted over a network. Parity scheme used by the sender is Odd Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 01110000
- b. 11110000
- c. 11100000
- d. 01111000
- e. 11100001

Next page

What is the correct answer about Don't care conditions of K-Maps

A. Don't care conditions cannot be considered for simplification

B. Don't care conditions can be taken either all 1s or all 0's

C. 0s or 1s can be assigned as and when required to improve simplification

Select one:

- a. A and B are correct
- b. Only C is correct
- c. Only A is correct
- d. Only B is correct
- e. B and C are correct

NetExam

Sri Lanka Institute of Information Technology

12

Question

Select the register that automatically increments the value sequentially?

Select one:

- a. IP
- b. BX
- c. AX
- d. SP
- e. MAR

Select the **incorrect** statement.

Select one:

- a. High-level programming languages introduced with 3rd generation computers.
- b. 4th generation computes use silicon chip called microprocessors.
- c. ENIAC is the first electronic general-purpose calculator.
- d. First generation computers used Thermion valves.

NetExam

Sri Lanka Institute of Information Technology

What is the functionality of PC register?

Select one:

- a. Instruction most frequently fetched
- b. Contains the address of the last instruction to be fetched
- c. Contains the address of the first instruction to be fetched
- d. Contains the address of the next instruction to be fetched
- e. Contains a word of data to be written to memory or the word most re

NetExam

Sri Lanka Institute of Information Technology

Select the correct order in execution.

1. CPU checks to see whether the next instruction it requires is in cache
2. If not, the CPU has to fetch next instruction from main memory
3. If it is, then the instruction is fetched from the cache
4. Cache fetches data from next to current addresses in main memory

Select one:

- a. C, D, B, A
- b. B, C, A, D
- c. D, C, A, B
- d. B, A, C, D
- e. D, A, C, B

ASCII code for character "n" is 1101110. Character "n" is transmitted over a network. Parity scheme used by the sender is Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11101110
- b. 11011100
- c. 11101010
- d. 11011101
- e. 01101110

Next page

ASCII code for character "p" is 112. Character "p" is transmitted over a network. Parity scheme used by the sender is Odd Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11100000
- b. 01110000
- c. 11110000
- d. 01111000
- e. 11100001

Next page

on 3
t answered
ed out of 1
ag question

Which rule is incorrect related to K-Maps?

Select one:

- a. Can consider 1 for grouping
- b. Overlapping is impossible
- c. Wrapping is possible for don't care conditions
- d. Grouping of 1s considering power of 2's
- e. Diagonal grouping is not possible

NetExam

Sri Lanka Institute of Information Technology

Select the simplified boolean expression of the function given by K-Map

Select one:

- a. b'd'+a'bd'
- b. b'd'+a'd'
- c. a'b'+b'd'+a'bd'
- d. a'b'+b'd'+a'b'd'
- e. a'b'+b'd'+a'd'

NetExam

Sri Lanka Institute of Information Technology

_____ is the process of carrying out commands.

Select one:

- a. Executing
- b. Fetching
- c. Decoding
- d. Buffering
- e. Storing

Question **3**

Not yet answered

Marked out of 1

Flag question

What is the correct answer about K-Maps

A. K-Map does not use any property of Boolean algebra

B. Each cell in a K-Map represents a Maxterm

C. 1's in adjacent cells cancels a variable in simplification of K-Maps

Select one:

- a. Only A is correct
- b. B and C are incorrect
- c. A and B are incorrect
- d. B and C are correct
- e. A and C are incorrect

NetExam

Sri Lanka Institute of Information Technology

11

nswered

out of 1

question

What is the register that uses to store the memory addresses?

Select one:

- a. MAR
- b. Stack
- c. AX
- d. PC
- e. ALU

ASCII code for character "n" is 1101110. Character "n" is transmitted over a network. Parity scheme used by the sender is Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

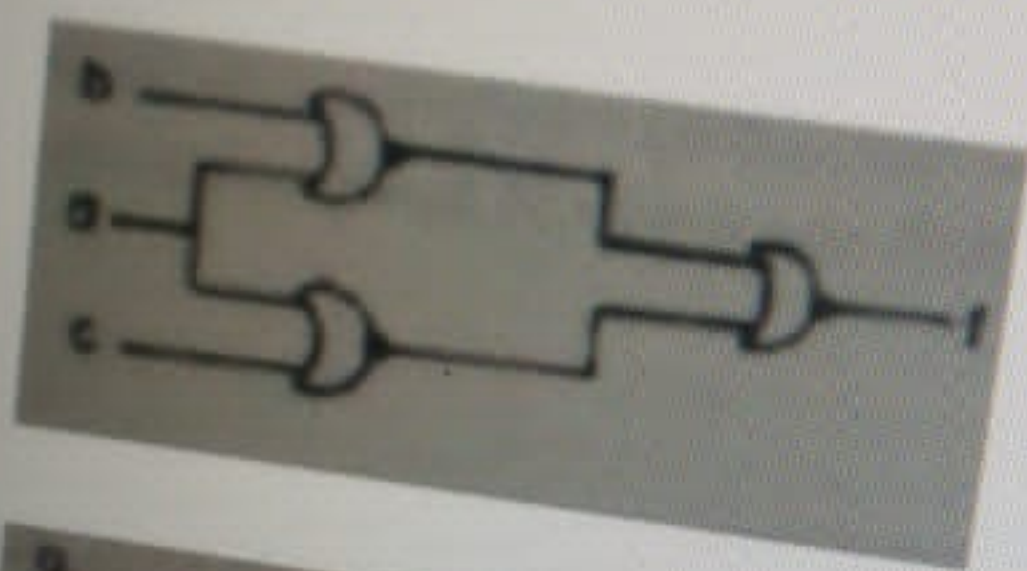
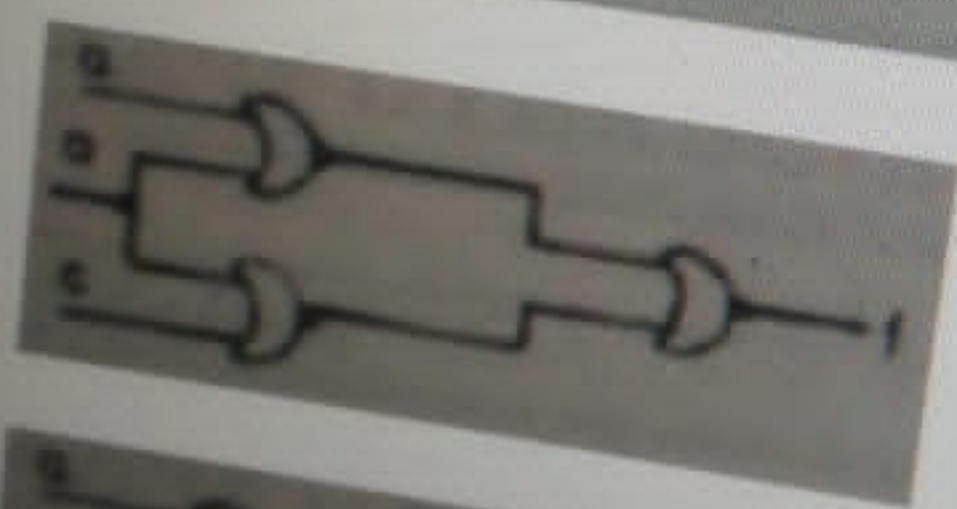
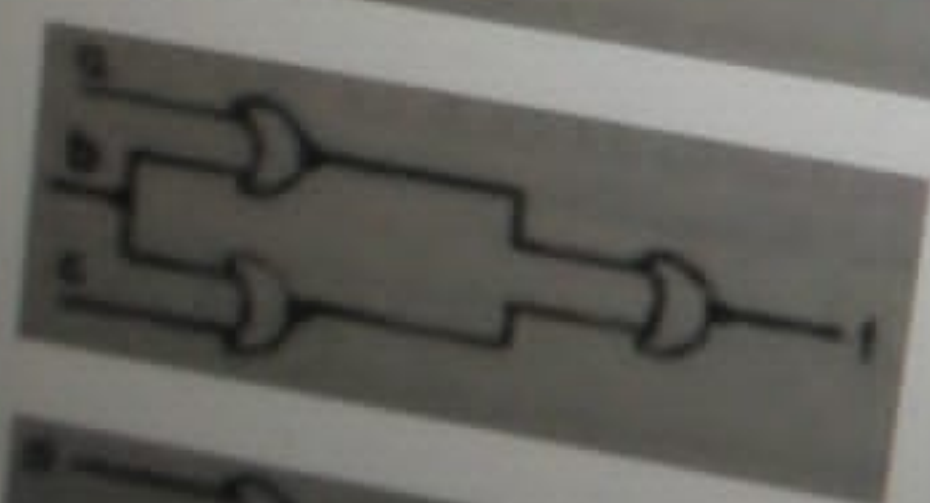
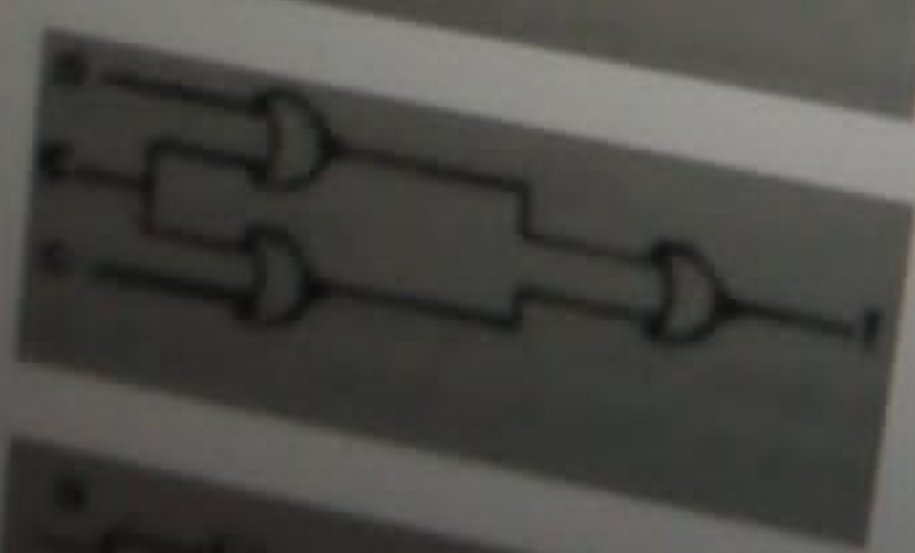
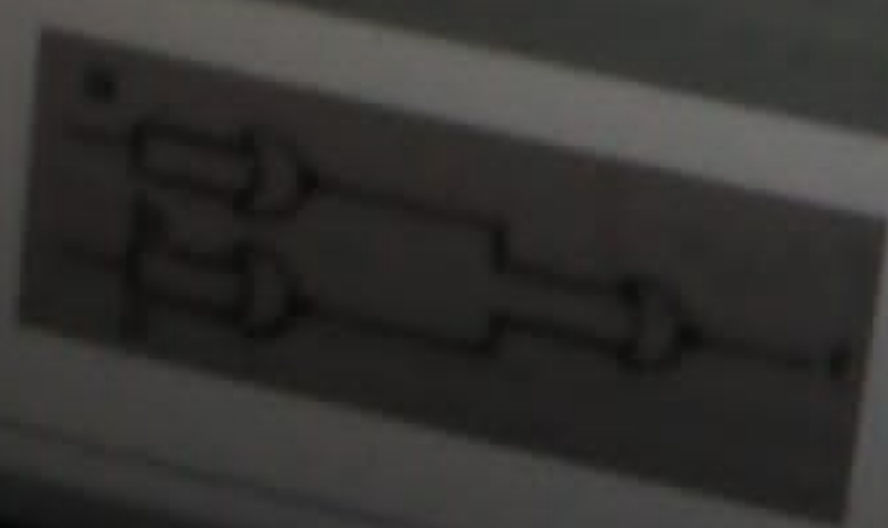
Select one:

- a. 11101110
- b. 11101010
- c. 01101110
- d. 11011100
- e. 11011101

uestion

c \ ab	00	01	11	10
0	0	0	1	1
1	0	1	1	1

Select one:

- a. 
- b. 
- c. 
- d. 
- e. 

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question 3

Not yet answered

Marked out of 1

Flag question

Write the simplified answer of the following K-Map?

cd \ ab	00	01	11	10
00				
01		1	1	
11		1	1	1
10			1	1

Select one:

- a. bd'+ac
- b. bd+a'c
- c. bd+ac'
- d. bd+ac
- e. b'd+ac

Question **4**

Not yet answered

Marked out of 1

Flag question

Select the simplified boolean expression of the function given by the K-Map

cd \ ab	00	01	11	10
00	1	X	X	1
01	X			1
11				
10	1			X

Select one:

- a. bd'+b'c'
- b. b'd'
- c. b'c'+ab'c'd
- d. b'd'+b'c'+c'd'
- e. b'd'+b'c'

Question 1

Not yet answered

Marked out of 1

Flag question

Write the simplified answer of the following K-Map?

cd \ ab	00	01	11	10
00				
01		1	1	
11		1	1	1
10			1	1

Select one:

- a. bd'+ac
- b. b'd+ac
- c. bd+ac'
- d. bd+ac
- e. bd+a'c

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question 1

Not yet answered

Marked out of 1

Flag question

Which rule is incorrect related to K-Maps?

Select one:

- a. Grouping of 1s considering power of 2's
- b. Diagonal grouping is not possible
- c. Can consider 1 for grouping
- d. Wrapping is possible for don't care conditions
- e. Overlapping is impossible

AMD
FreeSync
Premium

Question 1
Not yet answered
Marked out of 1
Flag question

Write the simplified answer of the following K-Map?

A \ BC	00	01	11	10
0	1	1	0	0
1	1	1	0	0

Select one:

- a. A'B'
- b. AB
- c. B'
- d. C
- e. B'C

Select the correct simplification of the function given by the K-Ma

YZ \ WX	00	01	11	10
00	0	X	0	X
01	X	1	X	1
11	0	X	1	0
10	0	1	X	0

Select one:

- a. XY+Y'Z
- b. XZ+Y
- c. W'X+Y'Z+XY
- d. W'X+Y'Z
- e. X'Z+Y

1
answered
out of 1
question

What is the correct answer about Don't care conditions of K-Maps

A. Don't care conditions cannot be considered for simplification

B. Don't care conditions can be taken either all 1s or all 0's

C. 0s or 1s can be assigned as and when required to improve simplification

Select one:

- a. B and C are correct
- b. Only B is correct
- c. Only C is correct
- d. Only A is correct
- e. A and B are correct

Marked out of 1

Flag question

cd \ ab	00	01	11	10
00				
01		1	1	
11		1	1	1
10			1	1

Select one:

- a. bd+ac'
- b. b'd+ac
- c. bd+ac
- d. bd'+ac
- e. bd+a'c

dle

NetExam

Sri Lanka Institute of Information Technology

2

answered

out of 1

question

Write the simplified answer of the following K-Map?

cd \ ab	00	01	11	10
00		1	1	
01		1	1	
11		1		
10		1		

Select one:

- a. ab+bc'
- b. a'b+b'c'
- c. a'b'+bc
- d. ab+b'c
- e. a'b+bc'

NetExam

Sri Lanka Institute of Information Technology

tion 1
et answered
ed out of 1
lag question

What is the correct answer about K-Maps

A. K-Map does not use any property of Boolean algebra

B. Each cell in a K-Map represents a Maxterm

C. 1's in adjacent cells cancels a variable in simplification of K-Maps

Select one:

- a. B and C are correct
- b. Only A is correct
- c. B and C are incorrect
- d. A and C are incorrect
- e. A and B are incorrect

Select the correct order in execution.

1. CPU checks to see whether the next instruction it requires is in cache
2. If not, the CPU has to fetch next instruction from main memory
3. If it is, then the instruction is fetched from the cache
4. Cache fetches data from next to current addresses in main memory

Select one:

- a. D, A, C, B
- b. B, C, A, D
- c. C, D, B, A
- d. B, A, C, D
- e. D, C, A, B

Question 22

Not yet answered

Marked out of 1

Flag question

In instruction format, data or address of any data location is said to be:

Select one:

- a. Logical code
- b. Instruction code
- c. Operand
- d. Function code
- e. Op code

3
wered
of 1
estion

When a computer is first turned on or restarted, a special type of absolute load

Select one:

- a. Basic OS Program
- b. Relegation Loader Program
- c. Bootstrap Program
- d. Boot Loader Program

Write the simplified answer of the following K-

A \ BC	00	01	11	10
0	1	1	0	0
1	1	1	0	0

Select one:

- a. C
- b. A'B'
- c. AB
- d. B'C
- e. B'

NetExam

Sri Lanka Institute of Information Technology

What is the correct answer about K-Maps

A. K-Map does not use any property of Boolean algebra

B. Each cell in a K-Map represents a Maxterm

C. 1's in adjacent cells cancels a variable in simplification of K-Ma

Select one:

- a. A and B are incorrect
- b. Only A is correct
- c. B and C are correct
- d. A and C are incorrect
- e. B and C are incorrect

What is meant by a Bus in computer Architecture?

Select one:

- a. Device which perform arithmetic and logical operations
- b. Combination of internal and external memory
- c. Combination of odd and even parity
- d. A communication pathway connecting two or more devices
- e. Combination of static and dynamic memory

NetExam

Sri Lanka Institute of Information Technology

Which one of the following consists of a sequence of operations performs by CPU?

Select one:

- a. Fetch Cycle
- b. Execute Cycle
- c. Instruction Cycle
- d. OS
- e. Decoding

NetExam

Sri Lanka Institute of Information Technology

Question 16

Not yet answered

Marked out of 1

Flag question

_____ register keeps tracks of the instructions stored in program stored in memory.

Select one:

- a. MAR (Memory Address Register)
- b. PC (Program Counter)
- c. MBR (Memory Buffer Register)
- d. IR (Instruction Register)
- e. AC (Accumulator)

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question 28

Not yet answered

Marked out of 1

Flag question

Select the **incorrect** statement about interrupts.

Select one:

- a. Interrupt transfers control to the interrupt service routine
- b. Interrupts can process later by the operating system.
- c. Interrupt architecture must save the address of the interrupted instruction
- d. An operating system is interrupt driven
- e. A trap or exception is a software-generated interrupt

NetExam

Sri Lanka Institute of Information Technology

What is the register that uses to store the memory addresses?

Select one:

- a. PC
- b. ALU
- c. MAR
- d. Stack
- e. AX

NetExam

Sri Lanka Institute of Information Technology

Identify the most suitable answer related to Address Bus?

Select one:

- a. Carries data
- b. Identify the source and destination of data
- c. Carries instructions
- d. Carry data and instructions
- e. Control and timing information

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question **23**

Not yet answered

Marked out of 1

Flag question

Environment for execution of programs is provided by:

Select one:

- a. Microprocessor
- b. Operating system
- c. Inputs
- d. Memory
- e. Outputs

dle

NetExam

Sri Lanka Institute of Information Technology

n 1

t answered

d out of 1

g question

Write the simplified answer of the following K-Map?

A \ BC	00	01	11	10
0	1	1	0	0
1	1	1	0	0

Select one:

- a. B'
- b. B'C
- c. A'B'
- d. AB
- e. C

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question 1

Not yet answered

Marked out of 1

Flag question

Write the simplified answer of the following K-Map?

A \ BC	00	01	11	10
0	1	0	0	1
1	1	0	0	1

Select one:

- a. C'
- b. A
- c. AB
- d. A'
- e. A'C'

NetExam

Sri Lanka Institute of Information Technology

What is the correct answer about K-Maps

A. K-Map does not use any property of Boolean algebra

B. Each cell in a K-Map represents a Maxterm

C. 1's in adjacent cells cancels a variable in simplification of K-Maps

Select one:

- a. A and B are incorrect
- b. Only A is correct
- c. B and C are correct
- d. A and C are incorrect
- e. B and C are incorrect

NetExam

Sri Lanka Institute of Information Technology

An I/O controller of a computer system transfers data from I/O devices to:

Select one:

- a. Registers
- b. Cache
- c. Buses
- d. Buffers
- e. Indexes

32
nswered
out of 1
question

A combinational circuit that adds two Boolean variable at a time

Select one:

- a. Half adder
- b. Full adder
- c. Shifter
- d. Quarter adder
- e. Comparator

Which of the following register is used to keep the memory address of the next instruction to be executed is located?

Select one:

- a. Memory Address Register
- b. Memory Data Register
- c. Program Counter
- d. Instruction Register
- e. Program Status Register

Next page

NetExam

Sri Lanka Institute of Information Technology

red

1

on

What is embedded system?

Select one:

- a. The programme which arrives by being wrapped in box
- b. The computer which is the part of a big computer
- c. The computer and software system that control the machine
- d. The programme which is the permanent part of the computer
- e. None of the above

NetExam

Sri Lanka Institute of Information Technology

The average time required to reach a storage location in memory and obtain its contents is called

Select one:

- a. Turnaround time
- b. Latency time
- c. Seek Time
- d. Access time
- e. Response time

NetExam

Sri Lanka Institute of Information Technology

on 17

In instruction format, data or address of any data location is said to be:

Select one:

- a. Logical code
- b. Function code
- c. Instruction code
- d. Operand
- e. Op code

Next page

ASCII code for character "n" is 1101110. Character "n" is transmitted over a network. Parity scheme used by the sender is Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11011101
- b. 11011100
- c. 11101110
- d. 11101010
- e. 01101110

Next page

NetExam

Sri Lanka Institute of Information Technology

red
1·
ion

A combinational circuit that adds two Boolean variable at a time

Select one:

- a. Shifter
- b. Quarter adder
- c. Half adder
- d. Comparator
- e. Full adder

NetExam

Sri Lanka Institute of Information Technology

What is the wrong statement of the first generation computers

Select one:

- a. It had high processing power and processed millions of instructions a second
- b. Needed a several people to operate it
- c. Used Vacuum tubes and vales
- d. Cost was very high
- e. Size was big and consumes high electricity

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question 22

Not yet answered

Marked out of 1

Flag question

Which of the following register is used to keep the address

Select one:

- a. Accumulator
- b. Instruction Register
- c. Memory Address Register
- d. Memory Buffer Register
- e. Program Counter

NetExam

Sri Lanka Institute of Information Technology

Identify the most suitable answer related to Data Bus?

Select one:

- a. Carries data
- b. Control and timing information
- c. Identify the source and destination of data
- d. Carries instructions
- e. Carry data and instructions

NetExam

Sri Lanka Institute of Information Technology

Find the **incorrect** statement about BIOS program:

Select one:

- a. BIOS initializes all aspects of system
- b. BIOS is typically stored in an EPROM
- c. BIOS handles requests comming from application software
- d. BIOS is loaded at power up or reboot
- e. BIOS comes in a factory fitted firmware

What is meant by a Bus in computer Architecture?

Select one:

- a. Device which perform arithmetic and logical operations
- b. Combination of internal and external memory
- c. Combination of odd and even parity
- d. A communication pathway connecting two or more devices
- e. Combination of static and dynamic memory

Question **1**

Not yet answered

Marked out of 1

Flag question

Write the simplified answer of the following K-Map?

A \ BC	00	01	11	10
0	1	0	0	1
1	1	0	0	1

Select one:

- a. A
- b. C'
- c. A'C'
- d. AB
- e. A'

6
nswered
out of 1
question

ASCII code for character "p" is 112. Character "p" is transmitted over a network. Parity scheme used by the sender is Odd Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11100001
- b. 01111000
- c. 11100000
- d. 11110000
- e. 01110000

Next page

NetExam

Sri Lanka Institute of Information Technology

32

answered

out of 1

question

The Final Carry out of the full adder represented by

Select one:

- a. By merging two intermediate carry out results using and NOR gate
- b. By merging two intermediate carry out results using and AND gate
- c. By merging two intermediate carry out results using and OR gate
- d. By merging two intermediate carry out results using and XOR gate
- e. By merging two intermediate carry out results using and XNOR gate

NetExam

Sri Lanka Institute of Information Technology

What is the incorrect statement related to 4 bit parallel adder?

Select one:

- a. Combination of half adder and full adders
- b. Consist with 4 adders
- c. It is a sequential circuit
- d. Carry out of one adder is inserted as carry in of next adder
- e. Produces the arithmetic sum in parallel

NetExam

Sri Lanka Institute of Information Technology

What is the functionality of PC register?

Select one:

- a. Contains a word of data to be written to memory or the word most recently read
- b. Contains the address of the first instruction to be fetched
- c. Instruction most frequently fetched
- d. Contains the address of the next instruction to be fetched
- e. Contains the address of the last instruction to be fetched

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question 20

Not yet answered

Marked out of 1

Flag question

The instructions like MOV and ADD are called as:

Select one:

- a. Commands
- b. Byte code
- c. Op code
- d. Operators
- e. Mnemonics

Question 23

Not yet answered

Marked out of 1

Flag question

What is the operating system service that belongs to memory management?

Select one:

- a. Allocating memory for incoming processes
- b. Creating and deleting files and directories
- c. Creating user and system processes
- d. Deadlock handling
- e. Allocate disk space for programs

Next page

NetExam

Sri Lanka Institute of Information Technology

Question **30**

Not yet answered

Marked out of 1

Flag question

This is **not** a task performed by an operating system:

Select one:

- a. Efficiently manage the computer hardware
- b. Coordinate the use of hardware among applications
- c. Execute user programs
- d. Performs a specific function for an end user
- e. Provide a convenient user interface

NetExam

Sri Lanka Institute of Information Technology

Find the **incorrect** statement about BIOS program:

Select one:

- a. BIOS initializes all aspects of system
- b. BIOS handles requests comming from application software
- c. BIOS is typically stored in an EPROM
- d. BIOS comes in a factory fitted firmware
- e. BIOS is loaded at power up or reboot

Select the **incorrect** feature of a CISC system:

Select one:

- a. Emphasis on hardware
- b. High cycles per second, small code sizes
- c. Transistors used for handling complex instructions
- d. Includes multi-clock complex instructions
- e. Low cycles per second, large code sizes

Moodle

NetExam

Sri Lanka Institute of Information Technology

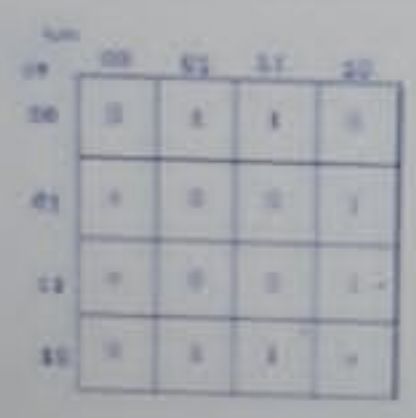
Question **4**

Not yet answered

Marked out of 1

Flag question

Select the simplified expression for the function produce by the K-Map



Select one:

- a. $xz'+zx'$
- b. $x'y+zx'$
- c. $xz+y'z$
- d. $xy+zx'$
- e. $x'y+z'x'$

NetExam

Sri Lanka Institute of Information Technology

Question **28**

Not yet answered

Marked out of 1

Flag question

Select the **incorrect** statement about interrupts.

Select one:

- a. Interrupts can process later by the operating system.
- b. A trap or exception is a software-generated interrupt
- c. Interrupt architecture must save the address of the interrupted instruction
- d. An operating system is interrupt driven
- e. Interrupt transfers control to the interrupt service routine

12
nswered
ut of 1
uestion

Select the register that automatically increments the value sequentially?

Select one:

- a. BX
- b. MAR
- c. SP
- d. AX
- e. IP

NetExam

Sri Lanka Institute of Information Technology

_____ register keeps tracks of the instructions stored in program stored in memory.

Select one:

- a. AC (Accumulator)
- b. PC (Program Counter)
- c. IR (Instruction Register)
- d. MAR (Memory Address Register)
- e. MBR (Memory Buffer Register)

NetExam

Sri Lanka Institute of Information Technology

red

1

ion

The Final Carry out of the full adder represented by

Select one:

- a. By merging two intermediate carry out results using and OR gate
- b. By merging two intermediate carry out results using and XOR gate
- c. By merging two intermediate carry out results using and XNOR gate
- d. By merging two intermediate carry out results using and AND gate
- e. By merging two intermediate carry out results using and NOR gate

1
wered
of 1
estion

Which one of the following consists of a sequence of operations performs by CPU?

Select one:

- a. Execute Cycle
- b. Instruction Cycle
- c. Fetch Cycle
- d. Decoding
- e. OS

NetExam

Sri Lanka Institute of Information Technology

Question 32

Not yet answered

Marked out of 1

Flag question

What is the incorrect statement related to 4 bit parallel adder?

Select one:

- a. Consist with 4 adders
- b. Carry out of one adder is inserted as carry in of next adder
- c. Produces the arithmetic sum in parallel
- d. Combination of half adder and full adders
- e. It is a sequential circuit

NetExam

Sri Lanka Institute of Information Technology

on 17

In instruction format, data or address of any data location is said to be:

Select one:

- a. Logical code
- b. Function code
- c. Instruction code
- d. Operand
- e. Op code

Next page

NetExam

Sri Lanka Institute of Information Technology

What is the incorrect statement related to 4 bit parallel adder?

Select one:

- a. Combination of half adder and full adders
- b. Carry out of one adder is inserted as carry in of next adder
- c. It is a sequential circuit
- d. Consist with 4 adders
- e. Produces the arithmetic sum in parallel

Question **31**

Not yet answered

Marked out of 1

Flag question

What is the incorrect statement related to 4 bit parallel adder?

Select one:

- a. Combination of half adder and full adders
- b. It is a sequential circuit
- c. Consist with 4 adders
- d. Produces the arithmetic sum in parallel
- e. Carry out of one adder is inserted as carry in of next adder

NetExam

Sri Lanka Institute of Information Technology

What is the operating system service that belongs to memory management?

Select one:

- a. Allocating memory for incoming processes
- b. Creating user and system processes
- c. Allocate disk space for programs
- d. Deadlock handling
- e. Creating and deleting files and directories

ASCII code for character "d" is 100 (i.e. hundrad). Character "d" is transmitted over a network. Parity scheme used by the sender is Odd Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11101000
- b. 11010011
- c. 11010001
- d. 01100100
- e. 11010000

Next page

wered
of 1
stion

The Final Carry out of the full adder represented by

Select one:

- a. By merging two intermediate carry out results using and XOR gate
- b. By merging two intermediate carry out results using and AND gate
- c. By merging two intermediate carry out results using and XNOR gate
- d. By merging two intermediate carry out results using and NOR gate
- e. By merging two intermediate carry out results using and OR gate

NetExam

Sri Lanka Institute of Information Techno

Question **34**

Not yet answered

Marked out of 1

Flag question

4X1 mux consist with

Select one:

- a. 1 inputs and 4 output
- b. 4^2 inputs and 1 outputs
- c. 4 inputs and 2 output
- d. 4 inputs and 1 output
- e. 2^4 inputs and 1 outputs

ered

1

ion

What are one time passwords?

Select one:

- a. Password which change hourly
- b. Unique password is required every time user tries to login into the system
- c. Password which consist with one word
- d. Password which change hourly
- e. Same password is required every time user tries to login into the system

NetExam

Sri Lanka Institute of Information Technology

Intruders are most common security threat which is known as:

Select one:

- a. Hackers or Crackers
- b. Malwares
- c. Viruses
- d. Spams
- e. Worms

ASCII code for character "d" is 100 (i.e. hundrad). Character "d" is transmitted over a network. Parity scheme used by the sender is Odd Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11101000
- b. 11010011
- c. 11010001
- d. 01100100
- e. 11010000

Next P

ASCII code for character "d" is 100 (i.e. hundrad). Character "d" is transmitted over a network. Parity scheme used by the sender is Odd Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11101000
- b. 11010011
- c. 11010001
- d. 01100100
- e. 11010000

Next page

NetExam

Sri Lanka Institute of Information Technology

uestion 15

ot yet answered

arked out of 1

Flag question

Computer bus is consists of

Select one:

- a. Set of registers
- b. Set of parallel wires
- c. Set of gates
- d. Set of accumulators
- e. Set of buffers

NetExam

Sri Lanka Institute of Information Technology

What is the incorrect statement related to Programmable logic array?

Select one:

- a. It is a combinational circuit
- b. Consist with an array of AND gates
- c. Output represents POS(Product of Sum) expression
- d. Consist with an array of OR gates
- e. Output represents SOP(Sum of Product) expression

34
nswered
ut of 1
question

Logic gates with a set of input and outputs is arrangement of

Select one:

- a. Register
- b. Logic circuit
- c. Sequential Circuit
- d. Combinational circuit
- e. Design circuits

What is meant by a Bus in computer Architecture?

Select one:

- a. Combination of static and dynamic memory
- b. Device which perform arithmetic and logical operations
- c. A communication pathway connecting two or more devices
- d. Combination of odd and even parity
- e. Combination of internal and external memory

Question 18
Not yet answered
Marked out of 1
Flag question

Identify the most suitable answer related to Address Bus?

Select one:

- a. Carries instructions
- b. Carry data and instructions
- c. Identify the source and destination of data
- d. Control and timing information
- e. Carries data

Next page

Which of the following register is used to keep the address of the current instruction is being executed?

Select one:

- a. Memory Buffer Register
- b. Program Counter
- c. Instruction Register
- d. Accumulator
- e. Memory Address Register

Next p

NetExam

Sri Lanka Institute of Information Technology

A bit that shows the current mode of the operating system is called;

Select one:

- a. System bit
- b. User bit
- c. Kernel bit
- d. Mode bit
- e. Overflow bit

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question **21**

Not yet answered

Marked out of 1

Flag question

_____ register keeps tracks of the instructions stored in program store

Select one:

- a. AC (Accumulator)
- b. PC (Program Counter)
- c. IR (Instruction Register)
- d. MAR (Memory Address Register)
- e. MBR (Memory Buffer Register)

red
f 1
tion

Which of the following register is used to keep the address of the current instruction is being executed?

Select one:

- a. Memory Buffer Register
- b. Program Counter
- c. Instruction Register
- d. Accumulator
- e. Memory Address Register

Question **33**

Not yet answered

Marked out of 1

Flag question

8X1 mux consist with

Select one:

- a. 1 inputs and 8 output
- b. 8 inputs and 4 output
- c. 2^8 inputs and 1 outputs
- d. 8^2 inputs and 1 outputs
- e. 8 inputs and 1 output

NetExam

Sri Lanka Institute of Information Technology

When a computer is first turned on or restarted, a special type of absolute loader called _____ is executed.

Select one:

- a. Boot Loader Program
- b. Relegation Loader Program
- c. Basic OS Program
- d. Bootstrap Program

NetExam

Sri Lanka Institute of Information Technology

How many AND gates and OR gates you need to draw a PLA for the following two expressions.

F1-A'B'+AB+AB'

F2-A'B'+AB+A'B

Select one:

- a. 4 AND gates and 1 OR gate
- b. 6 AND gates and 1 OR gate
- c. 4 AND gates and 4 OR gate
- d. 6 AND gates and 2 OR gates
- e. 4 AND gates and 2 OR gates

ASCII code for character "n" is 1101110. Character "n" is transmitted over a network. Parity scheme used by the sender is Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11101110
- b. 11011101
- c. 11011100
- d. 11101010
- e. 01101110

Next page

An I/O controller of a computer system transfers data from I/O devices to:

Select one:

- a. Registers
- b. Cache
- c. Buses
- d. Indexes
- e. Buffers

dle

NetExam

Sri Lanka Institute of Information Technology

on 23

t answered

d out of 1

g question

What are one time passwords?

Select one:

- a. Password which change hourly
- b. Same password is required every time user tries to login into the system
- c. Password which change hourly
- d. Password which consist with one word
- e. Unique password is required every time user tries to login into the system

red
1
ion

Select the **incorrect** feature of a CISC system:

Select one:

- a. Includes multi-clock complex instructions
- b. Transistors used for handling complex instructions
- c. Emphasis on hardware
- d. Low cycles per second, large code sizes
- e. High cycles per second, small code sizes

NetExam

Sri Lanka Institute of Information Technology

How many AND gates and OR gates you need to draw a PLA for the following two expressions.

F1-A'B'+AB+AB'

F2-A'B'+AB+A'B

Select one:

- a. 4 AND gates and 1 OR gate
- b. 4 AND gates and 4 OR gate
- c. 6 AND gates and 2 OR gates
- d. 4 AND gates and 2 OR gates
- e. 6 AND gates and 1 OR gate

NetExam

Sri Lanka Institute of Information Technology

Question **12**

Not yet answered

Marked out of 1

Flag question

What is the register that uses to store the memory addresses?

Select one:

- a. MAR
- b. PC
- c. AR
- d. Stack
- e. ALU

NetExam

Sri Lanka Institute of Information Technology

on 33

t answered

d out of 1

g question

4X1 mux consist with

Select one:

- a. 1 inputs and 4 output
- b. 4 inputs and 2 output
- c. 4^2 inputs and 1 outputs
- d. 2^4 inputs and 1 outputs
- e. 4 inputs and 1 output

red
f 1
ion

In instruction format, data or address of any data location is said to be:

Select one:

- a. Logical code
- b. Op code
- c. Instruction code
- d. Operand
- e. Function code

15

answered

out of 1

question

What is the functionality of PC register?

Select one:

- a. Contains the address of the next instruction to be fetched
- b. Contains the address of the first instruction to be fetched
- c. Contains the address of the last instruction to be fetched
- d. Contains a word of data to be written to memory or the word most recently read
- e. Instruction most frequently fetched

Select the **incorrect** statement about interrupts.

Select one:

- a. Interrupts can process later by the operating system.
- b. A trap or exception is a software-generated interrupt
- c. An operating system is interrupt driven
- d. Interrupt architecture must save the address of the interrupted instruction
- e. Interrupt transfers control to the interrupt service routine

7

swered

t of 1

uestion

Operating System is,

Select one:

- a. Software Program
- b. Hardware Component
- c. Combination of Hardware and Software
- d. A Cable
- e. A Interface Card

8
nswered
out of 1
question

ASCII code for character "d" is 100 (i.e. hundrad). Character "d" is transmitted over a network. Parity scheme used by the sender is Odd Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11101000
- b. 11010001
- c. 11010000
- d. 01100100
- e. 11010011

Next page

Quiz na

Finish attempt

Time left 0:23:

1 2 3
9 10 11
17 18 19
25 26 27
33 34 35

NetExam

Sri Lanka Institute of Information Technology

Question 19

Not yet answered

Marked out of 1

Flag question

Which of the following register is used to keep the memory address of the next instruction to be executed is located?

Select one:

- a. Memory Address Register
- b. Program Status Register
- c. Program Counter
- d. Instruction Register
- e. Memory Data Register

NetExam

Sri Lanka Institute of Information Technology

How many AND gates and OR gates you need to draw a PLA for the following two expressions.

F1-A'B'C'+ABC+AB'C'

F2-A'B'C+ABC+AB'C'

Select one:

- a. 4 AND gates and 4 OR gates
- b. 4 AND gates and 2 OR gates
- c. 4 AND gates and 1 OR gate
- d. 6 AND gates and 1OR gate
- e. 6 AND gates and 2 OR gates

NetExam

Sri Lanka Institute of Information Technology

4

wered

of 1

estion

Identify the most suitable answer related to Address Bus?

Select one:

- a. Carries instructions
- b. Carry data and instructions
- c. Carries data
- d. Control and timing information
- e. Identify the source and destination of data

NetExam

Sri Lanka Institute of Information Technology

A three input NOR gate gives logic high output only when

Select one:

- a. all input are low
- b. Two inputs are high
- c. one input is low
- d. one input is high
- e. two input are low

ed
1
on

Select the **incorrect** statement about interrupts.

Select one:

- a. An operating system is interrupt driven
- b. Interrupt transfers control to the interrupt service routine
- c. A trap or exception is a software-generated interrupt
- d. Interrupts can process later by the operating system.
- e. Interrupt architecture must save the address of the interrupted instruction

Identify the most suitable answer related to Address Bus?

Select one:

- a. Carries data
- b. Control and timing information
- c. Carries instructions
- d. Carry data and instructions
- e. Identify the source and destination of data

NetExam

Sri Lanka Institute of Information Technology

The instructions like MOV and ADD are called as:

Select one:

- a. Commands
- b. Byte code
- c. Op code
- d. Mnemonics
- e. Operators

NetExam

Sri Lanka Institute of Information Technology

Find the **incorrect** statement about BIOS program:

Select one:

- a. BIOS is typically stored in an EPROM
- b. BIOS initializes all aspects of system
- c. BIOS handles requests comming from application software
- d. BIOS comes in a factory fitted firmware
- e. BIOS is loaded at power up or reboot

NetExam

Sri Lanka Institute of Information Technology

ASCII code for character "n" is 1101110. Character "n" is transmitted over a network. Parity scheme used by the sen
Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 01101110
- b. 11101110
- c. 11101010
- d. 11011101
- e. 11011100

Next

on 35
et answered
ed out of 1
ag question

A three input NOR gate gives logic high output only when

Select one:

- a. two input are low
- b. Two inputs are high
- c. all input are low
- d. one input is high
- e. one input is low

on 25
et answered
ed out of 1
ag question

What is not a service of Operating System?

Select one:

- a. Memory Management
- b. Information Management
- c. Process Management
- d. Program Management
- e. Disk Management

on 40
t answered
d out of 1
g question

What is the maximum memory size that can be addressed directly in a computer which has a processor with 29 bit memory addresses?

Select one:

- a. 512KB
- b. 4MB
- c. 512GB
- d. 512MB
- e. 4GB

Finish attempt

NetExam

Sri Lanka Institute of Information Technology

Identify the most suitable answer related to Data Bus?

Select one:

- a. Carry data and instructions
- b. Control and timing information
- c. Identify the source and destination of data
- d. Carries data
- e. Carries instructions

Identify the most suitable answer related to Address Bus?

Select one:

- a. Carries instructions
- b. Control and timing information
- c. Carry data and instructions
- d. Identify the source and destination of data
- e. Carries data

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question **25**

Not yet answered

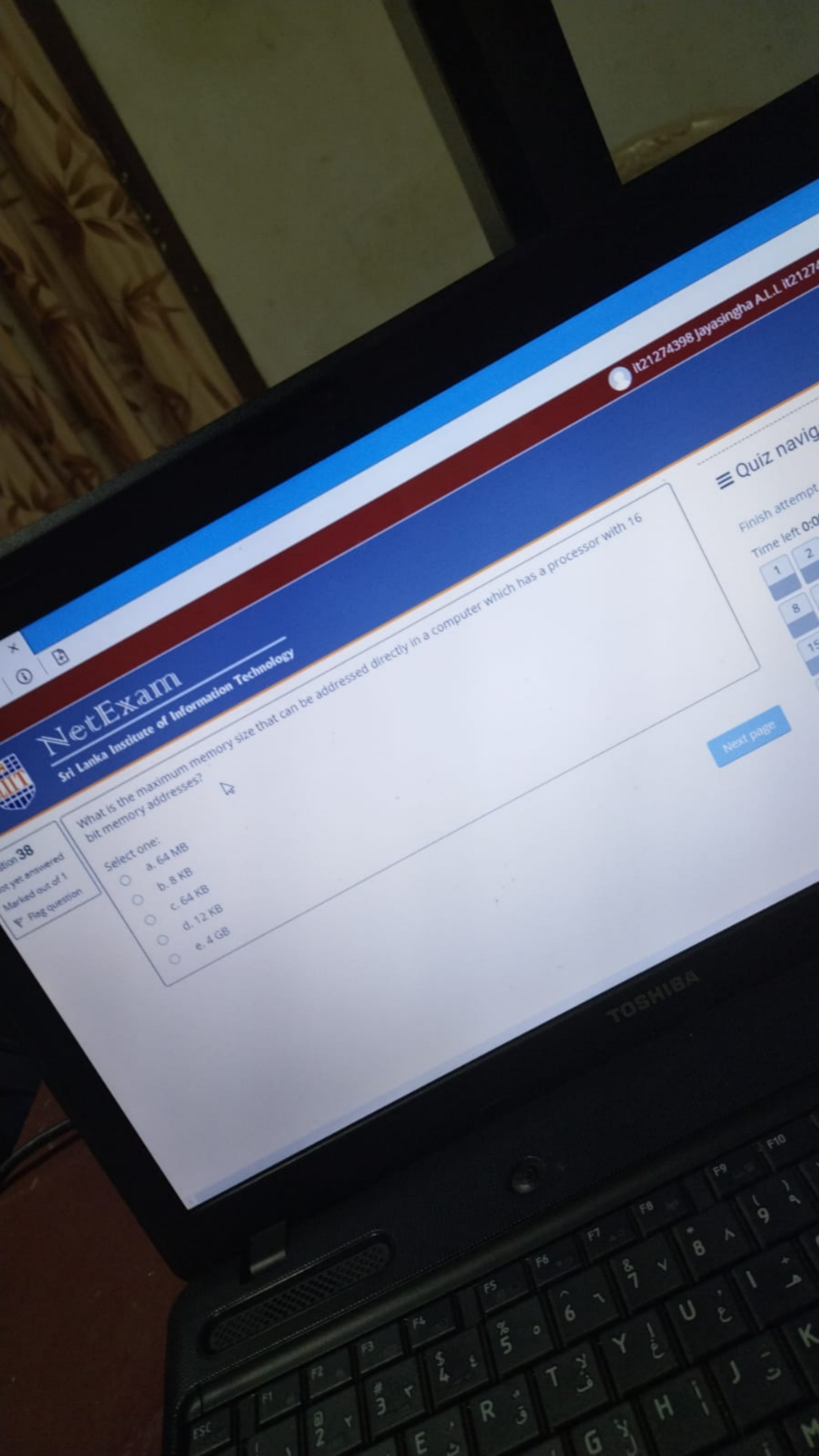
Marked out of 1

Flag question

What is the operating system service that belongs to memory management?

Select one:

- a. Deadlock handling
- b. Allocate disk space for programs
- c. Creating and deleting files and directories
- d. Allocating memory for incoming processes
- e. Creating user and system processes


NetExam
Sri Lanka Institute of Information Technology
it21274398 Jayasingha A.L.L it21274
Quiz navig
Finish attempt
Time left 0:0
1
2
8
15
Next page
stion 38
ot yet answered
Marked out of 1
Flag question
What is the maximum memory size that can be addressed directly in a computer which has a processor with 16 bit memory addresses?
Select one:
a. 64 MB
b. 8 KB
c. 64 KB
d. 12 KB
e. 4 GB
TOSHIBA

ered
f 1
tion

Select the correct order in execution.

1. CPU checks to see whether the next instruction it requires is in cache
2. If not, the CPU has to fetch next instruction from main memory
3. If it is, then the instruction is fetched from the cache
4. Cache fetches data from next to current addresses in main memory

Select one:

- a. B, C, A, D
- b. D, C, A, B
- c. D, A, C, B
- d. B, A, C, D
- e. C, D, B, A

Select the **incorrect** feature of a RISC system:

Select one:

- a. Need more RAM
- b. Processor hardware simple
- c. Instructions complete within one clock cycle
- d. Compiler workload is low
- e. Simple instructions

NetExam

Sri Lanka Institute of Information Technology

Which of the following register is used to keep the memory address of the next instruction to be executed

Select one:

- a. Program Counter
- b. Instruction Register
- c. Memory Address Register
- d. Memory Data Register
- e. Program Status Register

NetExam

Sri Lanka Institute of Information Technology

What is the functionality of Instruction Register (IR)?

Select one:

- a. Contains the address of the next instruction to be executed
- b. Instruction most frequently fetched
- c. Contains the address of the next instruction to be fetched
- d. Contains a word of data to be written to memory or the word
- e. Contains the instruction currently being executed by the CPU

NetExam

Sri Lanka Institute of Information Technology

What is the most correct statement related to Programmable logic array?

Select one:

- a. Consist with both array of AND gates and OR gates and output represents SOP expression
- b. Consist with array of AND gates and output represents SOP expression
- c. Consist with array of XOR gates and output represents SOP expression
- d. Consist with array of OR gates and output represents SOP expression
- e. Consist with both array of AND gates and OR gates

What is not a service of Operating System?

Select one:

- a. Memory Management
- b. Information Management
- c. Process Management
- d. Program Management
- e. Disk Management

21

nswered

out of 1

question

Identify the most suitable answer related to Address Bus?

Select one:

- a. Carries instructions
- b. Carry data and instructions
- c. Identify the source and destination of data
- d. Control and timing information
- e. Carries data

Question 22

Not yet answered

Marked out of 1

Flag question

Select the incorrect feature of a CISC system:

Select one:

- a. High cycles per second, small code sizes
- b. Transistors used for handling complex instructions
- c. Includes multi-clock complex instructions
- d. Emphasis on hardware
- e. Low cycles per second, large code sizes

Question 12
Not yet answered
Marked out of 1
Flag question

Which one of the following consists of a sequence of operations performs by CPU?

Select one:

- a. Fetch Cycle
- b. Instruction Cycle
- c. Decoding
- d. Execute Cycle
- e. OS

Question 23
Not yet answered
Marked out of 1
Flag question

_____ is built directly on the hardware.

Select one:

- a. Computer environment
- b. Operating system
- c. Database system
- d. Application system
- e. User environment

NetExam

Sri Lanka Institute of Information Technology

35

nswered

ut of 1

question

A three input NOR gate gives logic high output only when

Select one:

- a. all input are low
- b. one input is low
- c. one input is high
- d. Two inputs are high
- e. two input are low

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question **13**

Not yet answered

Marked out of 1

Flag question

Computer bus is consists of

Select one:

- a. Set of registers
- b. Set of buffers
- c. Set of parallel wires
- d. Set of accumulators
- e. Set of gates

21
answered
out of 1
question

What is the functionality of Instruction Register (IR)?

Select one:

- a. Contains a word of data to be written to memory or the word most recently read
- b. Contains the address of the next instruction to be executed
- c. Contains the address of the next instruction to be fetched
- d. Instruction most frequently fetched
- e. Contains the instruction currenly being executed by the CPU

26
swered
ut of 1
uestion

A bit that shows the current mode of the operating system is called:

Select one:

- a. Kernel bit
- b. Mode bit
- c. System bit
- d. Overflow bit
- e. User bit

red
1
ion

Process is a:

Select one:

- a. program in High Level language kept on disk
- b. program in execution
- c. contents of main memory
- d. job in secondary memory
- e. passive entity

Which one of the following consists of a sequence of operations performs by CPU?

Select one:

- a. Fetch Cycle
- b. Decoding
- c. Execute Cycle
- d. Instruction Cycle
- e. OS

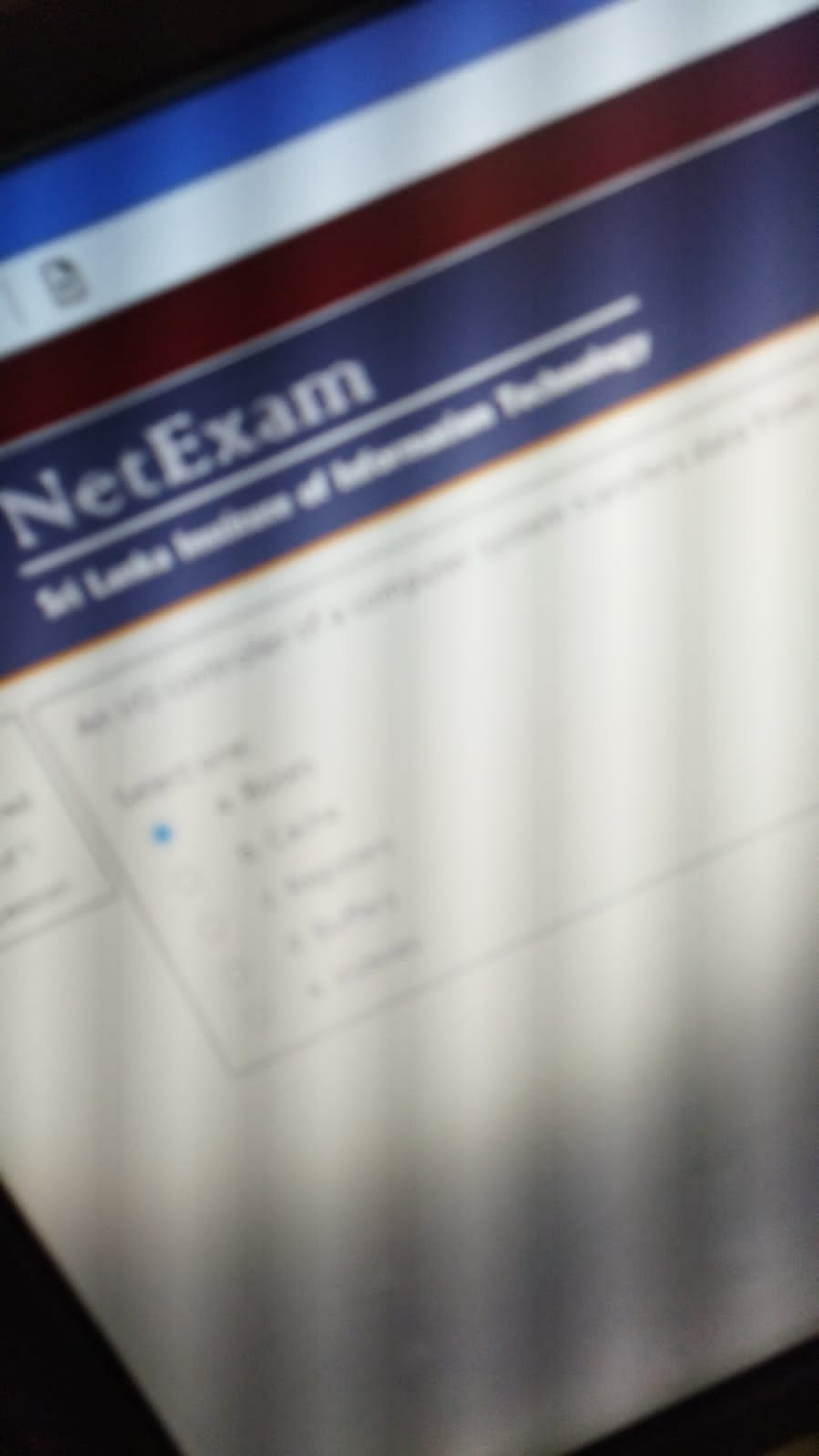
NetExam

Sri Lanka Institute of Information Technology

What is meant by a Bus in computer Architecture?

Select one:

- a. Device which perform arithmetic and logical operations
- b. Combination of odd and even parity
- c. A communication pathway connecting two or more devices
- d. Combination of static and dynamic memory
- e. Combination of internal and external memory


NetExam

odle

NetExam

Sri Lanka Institute of Information Technology

10
answered
out of 1
question

Fifth generation computers are likely to exhibit

Select one:

- a. heuristic behaviour
- b. artificial intelligence
- c. advanced parallel processing
- d. All the above
- e. natural language processing

red

1

on

Select the simplified boolean expression of the function given by K-Map

cd \ ab	00	01	11	10
00	1	1		1
01	x			
11	x			
10	1	1		x

Select one:

- a. a'b'+b'd'+a'd'
- b. a'b'+b'd'+a'bd'
- c. b'd'+a'd'
- d. a'b'+b'd'+a'b'd'
- e. b'd'+a'bd'

uestion 36

ot yet answered

larked out of 1

Flag question

How many AND gates and OR gates you need to draw a PLA for the following two expressions.

F1-A'B'C'+ABC+AB'C'

F2-A'B'C+ABC+AB'C'

Select one:

- a. 4 AND gates and 4 OR gates
- b. 4 AND gates and 1 OR gate
- c. 6 AND gates and 1OR gate
- d. 6 AND gates and 2 OR gates
- e. 4 AND gates and 2 OR gates

Next

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question **18**

Not yet answered

Marked out of 1

Flag question

Select the correct order in execution.

1. CPU checks to see whether the next instruction it requires is in cache
2. If not, the CPU has to fetch next instruction from main memory
3. If it is, then the instruction is fetched from the cache
4. Cache fetches data from next to current addresses in main memory

Select one:

- a. B, A, C, D
- b. B, C, A, D
- c. D, A, C, B
- d. C, D, B, A
- e. D, C, A, B

NetExam

Sri Lanka Institute of Information Technology

Select the register that automatically increments the value sequentially?

Select one:

- a. MAR
- b. SP
- c. AX
- d. IP
- e. BX

What is the register that uses to store the memory addresses?

Select one:

- a. MAR
- b. ALU
- c. Stack
- d. AX
- e. PC

NetExam

Sri Lanka Institute of Information Technology

9

swered

t of 1

uestion

In instruction format, data or address of any data location is said to be:

Select one:

- a. Instruction code
- b. Op code
- c. Function code
- d. Logical code
- e. Operand

NetExam

Sri Lanka Institute of Information Technology

ASCII code for character "p" is 112. Character "p" is transmitted over a network. Parity scheme used by the sender is Odd Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 01110000
- b. 11110000
- c. 11100001
- d. 01111000
- e. 11100000

Next page

Which of the following register is used to keep the address of the

Select one:

- a. Memory Buffer Register
- b. Program Counter
- c. Instruction Register
- d. Memory Address Register
- e. Accumulator

Select the **incorrect** feature of a RISC system:

Select one:

- a. Compiler workload is low
- b. Need more RAM
- c. Processor hardware simple
- d. Simple instructions
- e. Instructions complete within one clock cycle

Which of the following register is used to keep the address of the current instruction is being executed?

Select one:

- a. Program Counter
- b. Instruction Register
- c. Accumulator
- d. Memory Address Register
- e. Memory Buffer Register

9
wered
of 1
estion

Which of the following register is used to keep the memory address of the next instruction to be executed is located?

Select one:

- a. Memory Data Register
- b. Program Counter
- c. Instruction Register
- d. Program Status Register
- e. Memory Address Register

ion 21
et answered
ed out of 1
ag question

Select the correct order in execution.

1. CPU checks to see whether the next instruction it requires is in cache
2. If not, the CPU has to fetch next instruction from main memory
3. If it is, then the instruction is fetched from the cache
4. Cache fetches data from next to current addresses in main memory

Select one:

- a. C, D, B, A
- b. D, A, C, B
- c. D, C, A, B
- d. B, A, C, D
- e. B, C, A, D

on 27
t answered
ed out of 1
g question

What is not a component of a process management?

Select one:

- a. Deletion of Processes
- b. Resume Processes
- c. Suspend processes
- d. Backup of files on stable storage media
- e. Process Communication

Which one of the following consists of a sequence of operations performs by CPU?

Select one:

- a. Decoding
- b. Instruction Cycle
- c. Execute Cycle
- d. OS
- e. Fetch Cycle

NetExam

Sri Lanka Institute of Information Technology

ered

f 1

tion

The instructions like MOV and ADD are called as:

Select one:

- a. Byte code
- b. Operators
- c. Commands
- d. Op code
- e. Mnemonics

n 13
answered
d out of 1
g question

What is the functionality of Instruction Register (IR)?

Select one:

- a. Instruction most frequently fetched
- b. Contains the address of the next instruction to be fetched
- c. Contains the instruction currenly being executed by the CPU
- d. Contains a word of data to be written to memory or the word most recently read
- e. Contains the address of the next instruction to be executed

Next

on 22
: answered
l out of 1
: question

_____ register keeps tracks of the instructions stored in program stored in memory.

Select one:

- a. IR (Instruction Register)
- b. PC (Program Counter)
- c. MBR (Memory Buffer Register)
- d. MAR (Memory Address Register)
- e. AC (Accumulator)

NetExam

Sri Lanka Institute of Information Technology

_____ is the process of carrying out commands.

Select one:

- a. Fetching
- b. Executing
- c. Decoding
- d. Buffering
- e. Storing

Moodle

NetExam

Sri Lanka Institute of Information Technology

estion 9

yet answered

ked out of 1

Flag question

Select the **incorrect** statement.

Select one:

- a. High-level programming languages introduced with 3rd generation computers.
- b. ENIAC is the first electronic general-purpose calculator.
- c. 4th generation computes use silicon chip called microprocessors.
- d. First generation computers used Thermion valves.

ered
f 1
ion

What is the functionality of Instruction Register (IR)?

Select one:

- a. Instruction most frequently fetched
- b. Contains the instruction currenly being executed by the CPU
- c. Contains a word of data to be written to memory or the word most recently read
- d. Contains the address of the next instruction to be fetched
- e. Contains the address of the next instruction to be executed

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question **8**
Not yet answered
Marked out of 1
Flag question

ASCII code for character "d" is 100 (i.e. hundrad). Character "d" is transmitted over a network. Parity scheme used by the sender is Odd Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11010011
- b. 11010001
- c. 11101000
- d. 01100100
- e. 11010000

Next page

17 25 26 33 34 35

tion 14
yet answered
ked out of 1
lag question

In instruction format, data or address of any data location is said to be:

Select one:

- a. Op code
- b. Operand
- c. Logical code
- d. Function code
- e. Instruction code

Question **4**

Not yet answered

Marked out of 1

Flag question

Select the correct simplification of the function given by the K-Map

YZ \ WX	00	01	11	10
00	0	X	0	X
01	X	1	X	1
11	0	X	1	0
10	0	1	X	0

Select one:

- a. X'Z+Y
- b. W'X+Y'Z+XY
- c. XZ+Y
- d. W'X+Y'Z
- e. XY+Y'Z

Question **10**

Not yet answered

Marked out of 1

Flag question

What is embedded system?

Select one:

- a. None of the above
- b. The programme which arrives by being wrapped in box
- c. The programme which is the permanent part of the computer
- d. The computer which is the part of a big computer
- e. The computer and software system that control the machine

ASCII code for character "n" is 1101110. Character "n" is transmitted over a network. Parity scheme used by the sender is Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11101110
- b. 11011100
- c. 11101010
- d. 11011101
- e. 01101110

Next page

ASCII code for character "h" is 104. Character "h" is transmitted over a network. Parity scheme used by the sender is Even Parity. M
Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 01101000
- b. 11101000
- c. 11010011
- d. 11010000
- e. 11010001

Question **4**

Not yet answered

Marked out of 1

Flag question

Select the simplified boolean expression of the function given by the K-Map

cd \ ab	00	01	11	10
00	1	X	X	1
01	X			1
11				
10	1			X

Select one:

- a. b'd'
- b. b'd'+b'c'
- c. b'c'+ab'c'd
- d. bd'+b'c'
- e. b'd'+b'c'+c'd'

Question **5**

Not yet answered

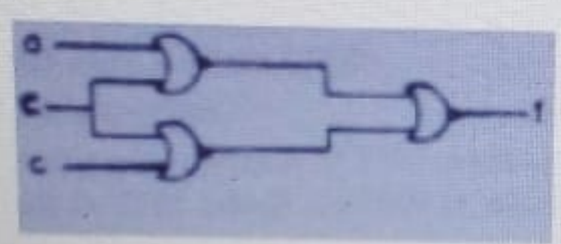
Marked out of 1

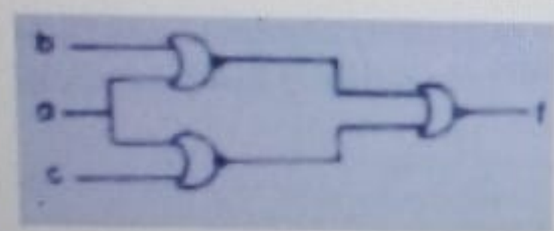
Flag question

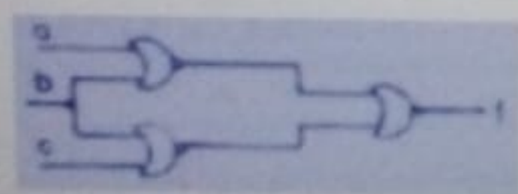
Select the correct implementation of the boolean function represented by the given K-Map

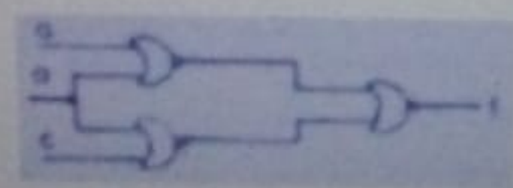
c \ ab	00	01	11	10
0	0	0	1	1
1	0	1	1	1

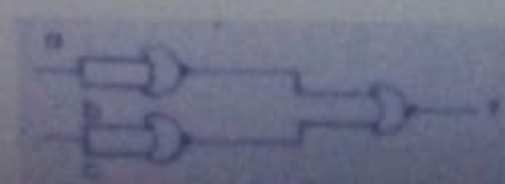
Select one:

a. 

b. 

c. 

d. 



NetExam

Sri Lanka Institute of Information Technology

wered
of 1
estion

Select the correct order in execution.

1. CPU checks to see whether the next instruction it requires is in cache
2. If not, the CPU has to fetch next instruction from main memory
3. If it is, then the instruction is fetched from the cache
4. Cache fetches data from next to current addresses in main memory

Select one:

- a. D, A, C, B
- b. C, D, B, A
- c. B, A, C, D
- d. D, C, A, B
- e. B, C, A, D

Moodle

NetExam

Sri Lanka Institute of Information Technology

Question 9
Not yet answered
Marked out of 1
Flag question

The proper definition of a modern digital computer is

Select one:

- a. none of the above
- b. a more sophisticated and modified electronic pocket calculator
- c. an electronic automated machine that can solve problems involving words and numbers
- d. a machine that works on binary code
- e. any machine that can perform mathematical operations

Next page

NetExam

Sri Lanka Institute of Information Technology

ASCII code for character "M" is 77. Character "M" is transmitted over a network. Parity scheme used by the sender is Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 01001101
- b. 11000111
- c. 11001100
- d. 11000000
- e. 11001111

Next page

tion 17
yet answered
ed out of 1
lag question

What is the functionality of PC register?

Select one:

- a. Instruction most frequently fetched
- b. Contains a word of data to be written to memory or the word most recently read
- c. Contains the address of the next instruction to be fetched
- d. Contains the address of the last instruction to be fetched
- e. Contains the address of the first instruction to be fetched

n 15
t answered
d out of 1
g question

_____ is the process of carrying out commands.

Select one:

- a. Storing
- b. Executing
- c. Buffering
- d. Fetching
- e. Decoding

NetExam

Sri Lanka Institute of Information Technology

ASCII code for character "h" is 104. Character "h" is transmitted over a network. Parity scheme used by the sender is Even Parity. Most Significant Bit (MSB) is used as the parity bit. What is the bit stream transmitted over the network?

Select one:

- a. 11010000
- b. 01101000
- c. 11010011
- d. 11010001
- e. 11101000

Next page

Select the correct simplification of the function given by the K-Map

YZ \ WX	00	01	11	10
00	0	X	0	X
01	X	1	X	1
11	0	X	1	0
10	0	1	X	0

Select one:

- a. W'X+Y'Z
- b. W'X+Y'Z+XY
- c. X'Z+Y
- d. XY+Y'Z
- e. XZ+Y

Moodle

Sri Lanka Institute of Information Technology

Question **5**

Not yet answered

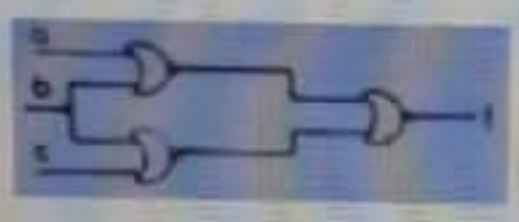
Marked out of 1

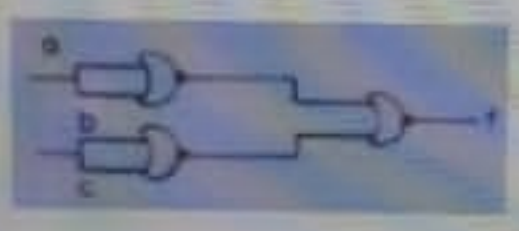
Flag question

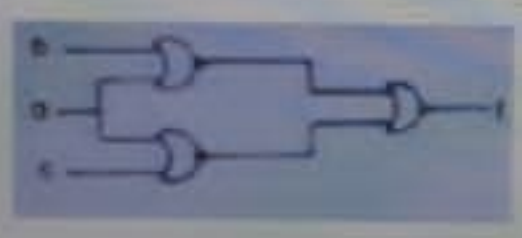
Select the correct implementation of the boolean function represented by the given K-Map

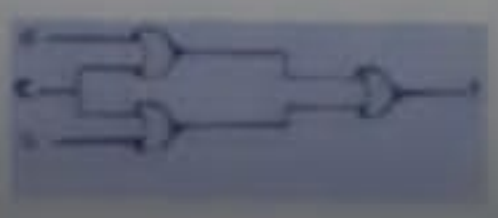
c \ ab	00	01	11	10
0	0	0	1	1
1	0	1	1	1

Select one:

a. 

b. 

c. 

d.

e.