

(6.1)

What is performance in the context of computer systems and why is it so difficult to define?

(6.2)

A system consists of a CPU, cache memory, main store, and hard disk drive. Where are time and effort best spent improving the system's performance? What factors affect your answer?

(6.4)

A data transmission system transmits data in the the form of a master frame containing 16 sub-frames. Each sub-frame includes a 1024-bit data word and a 12-bit error-correcting code. The master frame itself contains a 32-bit error correcting code. What is the efficiency of this system?

(6.6)

Why is clock rate a poor metric of computer performance? What are the relative strengths and weaknesses of clock speed as a performance metric?

(6.7)

The timing diagram in Figure P6.7 illustrates a system in which operations occur as three consecutive clock cycles. Actions taking place in clock cycle 1 are scalable; that is, if the clock cycle time changes, the actions can be speeded up or slowed down correspondingly. In cycle 2, the action process 1 requires 25 ns and in clock cycle 3 the action process 2 requires 32 ns. If the clock cycle is less than the time required for process 1 or process 2, then one or more wait cycles have to be inserted for the process to complete.

What is the time to complete an operation if the clock cycle time is

- a. 50 ns b. 40 ns c. 30 ns d. 20 ns e. 10 ns

(6.9)

Can you think of a better metric than MIPS or clock speeds that gives a good impression of the power of a processor (without having to use benchmarks)?

(6.11)

Overclocking a computer means operating it at a higher clock rate than that specified by its manufacturer; for example, a 2 GHz chip might be clocked at 2.1 GHz to squeeze more performance out of it.

Does overclocking disprove the famous aphorism “There’s no such thing as a free lunch,” or is there a hidden cost? If so, what is the cost of overclocking?

(6.12)

The following figures define the typical operating parameters of a processor.

Operation, Frequency, Cycles Arithmetic/logical instructions, 45%, 1 Register load operations, 20%, 3 Register store operations, 10%, 2 All branch instructions, 25%, 2

If the clock rate could be reduced by 15%, it would require only 2 cycles to perform a register load. Would that be a good idea?

(6.13)

A computer has the following parameters:

Operation, Frequency, Cycles Arithmetic/logical instructions, 45%, 1 Register load operations, 20%, 5 Register store operations, 10%, 2 All branch instructions, 25%, 8

If the average performance of the computer (in terms of its CPI) is to be increased by 20% while executing the same instruction mix, what target must be achieved for the cycles per conditional branch instruction?

(6.14)

A program is run on a computer with the following parameters.

Clock cycle time, 10 ns Instructions with 1 cycle, 70% Instructions with 2 cycles, 20% Instructions with 3 cycles, 10%

What is the MIPS rating of this computer?

(6.16)

In a particular system, a CPU is used for 78% of the time and a disk drive for 22% of the time. A designer has two options:

- a. improve the disc performance by 40% and the CPU performance by 20%
- b. improve the disc performance by 10% and the CPU performance by 80%

Which is the better option, and why?

(6.17)

For the following systems that have both serial and parallel activities, calculate the speedup ratio.

a. 10 processors, $f_s = 0.1$ b. 100 processors, $f_s = 0.1$ c. 5 processors, $f_s = 0.4$ d. 100 processors, $f_s = 0.01$

(6.18)

A system has a single core processor that costs \$150. Suppose that adding more cores to the chip costs \$10 per additional processor. (Note: For this system, the value of f_s is 0.10).

If it is considered worthwhile adding cores until the incremental speedup ratio increases by less than 5% over the original (unmodified) performance, what is the optimum number of processors? What percentage increase in cost is required to achieve this performance?

(6.22)

A coprocessor is added to a computer to speed up the execution time of string-processing instructions by a factor of 3.5. What fraction of the execution time must use these string-processing instructions in order to achieve an average speedup of 1.5?

(6.25)

A computer spends 25% of its time accessing a hard disk. It spends 20% of the time doing floating point. The hard disk is replaced by two disks operating in parallel and the floating point unit is replaced by one four times faster. The speed up is given by

equation here

So, the speedup for the disk is $S_{disk} = 2 / (2 * 0.75 + 1 - 0.75) = 1 / 1.75 = 1.429$

The speedup for the floating-point unit is $S_{floating-point} = 4 / (4 * 0.80 + 1 - 0.80) = 4 / 3.4 = 1.176$. The total speedup ratio is the product of the individual speedups which is $1.429 * 1.176 = 1.681$. Is this answer correct?

(6.26)

Someone decided to use the following C code as part of a benchmark to determine the performance of a computer including its memory. It has two potential faults. What are they?

```
for (i = 0; i < 100; i++){  
    p = q * s + 12345  
    x = 0.0;  
    for (j = 0; j < 60000; j++){  
        x = x + A[j] * B[j];  
    }  
}
```

(6.31)

For two benchmarks, x and y , show that their arithmetic mean is always higher than, or the same as, the geometric mean.