OREGON STATE UNIVERSITY

CS 472 - Computer Architecture Spring 2014

Lab 5 - The Memory Hierarchy and Endian-Neutral Programming

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Part 1

Code

1 #include <stdio.h> #include <stdlib.h> 3 #include <time.h> 5 #define KB 1024 #define MB 1024 * 1024 int main() { unsigned int steps = 256 * 1024 *static int arr[4 * 1024 * 1024]; int lengthMod; 11 unsigned int i; double timeTaken; 13 clock_t start; 15 int sizes[] = { 1 * KB, 4 * KB, 8 * KB, 16 * KB,32 * KB, 64 * KB, 128 * KB, 256 * KB, 512 * KB, 1 * MB, 1.5 * MB, 2 * MB17 , 2.5 * MB, 3 * MB, 3.5 * MB, 4}; int results[sizeof(sizes)/sizeof(int) 19]; int s; 21 // for each size to test for ... for (s = 0; s < sizeof(sizes)/sizeof(</pre> 23 int); s++) { lengthMod = sizes[s] - 1; start = clock(); 25 for $(i = 0; i < steps; i++) {$ arr[(i * 16) & lengthMod] *= 10; 27 arr[(i * 16) & lengthMod] /= 10; 29 } timeTaken = (double)(clock() - start 31)/CLOCKS_PER_SEC; printf("%d, %.8f \n", sizes[s] / 1024, timeTaken); 33 return 0; 35 }

Output

```
1, 16.52000000
3 4, 16.2400000
  8, 16.31000000
5 16, 17.85000000
  32, 18.74000000
7 64, 36.05000000
  128, 78.88000000
9 256, 91.57000000
  512, 92.83000000
11 1024, 92.88000000
  1536, 92.84000000
13 2048, 92.9400000
  2560, 92.83000000
15 3072, 92.86000000
  3584, 92.83000000
17 4096, 92.86000000
```

From the output our program produces while running on a Beaglebone Black we can see that the size of the L1 cache is about 32K as the jump in time happened immediately after that. As for the L2 cache it is likely to be around 128K as there is no jump in timing after that.

Part 2

$endian_neutral.c$ 1 #include <stdio.h> #include <stdint.h> #define IS_BIG_ENDIAN (*(uint16_t *)" $\0 \times ff$ " < 0x100) int main(int argc, char **argv) printf("%d\n", IS_BIG_ENDIAN); //0 if false 9 short val; 11 char *p_val; p_val = (char *) &val; 13 if (IS_BIG_ENDIAN){ $p_val[0] = 0x12;$ 15 $p_val[1] = 0x34;$ } else { 17 $p_val[0] = 0x34;$ $p_val[1] = 0x12;$ 19 printf("%x\n", val); 21 23 return 0; }