

BOTTOM-UP-PARSING

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→ Here from a string, we try to get a start symbol.

→ Here, we try to match our string with RHS of a production & replace it with LHS of that production.

Two types of grammar

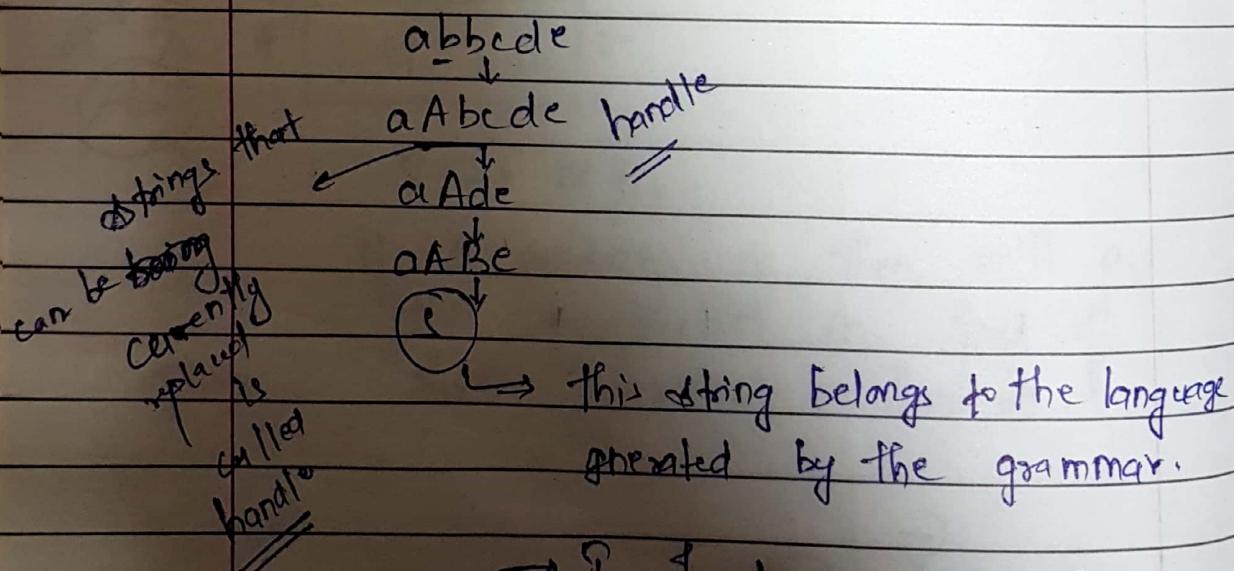
Operator Precedence
grammar

LR grammar

Eg.

$$\begin{aligned} S &\rightarrow aABe \\ A &\rightarrow Abc/b \\ B &\rightarrow d \end{aligned}$$

Input string abbcde



→ To find a suitable handle is a tough task & this technique is called handle pruning.

→ Here, we get rightmost derivative in reverse order.

↙ All parsers of Bottom - Up - Parsing works on,
shift - reduce principle. If are called
shift - reduce parser.

$$Q. E \rightarrow E + E / E * E / (E) / id$$

$id_1 + id_2 * id_3$	$id_1 + id_2 * id_3$
$\Rightarrow E + id_2 * id_3$	$\Rightarrow E + id_2 * id_3$
$\Rightarrow E + E * id_3$	$\Rightarrow E + E * id_3$
$\Rightarrow E * id_3$	$\Rightarrow E + E * E$
$\Rightarrow E * E$	$\Rightarrow E + E$
$\Rightarrow E$	$\Rightarrow E$

Stack	I/P	Action
\$	$id_1 + id_2 * id_3 \$$	shift id_1
\$ id_1	$+ id_2 * id_3 \$$	Reduce $E \rightarrow id_1$
* \$	$+ id_2 * id_3 \$$	shift +
top of stack is replaced by	$id_2 * id_3 \$$	shift id_2
the it is known as reduce action.	$* id_3 \$$	Reduce $E \rightarrow id_2$
	$=$	
	further we have either we can shift * or reduce $E \rightarrow E + E$.	of problems.
	This situation is called "Shift Reduce Conflict"	

Solving further

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invalid
case

1. id (4.) (IMP
2.) id 5. \$ \$ =
3. id id 6. (\$

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$E + E *$	\vdash
$$ E + E + id_3$	\vdash
$$ E + E * E$	\vdash
$(E + E)$	\vdash
$(E + E)$	\vdash

↓ start symbol \Rightarrow string belongs to grammar.

Operator Precedence Grammar

- There should not be any ϵ production in grammar.
- No two non-terminals can occur together.

Eg.

$$E \rightarrow E + E / E - E / E * E / E / E / (E) / (- E)$$

Step 1: first we make Operator Precedence Table
then derive any string using it.

a < b \rightarrow b has higher precedence than a

a > b \rightarrow a " " b

a ≈ b \rightarrow a has equal precedence as b.

\rightarrow \$ < a or a > \$ (high priority)
 \rightarrow id > a [for given grammar, id has higher precedence than any terminal i.e. \$ (* + ^ etc.)]
high precedence
left to right
associative

Eg. $E \rightarrow E + E / E * E / id$

id	>	+	*	\$
+	-	>	>	>
*	<	>	<	>
\$	<	<	<	-

(end of string) IMP

operator precedence table.

(will check

precedence b/w any

all two symbols

or P. will replace

them with

precedence

sign

\Rightarrow \$ < id >

Consider < as opening bkt & > as closing. Now,

whenever we get >, find closest < &

we'll get an appropriate handle

final

→ \$ < . + < . * < . id > \$

→ \$ < . + < . * > \$

→ \$ < . + > \$ = \$ final

right associative.

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$\$ < \cdot * < \cdot (\underline{\underline{< id >}} \uparrow < id >) > - < id > / < id > \$$

$\vdash \text{id} : \text{id}$

$\vdash \text{let } x = \text{id} \text{ in } x \rightarrow \text{id}$

$\$ < \cdot * \& \cdot (\doteq) \Rightarrow - < \cdot \text{id} \cdot \Rightarrow | < \cdot \text{id} \cdot \Rightarrow |$

\$ < \cdot * \cdot > - < \cdot id \cdot > / < \cdot id \cdot > \$

$\$ < \cdot - \underline{\underline{< \cdot \text{id} \cdot >}} / < \cdot \text{id} \cdot > \$$

~~\$ <· - <· / <· id ·> \$~~

$\$ < \cdot - \cdot / \cdot \rangle \$$

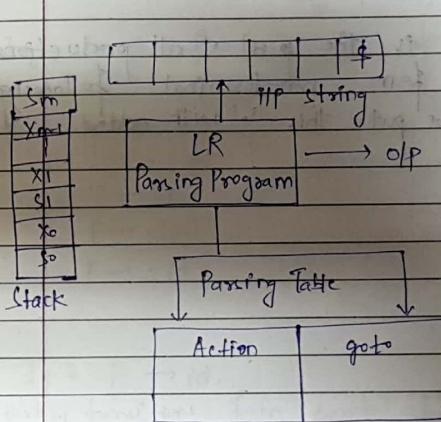
$$\frac{f(x_0)}{\|x-x_0\|} \geq f'(x_0)$$

LR-Parsers

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\downarrow
LSR Canonical LALR
(Simple LR)

* grammar generated is LR(1) only.



v) Left factoring & left recursion is not reqd.
to be removed.

→ LR grammar is a superset of LL grammar.

Augmented grammar

deriving start symbol of a given grammar
 with another symbol with unit production
 i.e. $E' \rightarrow E$, E is start symbol

$E' \rightarrow \cdot E$
 This is added in a production
 makes it an item.

E.g. $E' \rightarrow \cdot E \text{ Xa}$ if following terminals
 \downarrow
 It means our parser is expecting given
 string to be derived from production 'Exa'.
 In this case.

Note: Closure(item) is the set of all productions
 derived from non-terminal following
 1. If we put this \cdot with added
 productions.

Q. $E \rightarrow E + T$
 $E \rightarrow T$
 $T \rightarrow T * F$
 $T \rightarrow F$
 $F \rightarrow (E)$
 $F \rightarrow id$

A. Note: add, $E' \rightarrow F$ to make it augmented.

Now:	$I_0: E' \rightarrow \cdot E$	\cdot together makes closure(E)
Closure(E)	$E \rightarrow \cdot E + T$	
	$E \rightarrow \cdot T$	$I_0 \rightarrow$ equivalent
Closure(T)	$T \rightarrow \cdot T * F$	
	$T \rightarrow \cdot F$	all included in I_0 .
Closure(F)	$F \rightarrow \cdot (E)$	initial state
	$F \rightarrow \cdot id$	
• transitions from I_0 :		

(• is shifted)

Transition 1: $I_1: E' \rightarrow E.$
 $E \rightarrow E + T$
 closure of null + symbols which I can get for transition include the symbols following \cdot . Then symbols can be terminal as well as non-terminal. → make a new state for each transition symbol of add closure of each item in each state, which have been included.

Transition 2: $I_2: E \rightarrow T.$
 $T \rightarrow T * F$
 closure of null.

Transition 3: $I_3: T \rightarrow F.$

Transition 4: $I_4: F \rightarrow (\cdot E)$
 $E \rightarrow \cdot E + T$
 $E \rightarrow \cdot T$
 $T \rightarrow \cdot T * F$
 $T \rightarrow \cdot F$
 $F \rightarrow \cdot (E)$
 $F \rightarrow \cdot id$

$I_5: F \rightarrow id$.

Now start transitions from symbol I_1 :-

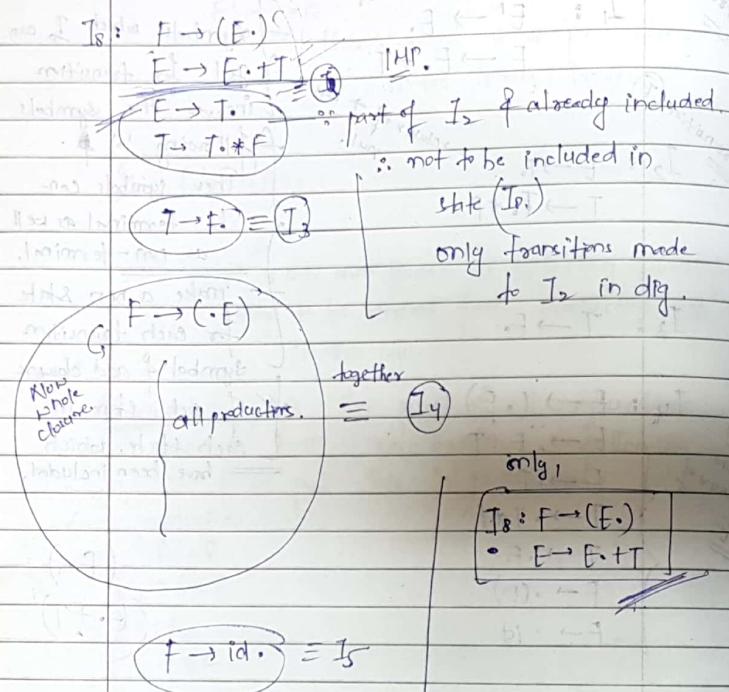
$I_6: E \rightarrow E + T.$
 $T \rightarrow \cdot T * F$
 $T \rightarrow \cdot F$
 $F \rightarrow \cdot (E)$
 $F \rightarrow \cdot id$

Transition from I_2 :-

$I_7: T \rightarrow T + F$
 $F \rightarrow \cdot (E)$
 $F \rightarrow \cdot id$

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transitions fr. I₄



I₉: $E \rightarrow E \cdot + T$

$T \rightarrow T \cdot * F$

$T \rightarrow F \cdot$ $\equiv I_3$ too far

$F \rightarrow (\cdot E)$

$\{ E\text{-closure} \}$

$F \rightarrow id \cdot$ $\equiv I_5$

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I₉: $F \rightarrow E \cdot + T$

$T \rightarrow T \cdot * F$

w-transition for I₇

I₁₀: $T \rightarrow T \cdot * F$

$F \rightarrow (\cdot E)$ $\equiv I_4$
 $\{ E\text{-closure} \}$

$F \rightarrow id \cdot$ $\equiv I_5$

I₁₀: $T \rightarrow T \cdot * F$

w-transition for I₈

I₁₁: $F \rightarrow (E \cdot)$

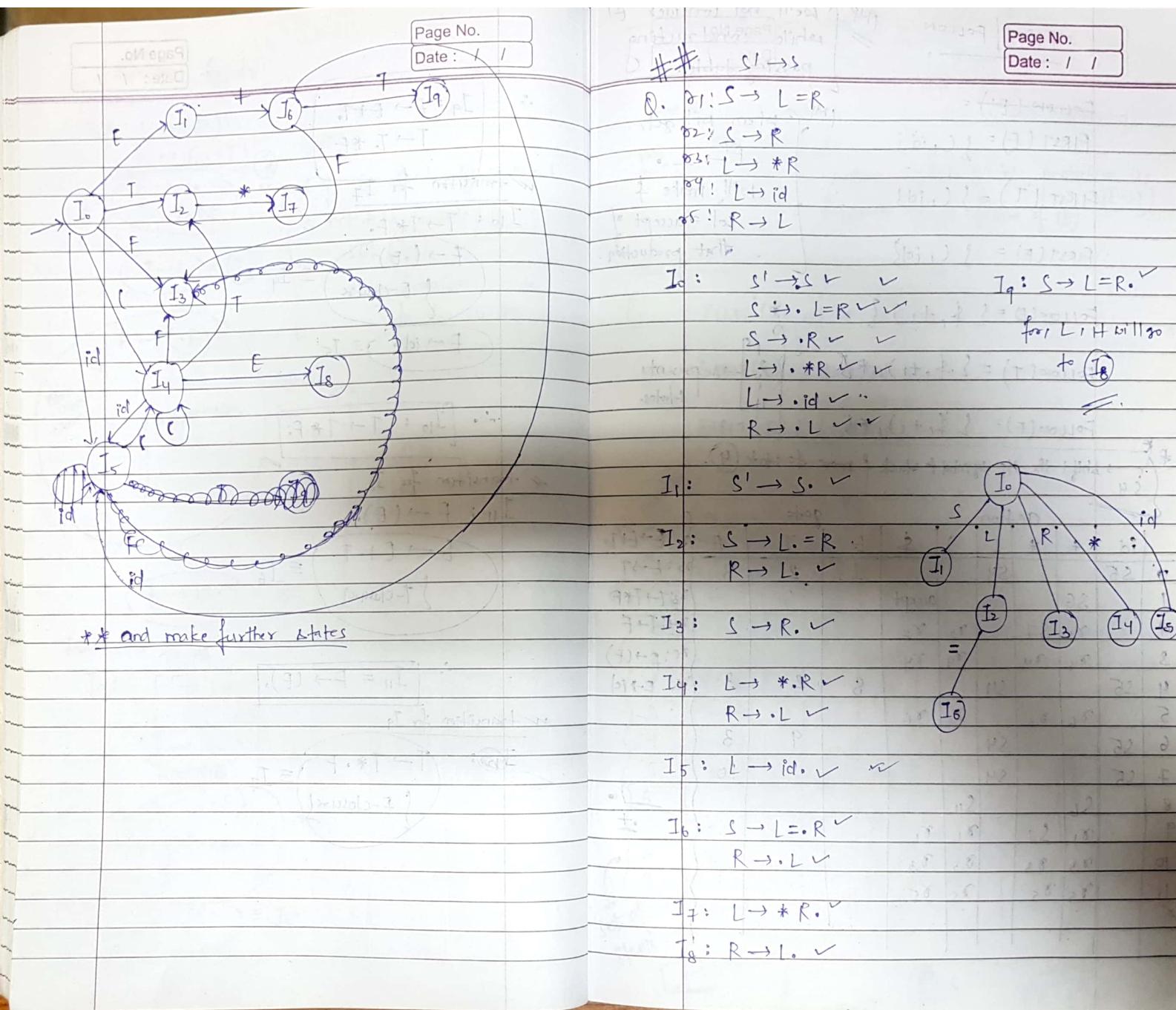
$E \rightarrow E \cdot + T$ $\equiv I_6$
 $\{ T\text{-closure} \}$

I₁₁ = $F \rightarrow (E \cdot)$

w-transition for I₉

I₁₂: $T \rightarrow T \cdot * F$

$\{ F\text{-closure} \}$ $\equiv I_7$



FIRST | FOLLOW

IMP
Well not consider E
while Page No. not writing
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parsing table.

$\text{Follow}(E) =$

$\text{FIRST}(E) = \{ \text{id} \}$

$\text{FIRST}(T) = \{ \text{id} \}$

$\text{FIRST}(F) = \{ \text{id} \}$

IMP token, will get

$E^* \rightarrow \cdot$

will make f

↓ ↓ col = accept of
that production.

$\text{Follow}(D) = \{ \$, +, * \}$

$\text{Follow}(T) = \{ \$, +, *, * \}$

$\text{Follow}(F) = \{ \$, +, *, * \}$

→ shift the imp symbol to stack & move to state 4.

		action						goto	
		*	()	\$	E	T	F	$r_1: E \rightarrow E + T$
0	S5		S4			1	2	3	$r_2: E \rightarrow T$
1	SG				accept				$r_3: T \rightarrow T * F$
2	r2 S7		r2 r2						$r_4: T \rightarrow F$
3	r4 r4		r4 r4						$r_5: F \rightarrow (E)$
4	S5		S4			8	2	3	$r_6: F \rightarrow id$
5	r6 r6		r6 r6						
6	S5		S4			9	3		
7	S5		S4			10			
8	S6		S11						
9	r1 S7		r1 r1						
10	r3 r3		r3 r3						
11	r5 r5		r5 r5						

→ Name all productions

$E \rightarrow id$

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→ if, we'll get '\$' at the end (not augmented)
the reduce it.

$E \rightarrow T$ → reduce T with E i.e. production r_2
& write it in follows of (E) ($E \rightarrow T$)

Q. $\text{FIRST}(S) = \{ *, id \}$

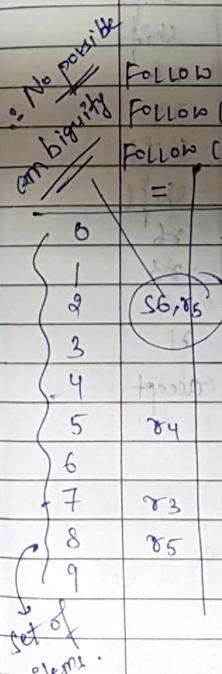
$\text{FIRST}(L) = \{ *, id \}$

$\text{FIRST}(R) = \{ *, id \}$

$\text{Follow}(S) = \{ \$ \}$

$\text{Follow}(L) = \{ \$, \$ \}$

$\text{Follow}(R) = \{ \$, \$ \}$



$E \rightarrow T$

F

A.R

$\frac{S(AB)}{(L=E)AB}$

Q. #

$$r_1 : E \rightarrow ET$$

(bottom part: bag add to L1 bag 11001)

$$r_2 : E \rightarrow T$$

(bottom part: T → T)

$$r_3 : T \rightarrow T * F$$

$$r_4 : T \rightarrow F$$

$$r_5 : F \rightarrow (E)$$

$$r_6 : F \rightarrow id$$

only states come

come

move to

5

on at

id we

move to

5

on at

CANONICAL LR PARSER

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$$S \rightarrow CC : n$$

$$C \rightarrow CC : n$$

$$C \rightarrow d : n$$

↓ will follow \circlearrowleft .

$$I_1: S^* \rightarrow S \cdot, \$ \checkmark$$

$$S \rightarrow C \cdot C, \$ \checkmark$$

$$C \rightarrow C \cdot C, C \cdot d \checkmark$$

$$C \rightarrow d \cdot, C \cdot d \checkmark$$

LHP.

(When making transition)

i.e. shifting dot,

copy follow of parent production.

$$I_2: S^* \rightarrow S \cdot, \$ \checkmark$$

$$C \rightarrow C \cdot C, \$ \checkmark$$

while taking closure,
find follow using $S \rightarrow C \cdot C$

$$C \rightarrow \cdot d, \$ \checkmark$$

$$I_3: C \rightarrow C \cdot C, C \cdot d \checkmark$$

using $C \rightarrow c \cdot C$

$$C \rightarrow \cdot d, C \cdot d \checkmark$$

$$I_4: C \rightarrow d \cdot, C \cdot d \checkmark$$

$$I_5: S \rightarrow C \cdot C, \$ \checkmark$$

$$I_6: C \rightarrow c \cdot C, \$ \checkmark$$

$$C \rightarrow \cdot C, \$ \checkmark$$

$$C \rightarrow d, \$ \checkmark$$

* { SLR

Parser is a weak parser.

There are chances of both shift & reduce.

for same symbol.

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→ Canonical Parser is comparatively strong than SLR.

(But no. of states in canonical parser are high.)

∴ More computation.

In canonical, to reduce avoid shift & reduce, we use lookahead symbol.

∴ It has, (LR(0)) set of symbol items.

In SLR, we do not use lookahead.

∴ High chances of shift and reduce.

It has (LR(0)) set of items.

CANONICAL

RULES: $A \rightarrow d \cdot B \beta, a$
 $B \rightarrow \text{_____}, \text{first}(B) \neq \epsilon \text{ if } \beta \text{ is } \epsilon.$
 $\text{follow}(A)$

$$I_7: C \rightarrow d \cdot, \$ \checkmark$$

$$I_8: C \rightarrow C \cdot C, C \cdot d \checkmark$$

$$I_9: C \rightarrow C \cdot C, \$ \checkmark$$

	c	d	\$	s	c
0	s3	s4	1	2	
1			accept		
2	s6	s7		5	
3	s3	s4		8	
4	s3	s3			
5			21	minimise beginning state	
6	s6	s7	108	(109)	9
7			83		
8	s2	s2	110	for ob 104 111 110	
9			82	10 110 111 112	

* LALR Parser
 advantages:
 no. of states
 got reduced.

LALR Parser
 (here, we will merge)
 states whose productions
 are same and lookahead
 are diff.

	c	d	\$	s	c
0	s36	s47		1	2
1			accept		
2	s36	s47		5	
36	s36	s47		89	
47	s3	s3	83		
5			81		
89	s2	s2	82		

* LALR is less efficient than canonical

Semantic Analysis

⇒ When we put values of variables then it forms a annotated parse tree.

↳ Inherited attribute : (inherit parent value)

↳ Synthesized attribute : (combine values of two nodes)

↳ Annotated parse tree is ill^{ta} to intermediate code generation.

Intermediate Code Generation :-

i) Post-fix notation

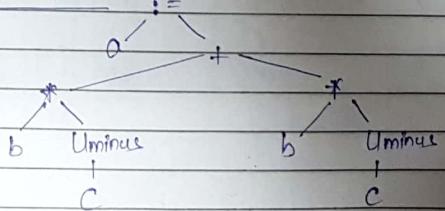
ii) Syntax tree / DAG → Directed acyclic graph.

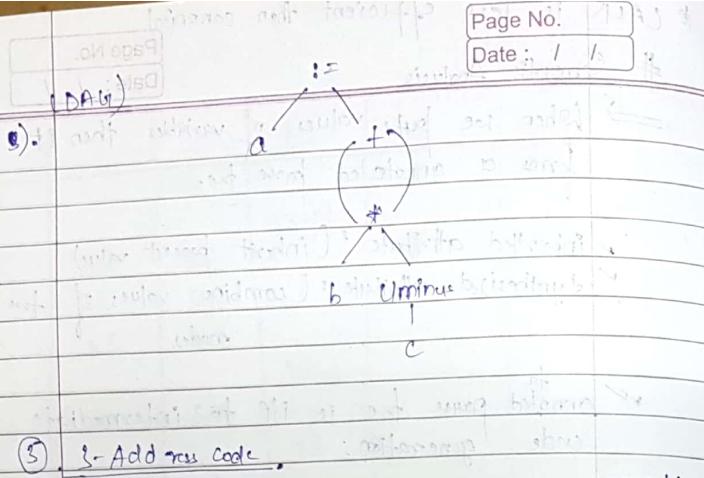
iii) 3-address code

Eg. $a := b * -c + b * -c$ unary operator

①. $abc -* bc -* + :=$ (post-fix)

②. Syntax Tree





3. Address code

1. $t1 = -c$
2. $t2 = b * t1$
3. $t3 = -c$
4. $t4 = b * t3$
5. $t5 = t2 + t4$
6. $a = t5$

} all the temporary needs
for being entered in
symbol Table.

variables

i) Quadruples (4 col)

	OP	arg1	arg2	result
(0)	-	c		t1
(1)	*	b	t1	t2
(2)	-	c		t3
(3)	*	b	t3	t4
(4)	+	t2	t4	t5
(5)	=	a	t5	a

there are
ptr to
symbol
table
for
corresponding
values.

ii) Triples (3 col)

	OP	arg1	arg2
(0)	-	c	
(1)	*	b	(0)
(2)	-	c	
(3)	*	b	(2)
(4)	+		(3)
(5)	=	a	(4)

(instead of
writing temporary
variables,

loc
mention
address of
refer con-
taining
those.
operations.

JIT Kharakpur
TV Chhattisgarh

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Indirect Triples:

	op	arg1	arg2	result
(14)	*	c		
(15)	*	b	(16)	
(16)	-	c		
(17)	*	b	(16)	
(18)	+	(1)	(17)	
(19)	=	a	(18)	

	op	arg1	arg2	result
(14)	*	c		
(15)	*	b	(16)	
(16)	-	c		
(17)	*	b	(16)	
(18)	+	(1)	(17)	
(19)	=	a	(18)	

Eq $x[i] := y$

$x[i] = y$ (for RHS)

	op	arg1	arg2	op	arg1	arg2
(10)	[] :=	n	i	(10)	= []	y
(1)	=	(10)	y	(1)	=	n

Quadruple
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$n[i] = y$

op	arg1	arg2	result
$=$	n	i	$t1$
$=$	y		$t1$

op	arg1	arg2	result
$=$	y	i	$t1$

param n1
param n2
param n3

Call p.3

return
return y.

switch case
break

(multiple cases)

if y[i] == t1

```

begin
    prod := 0;
    i := 1;
do begin
    prod := prod + a[i] * b[i];
    i := i + 1;
end
while i <= 20
end.

```

→ no need to write 3-address code for keywords.
such as begin, end, int, float, etc.

This info is already present in symbol Table.

→ Assign line no. to each 3-address code.

3-Address code :-

- (1) prod = 0
- (2) i = 1
- (3) if (i > 20) goto (12) IHP.
- (4) t1 = i * 4 // here we are assuming, int takes 4 bytes.
- (5) t2 = a[t1]
- (6) t3 = i + 4
- (7) t4 = b[t3]
- (8) t5 = t2 + t4
- (9) t6 = prod + t5
- (10) prod = t6
- (11) i = i + 1
- (12) goto (10) if (i <= 20) goto (4)
- (13) exit

* IHP

eg. foo (i=0; i< 5; i++)

(1) ↓ (2) → always reverse
of this in
if condition

(3) ↓

(1) i = 0 → this label is
end for loop
(2) if (i >= 5) goto (1) as to be
encountered in
next step

In address code for body
of loop.

(1) i = i + 1 // inc. statement at last of (1)
(2) goto (2)
(3) Exit

→ Similarly for do while and while loop.
→ In case of do while include if condition
at last of body.

* By default assume array to be of int type i.e. 4 bytes
else according to the given que.

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function

```
Eg: int dot_product (int n[], int y[])
{
    int d, i;
    d=0;
    for (i=0; i<10; i++)
        d += n[i] * y[i];
    return d;
}
```

3 Address code :-

- (1) func begin dot product
- (2) d=0
- (3) i=0
- (4) if (i >= 10) goto (14)
- (5) t1 = 4*i
- (6) t2 = n[t1]
- (7) t3 = 4*j
- (8) t4 = y[t3]
- (9) t5 = t2*t4
- (10) t6 = d + t5
- (11) d = t6
- (12) i = i+1
- (13) goto (4)
- (14) return d

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Void quicksort (m,n)

```
int m, n;
int i, j;
int v, n;
if (n < m) return;
i = m-1; j = n; v = a[n];
while (1)
    do i = i+1; while (a[i] < v);
    do j = j-1; while (a[j] > v);
    if (i >= j) break;
    n = a[i]; a[i] = a[j]; a[j] = n;
```

$n = a[i]; a[i] = a[n]; a[n] = n;$

quicksort(m, i);
quicksort(j+1, n);

no need to write 3 address code for these lines.

- ✓ (1) i = m-1
- (2) j = n
- (3) t1 = 4*n
- (4) v = a[t1]
- ✓ (5) i = i+1
- (6) t2 = 4*i
- (7) t3 = a[t2]
- (8) if (t3 < v) goto (5)
- ✓ (9) j = j-1
- (10) t4 = 4*j
- (11) t5 = a[t4]
- (12) if (t5 > v) goto (9)

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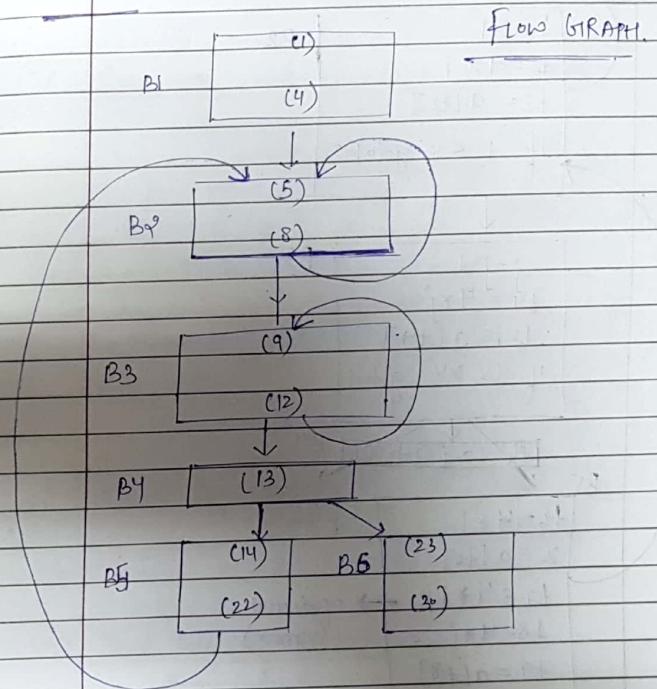
✓(13) if ($i \geq j$) goto (23)
 ✓(14) $t_6 = 4 * i$
 (15) $n = a[t_6]$
 (16) $t_7 = 4 * i$
 (17) $t_8 = 4 * j$
 (18) $t_9 = a[t_8]$
 (19) $a[t_7] = t_9$
 (20) $t_{10} = 4 * j$
 (21) $a[t_{10}] = n$
 (22) goto (5) // no condition is checked because while(1), i.e. loop will continue.
 ✓(23) $t_{11} = 4 * i$
 (24) $n = a[t_{11}]$
 (25) $t_{12} = 4 * i$
 (26) $a[t_{11}] = a[t_{12}]$
 (27) $t_{13} = 4 * n$
 (28) $t_{14} = a[t_{13}]$
 (29) $t_{15} = 4 * n$
 (30) $a[t_{15}] = n$

After writing 3 address code, we create a flow chart. For this we use basic block containing a set of statements.

Here we need to decide 'leader' for each block.

first statement in block.

→ Starting statement
 → LEADER : → target statement of conditional
 & unconditional statement.
 → statement next to the conditional & unconditional jump statement.



Optimisation → Local optimisation (within a block)

→ Global optimisation (between diffn blocks)
 ↗ if a statement is not changing in b/w

$i = m - 1$
 $j = n$
 $t_1 = 4 * n$
 $v = a[t_1]$

$i = i + 1$
 $t_2 = 4 * i$
 $t_3 = a[t_2]$
 if $t_3 < v$ goto B2

$j = j - 1$
 $t_4 = 4 * j$
 $t_5 = a[t_4]$
 if $t_5 > v$ goto l

$t_6 = 4 * i$
 $x = a[t_6]$
 $t_7 = 4 * j$ → optimised
 $t_8 = 4 * j$ (remove)
 $t_9 = a[t_8]$
 $a[t_7] = t_9$
 $t_{10} = 4 * j$ X → optimised
 $a[t_{10}] = x$ (remove)
 goto B2

B2

B3

B4

B5

B6

- ① Common Subexpression elimination
- ② Dead Code elimination
- ③ Renaming temporary variables
- ④ Interchange of statements.
- ⑤ Algebraic transformation.

① Common Subexpression elimination

$$\begin{aligned}
 a &= b + c \\
 b &= a - d \quad \text{not changing in b/w} \\
 c &= b + c \quad (\because b \text{ is changing in b/w}) \\
 d &= a - d \\
 \end{aligned}$$

redundant.

as $a \neq d$ as c not changing.

$a = b + c$

$b = a - d$

$c = b + c$

$d = b$

② Dead Code elimination.

$\text{debug} = \text{false}$

$\text{if } (\text{debug})$

{ }

this code will never
get executed as,
 $\text{debug} = \text{false}$ in
beginning only.

remove dead code

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(3) Renaming temporary variables.

$t = a + b$

\downarrow

$u = a + b$

(4) Interchange of statements

$t_1 = a + b$

$t_2 = c + d$

\downarrow

We can change order of statements.

(5) Algebraic transformation

$x = x + 0$ can be removed.

$x = x * 1$

$p = a^2 \rightarrow p = a * a$

Time consumed is less as compared to power operation.

(6) Copy Propagation

$a = b$

$b = a$

Here we try to remove occurrence of a variable.

(common SubExp elimination) \downarrow applying local optimisation on B5.

$\Rightarrow t_6 = 4 * i \rightarrow x \quad (t_2)$

$n = a[t_6]$

$t_8 = 4 * j \rightarrow x \quad (t_4)$

$t_9 = a[t_8]$

$a[t_8] = t_9$

$a[t_8] = n$

goto B2

(Common SubExp Elim)

\downarrow applying global optimisation.

$x = a[t_2]$

$t_9 = a[t_4]$

$a[t_2] = t_9$

$a[t_4] = x$

goto B2

↓ global (Common SubExp Elim)

$x = t_3$

$t_9 = t_5$

$a[t_2] = t_9$

$a[t_4] = x$

goto B2

↓ (Copy propagation)

$x = t_3$

$t_9 = t_5$

$a[t_2] = t_5$ (Dead)

$a[t_4] = t_3$ (Code Elim)

goto B2

Note: B6 block,

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$$t_{11} = 4 * i \times$$

$$n = a[t_{11}]$$

$$t_{12} = 4 * j \times$$

$$t_{13} = 4 * n \times$$

$$t_{14} = a[t_{13}]$$

$$a[t_{12}] = t_{14}$$

$$t_{15} = 4 * n \times$$

$$a[t_{15}] = n$$

$$\downarrow$$

$$n = a[t_2]$$

$$t_{14} = a[t_1] \rightarrow (\text{can't be replaced})$$

$$a[t_2] = t_{14} \quad \text{because in while}$$

$$a[t_{15}] = n \quad \text{if may happen,}$$

t_1 becomes equal

to some other

temp variable & that

index value may get

changed.

$$\downarrow$$

$$n = t_3$$

$$t_{14} = a[t_1]$$

$$a[t_2] = t_{14}$$

$$a[t_{15}] = \cancel{t_3} n$$

$$\downarrow$$

$$n = t_3$$

$$t_{14} = a[t_1] \rightarrow$$

$$a[t_2] = \cancel{t_3} t_{14}$$

$$a[t_{15}] = t_3$$

$$\downarrow$$

$$t_{14} = a[t_1]$$

$$a[t_2] = \cancel{t_3} t_{14}$$

$$a[t_{15}] = t_3$$

Loop OPTIMISATION TECHNIQUES

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→ Code motion

→ Induction-variable elimination

→ Reduction in strength

①. Code motion.

loop invariant codes are shifted outside the loop

limit = 10

Ex. while ($i \leq \underline{\underline{\text{limit}} - 2}$)

$$\left\{ \begin{array}{l} i=2 \\ \vdots \end{array} \right.$$

$$\begin{aligned} &\text{reduced} \\ &\text{compiling} \\ &\text{limit} = 10 \\ &t = \text{limit} - 2 \\ &j = 2 \\ &\text{while} (i \leq t) \\ &\{ \\ &\} \end{aligned}$$

②. Reduction in strength → can be in loop or elsewhere also.

Ex. $P * P = P + P$

computational cost reduces.

③. Induction-variable elimination

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B3
 $i = j - 1$
 $t_4 = 4 * i$
 $t_5 = a[t_4]$
 if $t_5 > v$ goto B3
 ↓
 B4 if $i > j$ goto B6
 ↓
 (9)

induction variables.
 t_4 is changing by change in i and every time it is reducing by diff of i .

$j = j - 1 \rightarrow$ can't be removed as it is being composed in next block.
 $t_4 = t_4 - 4$
 above, it is not correct.
 as needs to be updated
 as t_4 is not initialised.

other blocks

B1
 $i = m - 1$
 $j = n$
 $t_1 = 4 * n$
 $v = a[t_1]$

B2
 $i = i + 1$
 $t_0 = t_4 * 4$
 $t_3 = a[t_0]$
 induction.
 if $t_3 < v$ goto B2
 ↓
 (R2)

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B1
 $i = m - 1$
 $j = n$
 $t_1 = 4 * n$
 $v = a[t_1]$
 $t_2 = 4 * i$
 $t_4 = 4 * j$

B2
 $t_0 = t_4 + 4$
 $t_3 = a[t_0]$
 if $t_3 < v$ goto B2
 ↓

B3
 $t_4 = t_4 - 4$
 $t_5 = a[t_4]$
 if $t_5 > v$ goto B3
 ↓

B6
 if $t_2 >= t_4$ goto B6