Total No. of Questions : 8]

SEAT No. : [ ]

PB4275

[Total No. of Pages : 2

[6264]-254

F.Y. M.E. (E & TC) (IoT & Sensor System)

EMBEDDED SYSTEM DESIGN

(2017 Pattern) (Semester - II) (504608)

Time : 3 Hours] ***[Max. Marks : 50***

Instructions to the candidates:

1) ***Attempt any 5 questions out of 8.***
2) ***Neat diagrams must be drawn whenever necessary.***
3) ***Figures to the right of questions indicate full marks.***
4) ***Assume suitable data, if necessary.***

Q1) a) Enlist any five characteristics of embedded systems. **[5]**

b) Describe iteration and refinement of the partitioning. **[5]**

Q2) a) Define product survey. **[5]**

b) What are the specification need for hardware and software. **[5]**

Q3) a) Explain different process technology. **[5]**

b) Difference between IC technology and design technology. **[5]**

Q4) a) Explain below concept, **[5]**

i) Software and interfacing

ii) Memory

iii) FPGA design

b) Explain re-engineering process. **[5]**

P.T.O.

Q5) a) Explain the concept of validation and development of hardware and software. **[5]**

b) What are different tools of testing and their selection criteria. **[5]**

Q6) a) What is turnkey product design. **[5]**

b) Explain **[5]**

i) firm wire design

ii) driver types and its development

Q7) a) Explain any two real life embedded products. **[5]**

b) Explain aspect of Mechanical packing and testing. **[5]**

Q8) a) Define embedded product area of technology. **[5]**

b) Why EMI/RFI certification is important for embedded system. **[5]**

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