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[5671]-262
M.E. (E&TC-VLSI & Embedded Systems)
RECONFIGURABLE COMPUTING
(2017 Pattern) (Semester - I)

Time : 3 Hours] *[Max. Marks : 50*

Instructions to the candidates:

1) *Answer any five questions from questions (Q.1. to Q.8.)*

2) *Neat diagrams must be drawn wherever necessary.*

3) *Figures to the right side indicate full marks.*

4) *Assume Suitable data wherever necessary*

5) *Use of Calculator is allowed.*

Q1) a) What are the different types of partial reconfiguration? Explain any one**[4]**

b) Explain difference between reconfigurable machines and conventional processor **[3]**

c) Define: **[3]**

   i) Run Time

   ii) Reconfigurable

   iii) Specialization

Q2) a) With labeled diagram, explain PAM as RC platform. **[4]**

b) Explain Pipeline and block Reconfigurable architectures. **[3]**

c) Elaborate application of Reconfigurable Computing. **[3]**

Q3) a) Explain DSP processor as domain specific processor .Give example of DSP processor. **[5]**

b) What do you mean by Context? Explain various types of context with diagram. **[5]**

Q4) a) Explain in detail the main components of system on programmable Chip **[5]**

b) Explain Relocation and Defragmentation w.r.t. RC point of view. **[5]**

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Q5) a) Explain Non -frequently reconfigurable systems &its applications. **[5]**

b) Explain FPGA flow design with the help of flow diagram. **[5]**

Q6) a) Draw and explain the transfer of system from PCB to system on programming chip. **[5]**

b) What are the Computational characteristics of FPGAs? Give different FPGA design tools. **[5]**

Q7) a) What are the communication protocol involved in a network. **[5]**

b) State the application of RC in Video Streaming. **[5]**

Q8) a) Explain Architecture of Adaptive Cryptographic Systems. **[5]**

b) Explain High performance computing w.r.t. FPGA. **[5]**

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