| Total       | l No.      | of Questions : 8] SEAT No. :  |
|-------------|------------|---|
| <b>P.7</b>  | 599        | [Total No. of Pages : 3   |
| 1 - /       |            | [6180]-116  |
|             |            | T.E. (E & TC)   |
|             |            |   |
|             |            | POWER DEVICES AND CIRCUITS  |
|             |            | (2019 Pattern) (Semester - II) (304194)   |
|             |            |   |
| Time        | 2:24       | [Max. Marks: 70   |
|             |            | ons to the candidates:  |
| 110001      | 1)         | Answer Q1 or Q2, Q3 or Q4, Q5 or Q6 and Q7 and Q8.  |
|             | 2)         | Neat diagrams and Waveforms must be drawn wherever necessary.   |
|             | 3)         | Figures to the right side indicate full marks.  |
|             | 4)         | Use of nonprogrammable calculator is allowed.   |
|             | <i>5</i> ) | Assume suitable data if necessary.  |
|             |            |   |
|             | 7          |   |
| <b>Q</b> 1) | a)         | Explain working of single phase half bridge inverter (using MOSFET/IGBT)  |
| ~           | ,          | for R-L load with input & output waveforms. [7]   |
|             | b)         | Single phase full bridge inverter is operated from 50V dc supply, it has a  |
|             |            | resistive load of $R=5\Omega$ . Find: [6]   |
|             |            | i) RMS o/p voltages at third & fifth harmonic $(V_{O3} \& V_{O5})$  |
|             |            | ii) Distortion factor (DF) of 3 <sup>rd</sup> harmonic component  |
|             |            | iii) Total Harmonic Distortion (THD)  |
|             | c)         | Distinguish between freewheeling diode with feedback diode. [4]   |
| (12)        | ۵)         | OR What is mean by how aniss in inventors? Evaloin affects of howeflying [5]  |
| <b>Q</b> 2) | a)         | What is mean by harmonics in inverters? Explain effects of harmonics.[5]  |
|             | b)         | Draw a three phase inverter for balanced star R load? Explain its operation of 180° mode with gate signals & output waveforms. [12] |
|             |            | [12]  |

Q3) a) Explain working of step down chopper for R load and derive an expression for its average o/p voltage? [6]

b) Explain with block schematic working of SMPS. [6]

- c) A step down chopper is operated from dc supply voltage of 230V. It has resistive load with  $R=10\Omega$ . If duty cycle is 40%, calculate: [6]
  - i) Average & rms o/p voltages
  - ii) Average & rms o/p currents
  - iii) Chopper efficiency

A step up chopper is operated from 220V dc supply and it provides **Q4**) a) 550V output. If chopping frequency is 1KHz, calculate ON & Off times of chopper. [4] What are various types of choppers? Explain operation of two quadrant b) chopper with circuit diagram. [8] Draw circuit diagram of step up chopper and distinguish between step c) up & step down choppers. [6] **Q5**) a) What are different over voltage protection techniques in power electronics? Explain any one in detail. [7] Why isolation is required in power electronic circuits? Explain with neat b) diagram working of isolation transformer. [6] For a thyristor, Maximum junction temperature is 150°C. The thermal resistances are  $\emptyset_{IC} = 0.16$  °C/W,  $\emptyset_{CS} = 0.08$  °C/W. for heat sink temperature of 60°C, calculate total average power loss in thryistor - sink combination. If heat sink temperature is reduced to 50°C, find new total average power loss in thryistor - sink combination. [4] What is resonant converter? What are its various types? Explain any one **Q6**) a) resonant converter with circuit & waveforms. What is EMI? Explain various sources & minimizing techniques of EMI. b) Explain the role of heat sink in power electronic circuits with its thermal c) equivalent circuit [4] **Q7**) a) Explain with circuit diagram single phase full wave for R-load. Also draw following waveforms [7] i) Input voltage ii) Gate signals for power devices

iii)

iv)

Output voltage

Output current

|             | b)    | What is UPS? Explain operation of Off-line UPS with block schematic. | [6]              |
|-------------|-------|--|------------------|
|             | c)    |  | 5]               |
|             |       | OR   |                  |
| <b>Q</b> 8) | a)    | Explain various performance parameters of batteries used in batte    | ry<br><b>6</b> ] |
|             | b)    |  | <b>6</b> ]       |
|             | c)    | Explain working of electronic ballast with block schematic. [        | <b>6</b> ]       |
| [618        | 80]-1 | Explain working of electronic ballast with block schematic.          | 652              |