Total No. of Questions: 8]	90	SEAT No.:
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EMBEDDED SYSTEM DESIGN

M.E. (VLSI & Embedded System) (E & TC) (2017 Pattern) Time: 3 Hours] [*Max. Marks* : 50 Instructions to the candidates: Answer any five questions. 1) 2) Figure to the right indicate full marks. Assume suitable data, whenever necessary. 3) Explain various design metrics of embedded system design. **Q1**) a) [5] Dist different life cycle models and explain the V type model in embedded b) system design. [5] List the different software tools. Explain the software tools in brief **Q2**) a) (1) Cross compiler (2) Linkers (3) Loader. [5] Discuss the features, IDE and applications of Raspberri Pi microcontroller. Explain the different features of LPC 1768 ARM Cortex. How it has **Q3**) a) improvements over classical series? [5] What is the need of Pin Connect Block in LPC2148? Explain the role of b) PINSELx registers. [5] Draw and explain Cortex Microcontroller Software Interface Standard **Q4**) a) (CMSIS) structure. [5] Explain with suitable diagram, the Ethernet protocol with respect to LPC b)

1768. [5]

Q 5)	a)	What is Linux Kernel configuration? Give steps for Linux Kernel configuration.	rnel [5]		
	b)	Explain various storage considerations in Embedded Linux.	[5]		
Q6)	a)	Compare BIOS v/s Boot loader.	[5]		
	b)	Explain the file structure used in Embedded Linux.	[5]		
Q 7)	a)	Explain design of Digital camera as Embedded system case study.	[6]		
	b)	Discuss the reliability and failure analysis in Embedded system.	[4]		
Q 8)	a)	Explain design of Automated meter reading as Embedded system of study.	ease [6]		
	b)	Explain types of documents to be prepared for embedded real t			
		products.	[4]		
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