Total No	o. of Questions : 10] SEAT No. :	٦
P2435	[Total No. of Pages :	2
	[5253] - 158	
	T.E. (E & TC)	
	Embedded Processor	
Time .	(2012 Pattern) (Semester - II) [Max. Marks : 7	70
	tions to the candidates :	U
1)		
2)		
3)		
4)	and all the standards and the standards are standards and the stan	et
5)	calculator and steam tables is allowed.  Assume suitable data if necessary.	
0)	Table 1 and 3 houses y.	
<b>Q1</b> ) a)	Catculator and steam tables is allowed.  Assume suitable data if necessary.  State the features of LPC2148.  Explain instructions:  i) BICEQ r2, r7, #7  ii) ADDEQ r2,r3  iii) CMP r2, r1  iv) SUBGT r1,r2,#5  Explain the DINSEL resisters	4]
<b>2</b> 1) u) b)	Explain instructions:	., []
U)	i) DICEO 2 27 #7	r J
	i) BICEQ r2, r7, #7	
	ii) ADDEQ r2,r3	
	iii) CMP r2, r1	
	iv) SUBGT r1,r2,#5	
c)	Explain the PINSEL registers [2	2]
	i) PINSEL0	
	ii) PINSEL1	
	OR	
<b>Q2</b> ) a)	State significance of PLL and explain VPB divider block of LPC2148.[4	<b>4</b> ]
b)	Draw interfacing diagram of keypad with LPC2148 and write algorithm	m
	or draw flowchart for the same.	<b>6</b> ]
<b>Q3</b> ) a)		<b>4</b> ]
b)		
		<b>6</b> ]
	OR	
<b>Q4</b> ) a)		d
	'C' program for the same.	<b>6</b> ]
b)	Explain UART block of LPC2148.	<b>4</b> ]

<b>Q</b> 5)	a)	Explain thread and handler modes of cortex M3 with the help of state diagram. [8]
	b)	Compare ARM CORTEX A, CORTEX M, CORTEX R processor series. [8]
<b>Q6</b> )	a)	Explain CMSIS standard. [8]
20)	b)	Explain the need of operating system in developing complex applications
	0)	in embedded system. [8]
<b>Q</b> 7)	a)	Interface DC motor with LPC1768 and write a 'C' program to control
21)	u)	the speed of DC motor using PWM signal with 60% duty cycle. [8]
	b)	Draw and explain block diagram of LPC1768. [8]
		OR
<b>Q</b> 8)	a)	Explain clock and power control block of LPC1768. [8]
	b)	Interface RGB LED to LPC1768 and write a 'C' program to display red,
		blue and green color with some delay. [8]
<b>Q9</b> )	Writ	e short note on followings. [18]
	a)	USB
	b)	CAN
	c)	NVIC
Q10	)a)	Explain Ethernet based communication using Microcontroller. [9]
	b)	Explain any four GPIO registers of LPC1768 [9]
		Explain Ethernet based communication using Microcontroller.  [9] Explain any four GPIO registers of LPC1768  [9]