

# CHANKYU LEE

◇ **Address:** EE 350, 475 Northwestern Ave, West Lafayette, IN, 47907

◇ **Phone:** (+1) 765 337 4551 ◇ **Email:** lee2216@purdue.edu ◇ **Website:** chan8972.github.io

## RESEARCH FOCUS

---

Efficient and robust deep learning, and computer vision for event-based camera

## EDUCATION

---

**Purdue University, West Lafayette, IN, USA**

*Fall 2015 - present*

Ph.D., Electrical and Computer Engineering (Advisor: Prof. Kaushik Roy)

- Thesis Title: "Training Deep Spiking Neural Networks: Enabling Spike-based Learning"

**Sungkyunkwan University (SKKU), South Korea**

*Spring 2009 - Spring 2015*

B.S., Electrical and Electronics Engineering

**Hong Kong University of Science and Technology (HKUST)**

*Fall 2013*

Exchange Student Program, Electronic and Computer Engineering

## EXPERIENCE

---

**Nanoelectronics Research Laboratory, Purdue University**

*Aug. 2016 - present*

*Graduate Research Assistant (Funded by Center for Brain Inspired Computing, West Lafayette, IN one of six centers in JUMP, a SRC program, Advisor: Prof. Kaushik Roy)*

- Exploratory research on energy-efficient and robust machine learning, overcoming the limitations of current artificial intelligence through algorithm-hardware co-design.
- Proposed an approximate derivative method to overcome the discontinuous, non-differentiable nature of the spike generation function in Spiking Neural Networks (SNNs). This method enables the training of deep convolutional SNNs, such as VGG and ResNet architectures, using backpropagation algorithm. [*Frontiers in Neuroscience*, 2020]
- Presented a pre-training scheme using a biologically plausible unsupervised learning, namely spike timing dependent plasticity, to better initialize the parameters in deep SNNs. This semi-supervised approach offers three advantages: faster convergence, enhanced robustness and better generalization. [*Frontiers in Neuroscience*, 2018]
- Developed motion estimation algorithms for an event-based camera in challenging scenes such as high speed and wide dynamic range. More specifically, I designed novel discretized input representation and hybrid neural architecture for estimating optical flow using event sensors. [*ECCV 2020*, *Demo Video*]
- My ongoing work includes an exploration of sensor fusion techniques to accurately predict optical flows by leveraging the complementary characteristics of sparse event data and dense frame-based images.

**Access Research Laboratory, Bell Labs, Nokia**

*Jun. 2018 - Aug. 2018*

*Graduate Internship (Supervisor: Dr. Hungkei Chow and Mr. Joseph Galaro) Murray Hill, NJ*

- Built a compiler for mapping and scheduling convolutional neural network algorithms on a wave-front MIMD (Multi-Instruction Multi-Data) processor toward accelerated and energy-efficient AI computing.

**Graduate School of Convergence Science and Technology, Seoul National University**

*Jul. 2014 - Sep. 2014, Undergraduate Internship (Advisor: Prof. Yoonkyu Song) Suwon, South Korea*

- Research on mid-field wireless powering for simulating neural signals in the brain-machine interface.
- Designed a full-wave synchronous four-transistor cell rectifier, fed in parallel into each stage through a pump capacitor in implantable bio-chip antenna transmissions.

## ACADEMIC PUBLICATION

---

### • International Conference

1. **Chankyu Lee**, Adarsh Kumar Kosta, Alex Zihao Zhu, Kenneth Chaney, Kostas Daniilidis and Kaushik Roy, “Spike-FlowNet: Event-based Optical Flow Estimation with Energy-Efficient Hybrid Neural Networks”, *In Proceedings of the European Conference on Computer Vision (ECCV) 2020*.
2. Gopalakrishnan Srinivasan, **Chankyu Lee**, Abhronil Sengupta, Priyadarshini Panda, Syed Shakib Sarwar and Kaushik Roy, “Training Deep Spiking Neural Networks for Energy-Efficient Neuromorphic Computing”, *International Conference on Acoustics, Speech, and Signal Processing (ICASSP) 2020, Virtual Barcelona*
3. Saima Sharmin, Priyadarshini Panda, Syed Shakib Sarwar, **Chankyu Lee**, Wachirawit Ponghiran and Kaushik Roy, “A Comprehensive Analysis on Adversarial Robustness of Spiking Neural Networks”, *International Joint Conference on Neural Networks (IJCNN) 2019, Budapest, Hungary*

### • International Journal

1. **Chankyu Lee\***, Syed Shakib Sarwar\*, Priyadarshini Panda, Gopalakrishnan Srinivasan and Kaushik Roy, “Enabling Spike-based Backpropagation for Training Deep Neural Network Architectures” (\*Equally Contributing Authors), *Frontiers in Neuroscience, Neuromorphic Engineering, 2020*.
2. **Chankyu Lee**, Priyadarshini Panda, Gopalakrishnan Srinivasan and Kaushik Roy, “Training Deep Convolutional Spiking Neural Networks with STDP-based Unsupervised Pre-training followed by Supervised Fine-tuning”, *Frontiers in Neuroscience, Neuromorphic Engineering, 2018*.
3. **Chankyu Lee**, Gopalakrishnan Srinivasan, Priyadarshini Panda and Kaushik Roy, “Deep Spiking Convolutional Neural Network Trained with Unsupervised Spike Timing Dependent Plasticity”, *IEEE Transactions on Cognitive and Developmental Systems (TCDS), 2018, Chosen as popular article for the month of May 2018*.
4. Amogh Agrawal\*, Akhilesh Jaiswal\*, **Chankyu Lee** and Kaushik Roy, “X-SRAM: Enabling In-Memory Boolean Computations in CMOS Static Random Access Memories”, *IEEE Transactions on Circuits and Systems I, 2018*

### • Preprint

1. Sayeed Shafayet Chowdhury\*, **Chankyu Lee\*** and Kaushik Roy, “Towards Understanding the Effect of Leak in Spiking Neural Networks” (\*Equally Contributing Authors), *arXiv preprint, 2020*.
2. Amogh Agrawal, **Chankyu Lee** and Kaushik Roy, “X-CHANGR: Changing Memristive Crossbar Mapping for Mitigating Line-Resistance Induced Accuracy Degradation in Deep Neural Networks”, *arXiv preprint arXiv:1907.00285 (2019)*.

## PRESENTATION

---

- **Chankyu Lee**, Priyadarshini Panda, Gopalakrishnan Srinivasan and Kaushik Roy, “Learning useful representations in deep spiking neural network using unsupervised STDP prior to supervised fine-tuning”, *SRC Techcon 2018, Austin, TX*

## RELEVANT COURSE PROJECTS

---

### • Algorithm

- **Artificial Intelligence:** Developed Binary weighted SNNs (BSNNs) to reduce the memory storage and remove batchnorm layers. Stochastic input encoding scheme and a spiking neuron model enable BSNNs to achieve competitive accuracy and perform efficient bit-wise computations without the need of the batchnorm layer. [*Language & Software: Python, Pytorch*]

- **Deep Learning:** Presented dynamic iterative synapse pruning for optimizing toward energy-efficient neural networks. Obtained 38-79% pruning efficiency at  $1.5\text{-}2.2\times$  increase in training effort across the layers on MNIST and CIFAR-10 datasets. [*Language & Software: Lua, Torch*]
- **Advanced VLSI Design:** Implemented rank-order temporal spike encoding scheme for rapid and energy-efficient classification tasks. Multi-layer convolutional SNNs are trained using unsupervised spike timing dependent plasticity learning. Obtained  $3.5\times/10\times$  reduction in time-steps/spike-counts during the inference stage compared with a rate-based spike encoding scheme. [*Language: Matlab*]

#### • Hardware System

- **System-on-Chip Design:** Demonstrated in-memory computing within STT-MRAMs memory units for Boolean logic operations. Applied the in-memory computing to AES encryption algorithms and evaluated the 172% energy benefit and 8% performance improvement compared with conventional CPU-based computing. Results are published in *IEEE TCAS-I*. [*Language & Platform: C++, Nios-II processor*]
- **MOS VLSI Design:** Implemented 8-bit Wallace Tree multiplier with scaled-down supply voltage and boost clock frequency by using the principle of pipelining and detecting critical path to lengthen the clock period in the worst case. [*EDA tool: Cadence Virtuoso, Hspice, Nanosim*]

### TECHNICAL STRENGTHS

---

<b>Programming Language</b>	Python, Matlab, C++
<b>Machine Learning Tools</b>	Pytorch, TensorFlow, Numpy, OpenCV

### SERVICE ACTIVITIES

---

#### • Technical Reviewer

- Frontiers in Computational Neuroscience
- IEEE Transactions on Neural Networks and Learning Systems (TNNLS)
- IEEE Access
- IEEE International Symposium on Circuits and Systems (ISCAS)
- Journal of Sensors
- International Conference on Computer Science and Application Engineering (CSAE)

#### • Leadership Experience

- President of Purdue Electrical Engineering Korean Association (PEEKA) in 2017-2018 academic year
- Student Ambassador Member of ‘Qualcomm IT Tour’ in summer 2013 (Selected as one of top 30 student engineers in Korea and sponsored by Qualcomm incorporation to visit its’ headquarter company in San Diego, CA, USA).

### REFERENCE

---

Available Upon Request