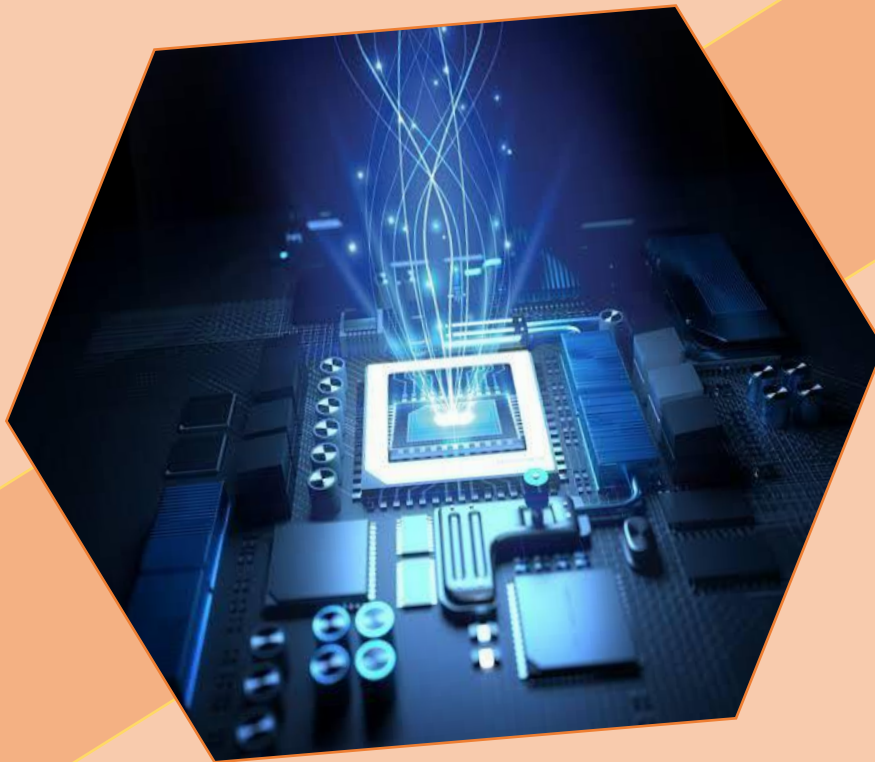


Free resources for Design Verification



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Free resources

DIGITAL ELECTRONICS :-

Chip verify: - <https://www.chipverify.com/tutorials/digital-fundamentals>

Digital electronics neso academy:-

<https://youtube.com/playlist?list=PLBlnK6fEyqRjMH3mWf6kwqiTbT798eAOm&si=bbrKSo0sS6OxAzBB>

digital electronics all about electronics :-

<https://youtube.com/playlist?list=PLwjK iyK4LLBC so3odA64E2MLglRKafl&si=mj94CR7ysC7mG AG>

digital circuit by Santana Chattopadhyay :-

<https://youtube.com/playlist?list=PLbRMhDVUMngePP5JcezxImF-FzOC9wstz&si=P1uimC-502ASbuDB>

VERILOG HDL :-

Chip verify :- <https://www.chipverify.com/tutorials/verilog>

Vlsi verify :- <https://vlsiverify.com/verilog/>

Hard ware modelling using Verilog :-

<https://youtube.com/playlist?list=PLJ5C 6qdAvBELELTSPgzYkQg3HgclQh-5&si=Q9DIcdRTT2GUWtJ7>

System design through Verilog :-

<https://youtube.com/playlist?list=PLwdnzlV3ogoVGq4TlpX4NH6QEFYiAnyvA&si=uvq2LVdZ1YkwIGye>

Digital design with Verilog :-

<https://youtube.com/playlist?list=PLwdnzlV3ogoVIY7iVqr-FhWUQEX7JDdiP&si=7-K06goO2AyD0G5F>

Verilog tutorial for beginners:-

<https://youtube.com/playlist?list=PLAC jmBddcjTPEh1UV ojRJmsx2D9sQXH&si=AodGwx2pA3GqgMyZ>

Verilog by vlsipoint :-

<https://youtube.com/playlist?list=PL 3xKnVkfl2hB9gBEsv5QGB4LRscGAQl9&si=VJCjvC7GTbEkvgMP>

Free resources

SYSTEM VERILOG HDVL: -

Vlsi verify :- <https://vlsiverify.com/systemverilog/>

Chip verify :- <https://www.chipverify.com/tutorials/systemverilog>

Verification guide:- <https://verificationguide.com/systemverilog-topics/>

All about vlsi :-

https://youtube.com/playlist?list=PLqPfWwayuBvNLAcH38tAs7_JkFkcCk2l8&si=J8xHKp_e7cyVxDJO

https://youtube.com/playlist?list=PLqPfWwayuBvOL920Q8ljlVmUMHshb5lJE&si=kQcKiCknJQD6hD_e

We_lsi :-

https://youtube.com/playlist?list=PLcmfUkZaXr_yssjVbupeUC9N2B7UnMjY1&si=lvEchXdR42fzvWEh

System verilog functional coverage :-

<https://youtube.com/playlist?list=PLDAnhhk0KczzWXNfA7S58FjlnCP71lzFo&si=WUiyVAsizMoOE-lj>

System verilog assertion:-

<https://youtube.com/playlist?list=PLDAnhhk0Kczx4zHid9iYXFvpigqb8B8FP&si=87XWR-J9dy-ibBHz>

UVM (UNIVERSAL VERIFICATION METHODOLOGY):-

Verification guide :- <https://verificationguide.com/uvm/uvm-tutorial/>

Chip verify :- <https://www.chipverify.com/uvm/uvm-tutorial>

Vlsi verify :- <https://vlsiverify.com/uvm/>

Introduction to sv-uvm RAL :-

<https://youtube.com/playlist?list=PLDAnhhk0KczwvgPh5sWhRP4e8l7HH9Bge&si=fEPTaeFaKaugKpOl>

UVM :- <https://youtube.com/playlist?list=PLRX7Xm8POVgfwahSAi-KgTILmpM8aDBcz&si=9UaqEGAgV8fsgAx9>

UVM INTERVIEW :-

https://youtube.com/playlist?list=PL003P3HpY4helkmb6a4H93dQ_P8D7YXIj&si=jwyKbVUyV3wByNaC

Free resources

ONLINE COMPILER: -

Edaplayground :- <https://edaplayground.com/>

Tutorialspoint:-

https://www.tutorialspoint.com/compile_verilog_online.php

Jdoodle :- <https://www.jdoodle.com/execute-Verilog-online>

OFFLINE COMPILER: -

Icarus verilog (iverilog) :- https://bleyer.org/icarus/iverilog-v11-20210204-x64_setup.exe

QuestaSim (mentor graphic) :-

https://drive.google.com/file/d/1Gr6EmE4k0SMv50AsQ_ktv3xQOWEh-qE3/view

Installation video :-

<https://youtu.be/WgyNRYXe2dc?si=lHs7jn9vb6JO4yCR>

Xilinx vivado :-

<https://www.xilinx.com/support/download/index.html/content/xilinx/en/downloadNav/vivado-design-tools.html>

Installation video :-

https://youtu.be/Lc2EEbZmlz0?si=ohVfeuedQ_o2uC4