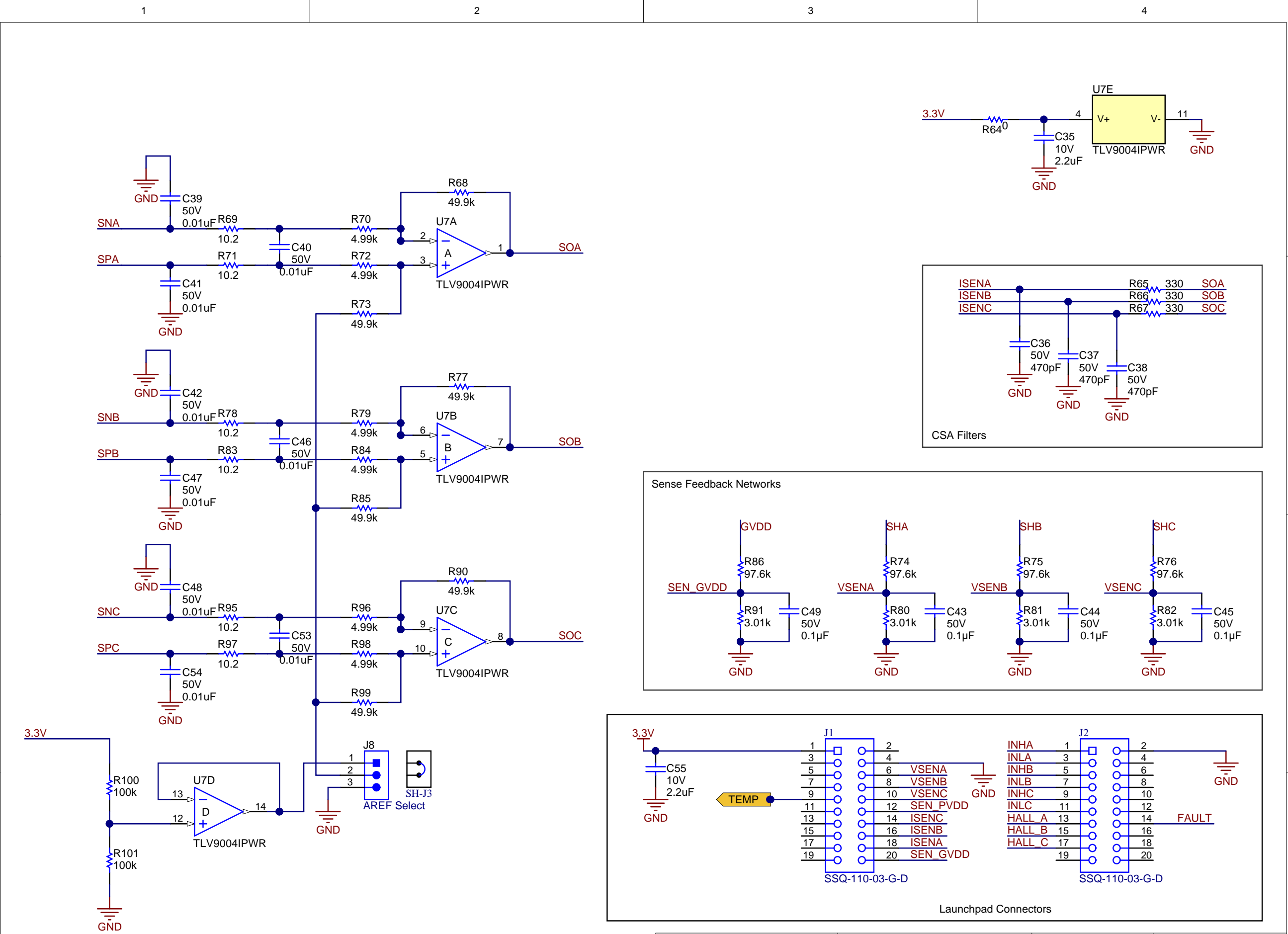
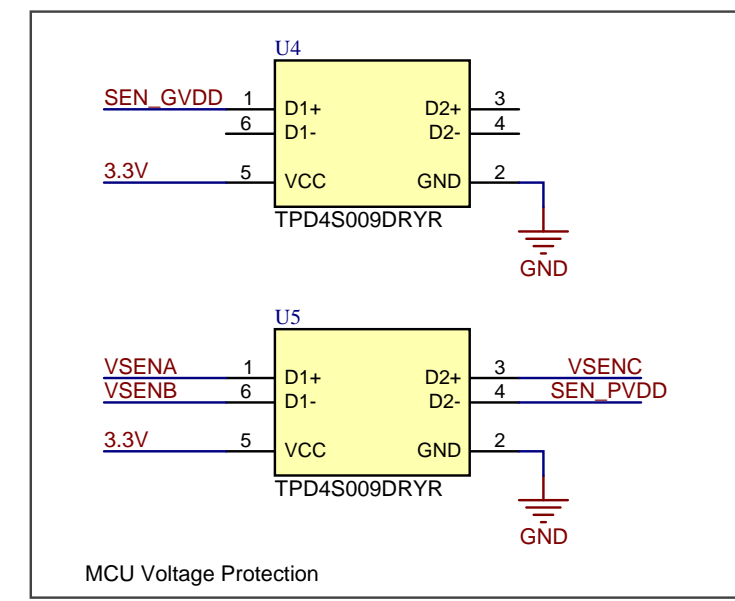
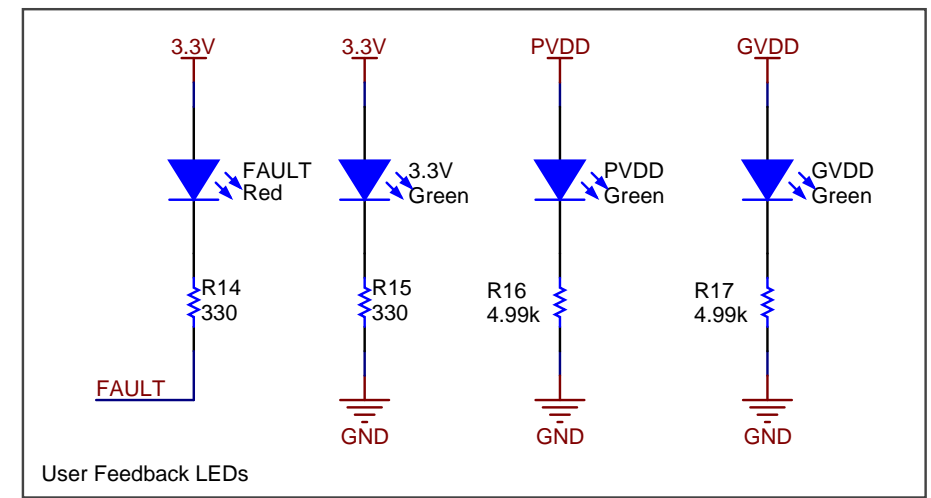
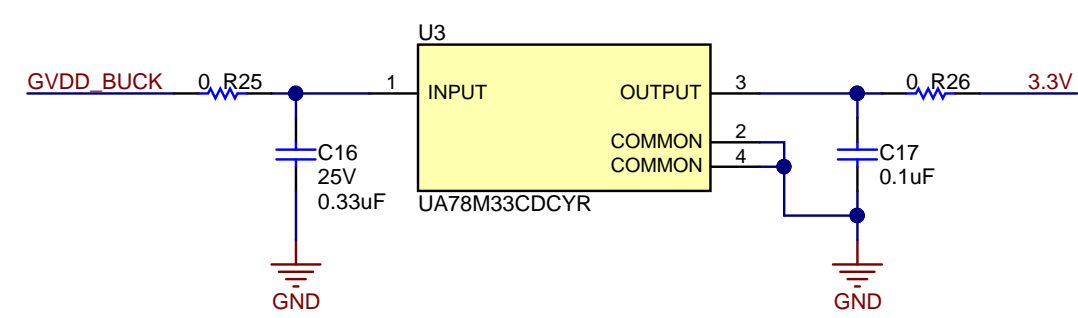
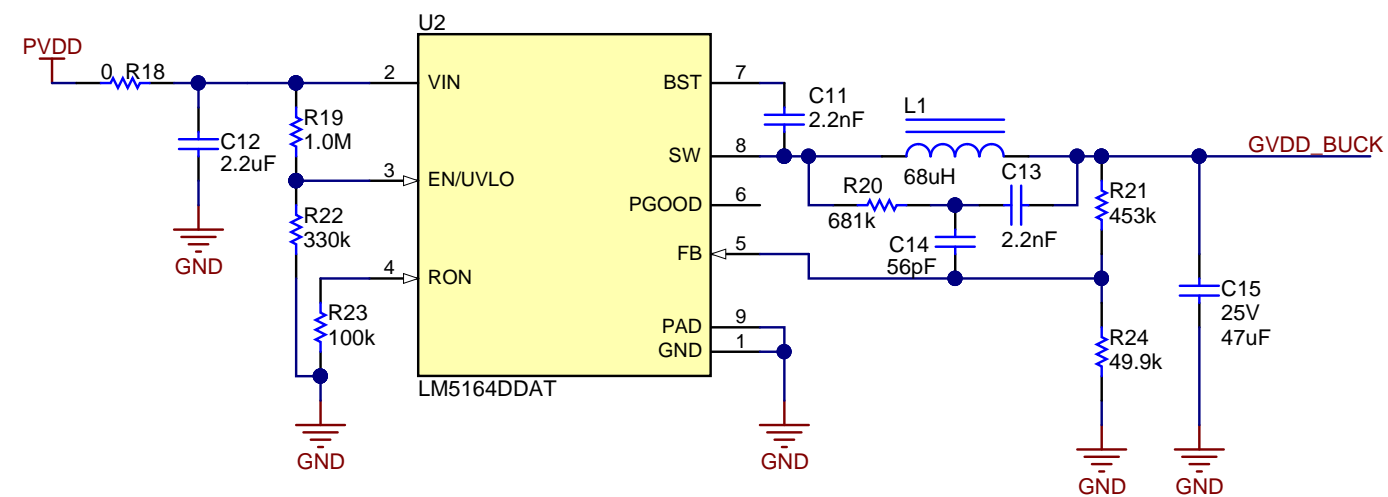
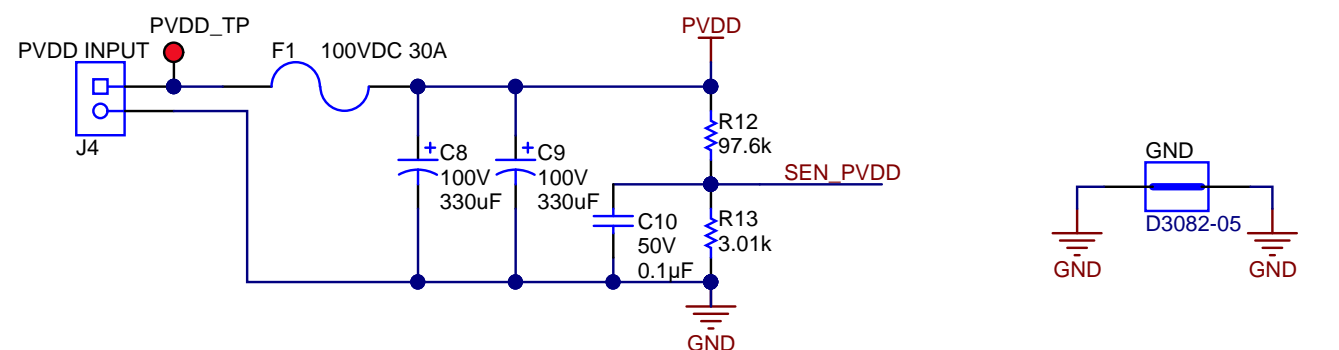


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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 10/26/2020
TID #: N/A	Project Title: DRV8300DIPW-EVM	
Number: MD029	Rev: A	Sheet: 2 of 5
SVN Rev: Not in version control	Assembly Variant: 001	Size: Letter
Drawn By: Adam Sidelsky	File: MD029A_HalfBridges.SchDoc	http://www.ti.com
Engineer: Adam Sidelsky	Contact: http://www.ti.com/support	© Texas Instruments 2018







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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 3/24/2020
TID #: N/A	Project Title: DRV8300DIPW-EVM	
Number: MD029	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 5
Drawn By:	File: MD029A_Power.SchDoc	Size: Letter
Engineer: Adam Sidelsky	Contact: http://www.ti.com/support	



PCB Number: MD029
PCB Rev: A



Variant/Label Table	
Variant	Label Text
001	Default

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.