LAB 1

- ❖ Design Following Circuits using Data Flow Modeling
 - 1. Multiplexer 4x1
 - 2. Demultiplexer 1x4
 - 3. Encoder 8x3
 - 4. Decoder 3x8
 - 5. Half Adder
 - 6. Full Adder
 - 7. 4-bit Full Adder
 - 8. BCD to 7 Segment Display