Lab 7

1. Study datasheet of Asynchronous RAM (CY6264). Also Design and verify the functionality of this RAM.

2. Using switch level modeling (MOS Switches), Design the following:
   a. 3 input NAND gate
   b. 3 input NOR gate
   c. 2 input XOR gate

3. Design a 4x4 sequential multiplier using booth's algorithm