

## EMACS

Command	Operation
C-x C-f	Find File
C-x 1	Remove Split Windows
C-c C-b	Align Text
C-x 2	Split windows
C-c C-m C-e	Toggle Electronic Mode
C-s	Search
A-w	Copy
C-y	Paste
C-w	Cut
C-x u	Undo
C-x C-s	Save file

C- Ctrl

A- Alt

---

## QUESTASIM

Command	Operation
vcom -work work ../src/<file name>	Compiling file by using relative path
vcom -work work <complete path>	Compiling file by using full path
vcom -93 -work work <any of the above> vcom -87 -work work <any of the above> vcom -2002 -work work <any of the above>	Compiling file using specific version -93 for VHDL-93
vsim <entity name>	loads a new design into the simulator
vsim -novopt <entity name>	Open Simulation window with no optimization
add wave sim:/<signal name>	Adding individual Signal to wave window
add wave *	Adds all the objects in the current region to the List window.
add wave -r *	Adds all objects in the design to the Wave window

force <clock name> 1 0, 0 10 -repeat 20	Force clock to take value 1 at 0ns, 0 at 10ns and repeat forcing at every 20ns
force <signal name> 1	Forcing signal with value 1
force <signal name> 0	Forcing signal with value 0
force <signal name> 1010	Forcing signal with vector 1010
run <time>	Force values for specified time
do <do file name>.do	Runs entries present in do file
restart -f	Clears Wave window
project new c:/user.../sim <project name> work	Creating new project
project close	Closing Project
project addfile c:/user.../<file.vhd>	Adding file to project
	adding variables to the wave window
freeze	
vlog	
vlib <library name >	Create library with given name
vsim -wlf abc.wlf <entity name>	Changing name of vsim.wlf to abc.wlf
vsim -view vsim.wlf	To observe the content of wlf file
vsim -msgmode both <entity_name> tran wlf	Used for enabling message mode debugging (show markers on wave window)
vmap <new_lib> <old_lib>	Mapping new library to name of an older library
vmap <new_lib> <library path>	Mapping new library with path of an older library
vdel -all -lib <lib_name>	Deleting a library
project compileall	Compile all the source files present in current project
quit -sim	quit the simulation

Complete path - c:/users/dvlsi12/Desktop/mux\_2\_1/src/mux.vhd

.. - Parent Directory.

... - Mention complete path.

-----