Signal	Values
ins_data[15:0]	1000011111010100 / 1000011010101001 / 0011100000000001 / 1000011011100001 / 0000100000000001 / 0...
ins_ack	
clock	
dat_ack	
dat_data[7:0]	UUUUUUUU
ins_addr[15:0]	UUUUUUUU... / XXXXXXXXXXXXXX
ins_req	
dat_addr[15:0]	UUUUUUUUUUUUUUUU / 0000000000000000 / UUUUUUUUUUUUUUUU
dat_read	
dat_req	
test[7:0]	UUUUUUUU / 11110101 / 10101010 / 01011111 / 10111000 / 00011000
regout1[7:0]	UUUUUUUU / 11110101 / UUUUUUUU / 10101010 / 11110101 / 01011111 / 10101010 / 10111000 / 01011111 / 00011000 / 1...
regout2[7:0]	UUUUUUUU / 10101010 / 10111000
alout[7:0]	00000000 / 01011111 / 11110101 / 00000000 / 00011000 / 0...
clock_period	10000 ps

| 70 ns | 80 ns | 90 ns | 100 ns | 110 ns | 120 ns | 1 |

0001000100100001 | 0001000000100000 | 0111110000100000 | 1111110001001001 | 1111110001100001 | 0011100001000011 | 1110110000101100

UUUUUUUU

XXXXXXXXXXXXXXX | 1011100000011000 | 1011100000011001 | 1011100000011010 | 1011100000011011 | 101110000010..

UUUU... | 10111000UUUUUUUU | 1011100000011000 | 1011100000011000 | UUUUUUUUUUUUUUUU | 1011100000011000

00011... | 10111000 | 00011000 | 10111000 | 00011000 | 10100000 | 10111000

10111000 | 00011000 | 10111000 | UUUUUUUU | 10111000 | UUUUUUUU | 00011000 | 10111000 | 10100000 | 10111000

10111000 | 00011000 | 10111000 | 00011000 | UUUUUUUU

00000000 | 10100000 | 10111000 | 00000000

10000 ps

	140 ns	150 ns	160 ns	170 ns	180 ns	190 ns

0001010000100010 1000011111010100 1000011010101001 0011100000000001 1000011011100001 0000100000000001

UUUUUU... 10100000

1011100... 1011100000100111 1011100000101000 1011100000101001 1011100000101010 1011100000101011 1011100000101100 1011100000...

1011100000011000 UUUUUUUUUUUUUUUU 1011100000011000 UUUUUUUUUUUUUUUU 0000000000000000 UUUUUUUUUUUUUUUU

10111000 11110101 10101010 01011111 10111000 00011000

10111000 00011000 11110101 10111000 10101010 11110101 01011111 10101010 10111000 01011111 00011000

10100000 UUUUUUUU 10111000 10101010 10111000

00000000 01011111 11110101 00000000 00011000

10000 ps