

GATE ASSIGNMENT

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1. Which one of the following options is CORRECT for the given logic circuit?

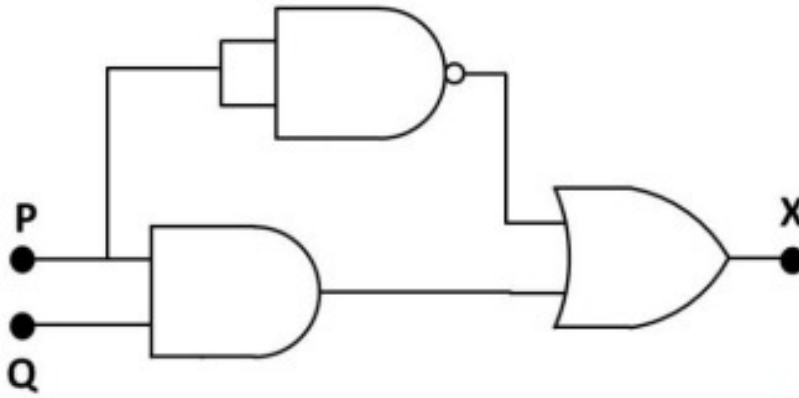


Figure 1: k-maps

Fig. 1

- (a) $P = 1, Q = 1; X = 0$
- (b) $P = 1, Q = 0; X = 0$
- (c) $P = 0, Q = 1; X = 0$
- (d) $P = 0, Q = 0; X = 1$