GATE ASSIGNMENT

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IITH - IITH-Future Wireless Communication

CONTENTS

I. QUESTION

1) Which one of the following options is CORRECT for the given logic circuit?

(a) P = 1, Q = 1; X = 0
(b) P = 1, Q = 0; X = 0
(c) P = 0, Q = 1; X = 0
(d) P = 0, Q = 0; X = 0

II. ANSWER

$\rightarrow P' + Q$

Therefore, the Boolean function $F(X) = (P' + Q)$

III. LOGIC DIAGRAM

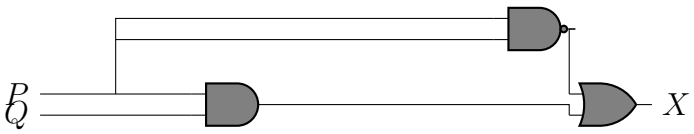

Fig. 1

IV. TRUTH TABLE

P	Q	$P.Q$	P'	X
0	0	0	1	1
0	1	0	1	1
1	0	0	0	0
1	1	1	0	1

Truth table for Boolean Function X

V. K-MAP IMPLEMENTATION

Using the boolean logic output F can be expressed in terms of the inputs X, Y, Z with the help of the following Kmap.

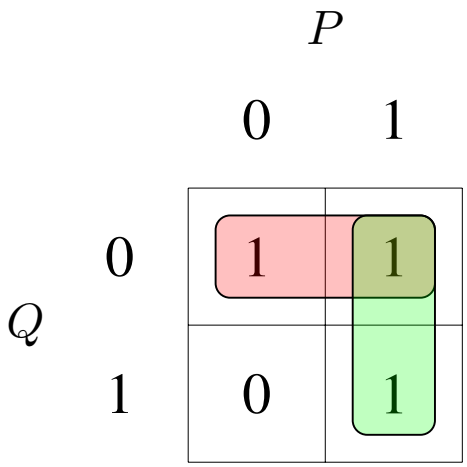

Fig. 2

VI. COMPONENTS

Component	Values	Quantity
Arduino	UNO	1
LED		1
Resistor	220ohms	1
Jumper Wires	M-M	5
Breadboard		1

VII. IMPLEMENTATION

Arduino PIN	INPUT	OUTPUT
2	P	
3	Q	
13		X

Connections

Procedure

1. Connect the circuit as per the above table.
2. Connect inputs to Vcc for logic 1, ground for logic 0.
3. Execute the circuit using the below code.

https://github.com/Chandinivooribindi/FWC gate/2023

4. Change the values of P,Q in the code and verify the Truth Table.