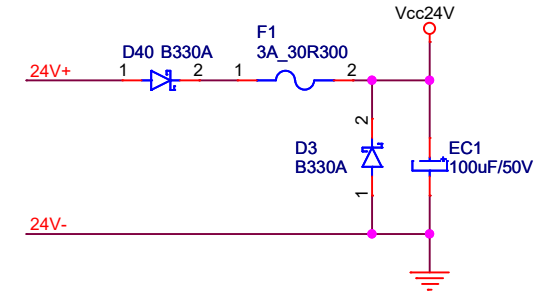
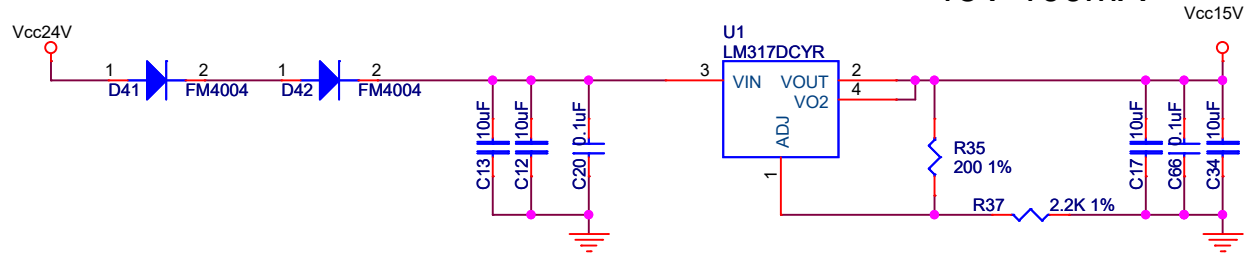
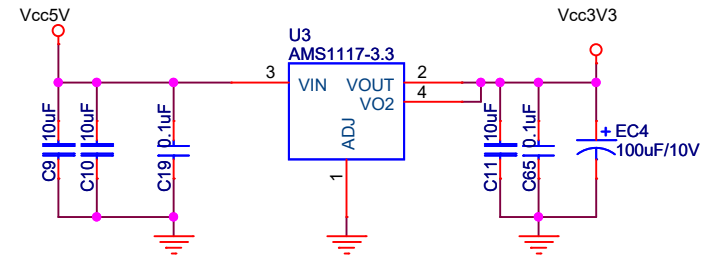
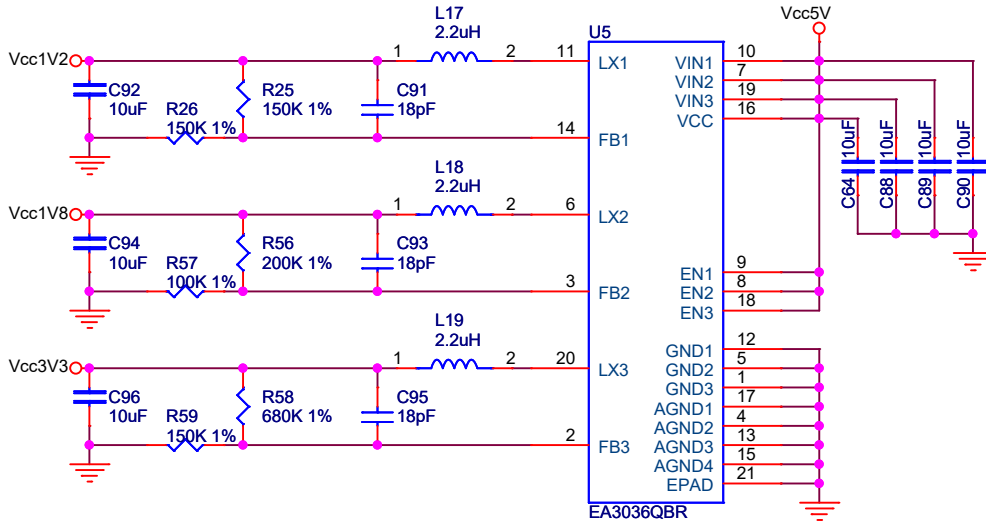
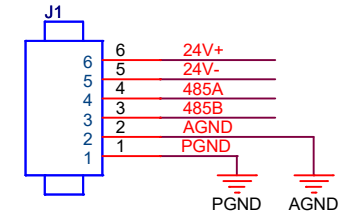
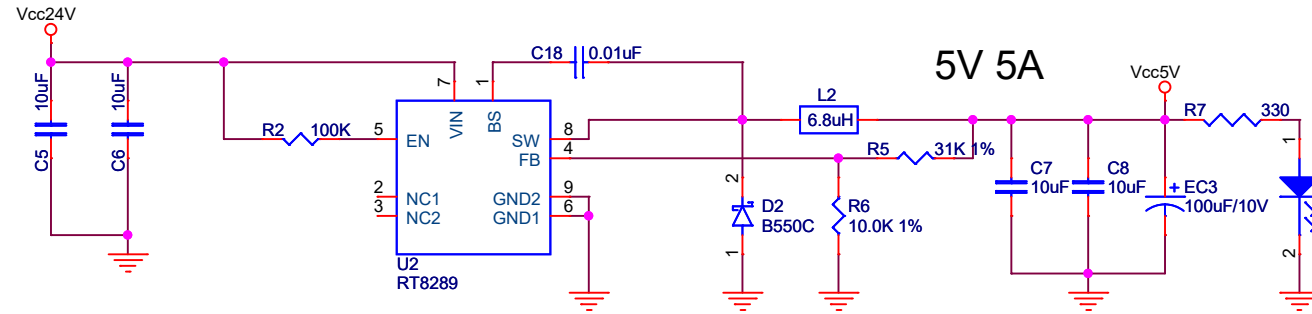


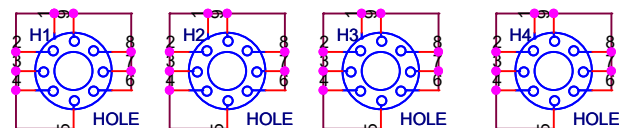
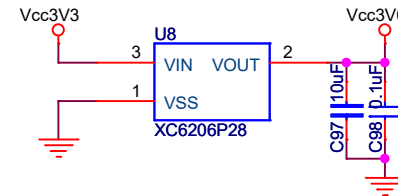
15V 100mA



5V 5A



485A  
485B

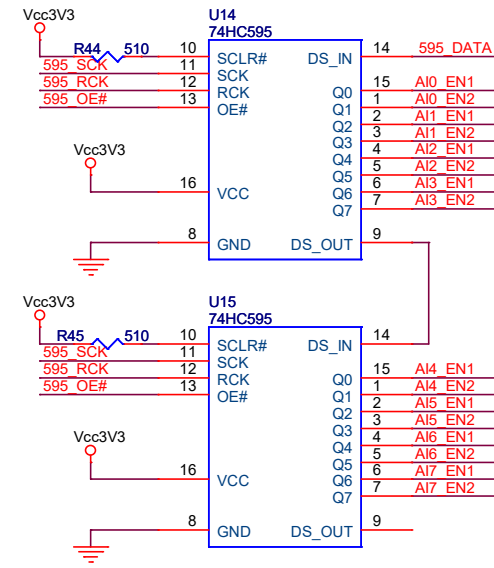
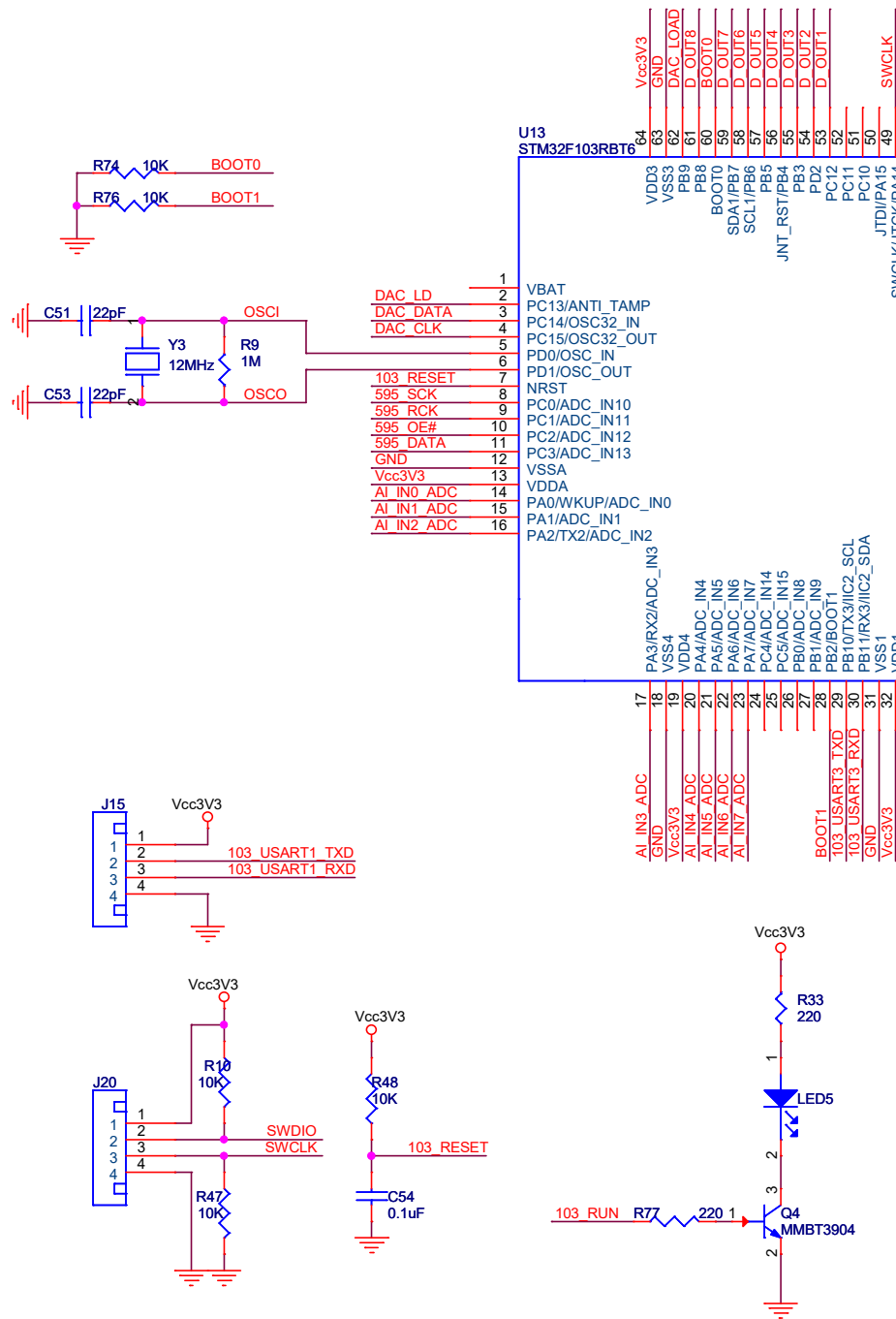


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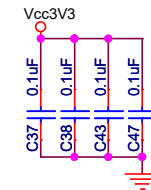
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103 USART3 RXD R79 10K V3S USART0 TXD

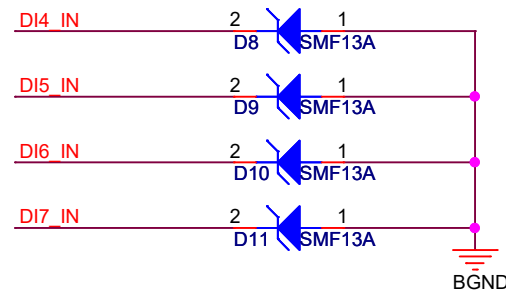
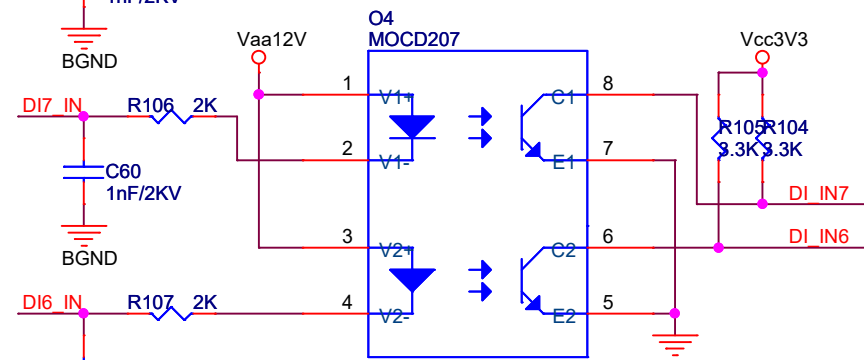
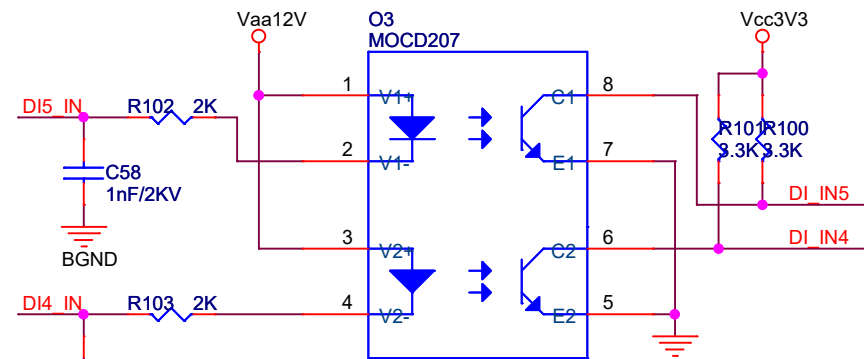
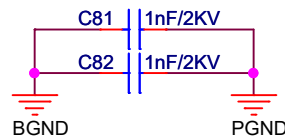
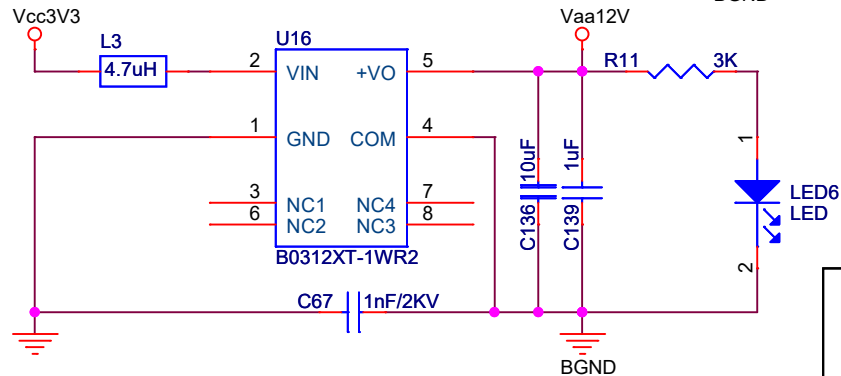
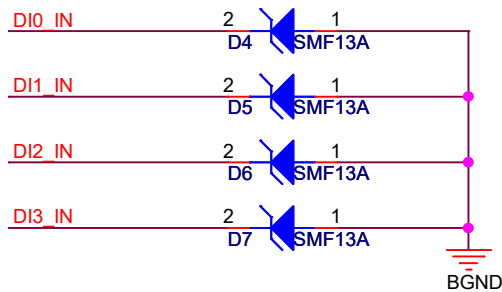
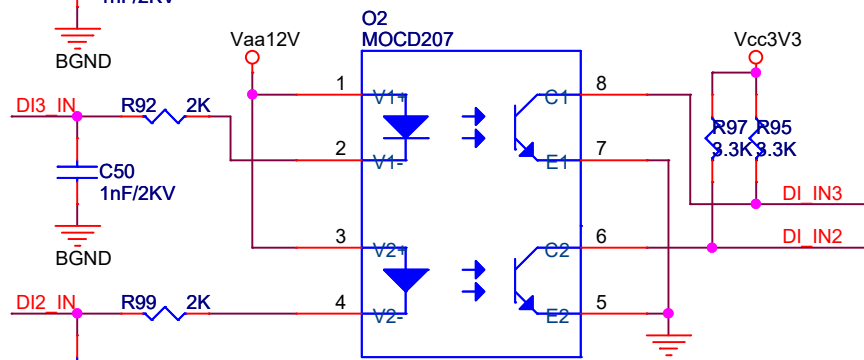
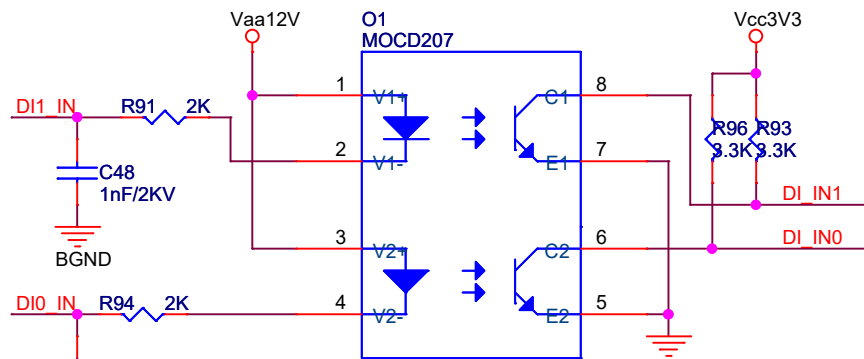


V3S USART0 TXD V3S USART0\_TXD  
V3S USART0 RXD V3S USART0\_RXD

DI\_IN0 DI\_IN0  
DI\_IN1 DI\_IN1  
DI\_IN2 DI\_IN2  
DI\_IN3 DI\_IN3  
DI\_IN4 DI\_IN4  
DI\_IN5 DI\_IN5  
DI\_IN6 DI\_IN6  
DI\_IN7 DI\_IN7  
AI\_IN0\_ADC AI\_IN0\_ADC  
AI\_IN1\_ADC AI\_IN1\_ADC  
AI\_IN2\_ADC AI\_IN2\_ADC  
AI\_IN3\_ADC AI\_IN3\_ADC  
AI\_IN4\_ADC AI\_IN4\_ADC  
AI\_IN5\_ADC AI\_IN5\_ADC  
AI\_IN6\_ADC AI\_IN6\_ADC  
AI\_IN7\_ADC AI\_IN7\_ADC  
AI0\_EN1 AI0\_EN1  
AI0\_EN2 AI0\_EN2  
AI1\_EN1 AI1\_EN1  
AI1\_EN2 AI1\_EN2  
AI2\_EN1 AI2\_EN1  
AI2\_EN2 AI2\_EN2  
AI3\_EN1 AI3\_EN1  
AI3\_EN2 AI3\_EN2  
AI4\_EN1 AI4\_EN1  
AI4\_EN2 AI4\_EN2  
AI5\_EN1 AI5\_EN1  
AI5\_EN2 AI5\_EN2  
AI6\_EN1 AI6\_EN1  
AI6\_EN2 AI6\_EN2  
AI7\_EN1 AI7\_EN1  
AI7\_EN2 AI7\_EN2  
D\_OUT1 D\_OUT1  
D\_OUT2 D\_OUT2  
D\_OUT3 D\_OUT3  
D\_OUT4 D\_OUT4  
D\_OUT5 D\_OUT5  
D\_OUT6 D\_OUT6  
D\_OUT7 D\_OUT7  
D\_OUT8 D\_OUT8  
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DAC\_LD DAC\_LD  
DAC\_DATA DAC\_DATA  
DAC\_CLK DAC\_CLK  
RELAY\_EN RELAY\_EN  
103\_RESET 103\_RESET

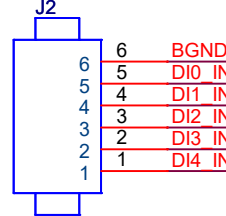
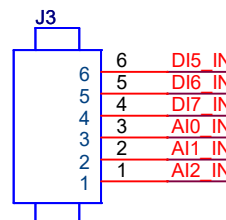
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AI0\_IN  
AI1\_IN  
AI2\_IN

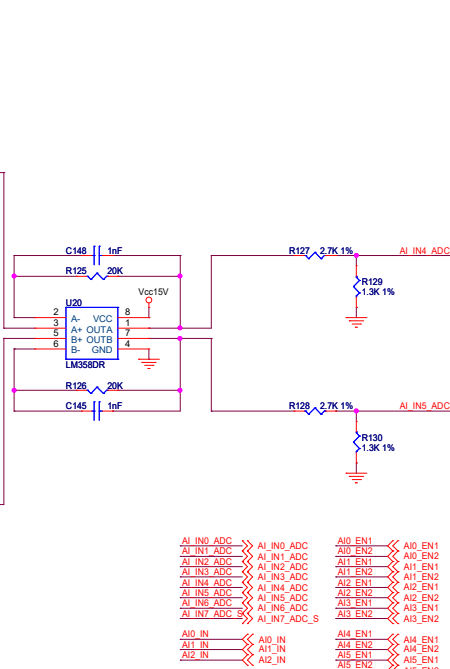
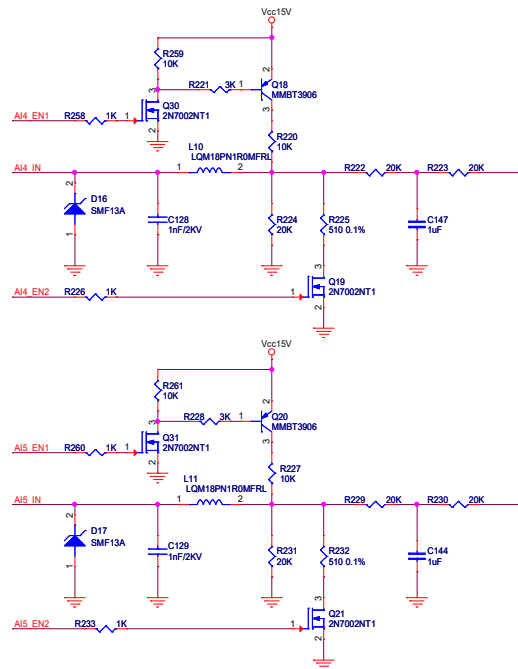
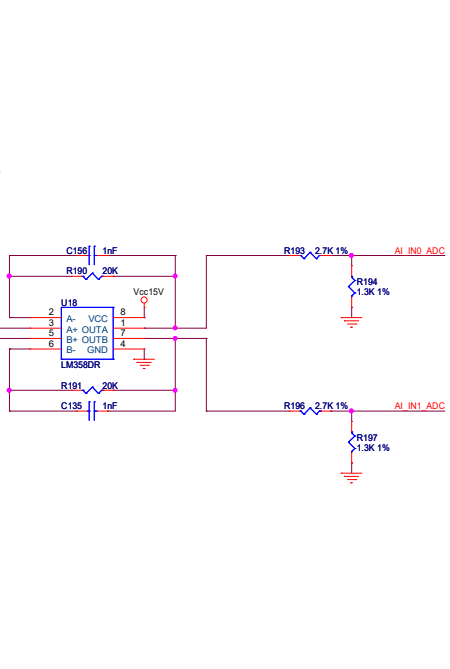
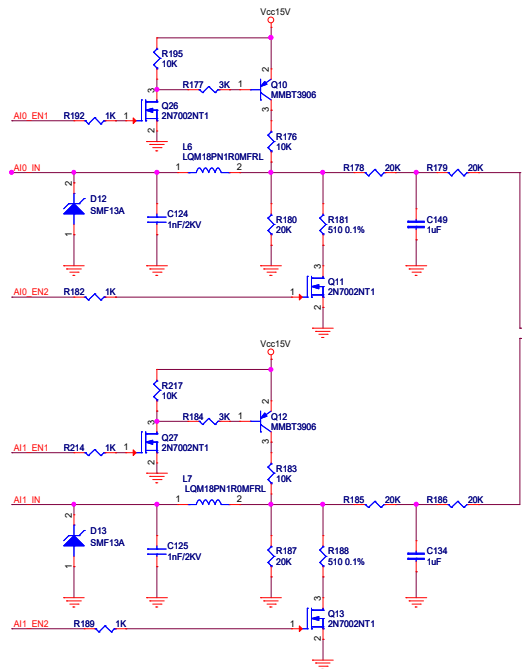
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DI\_IN1  
DI\_IN2  
DI\_IN3  
DI\_IN4  
DI\_IN5  
DI\_IN6  
DI\_IN7



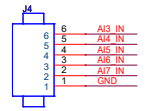
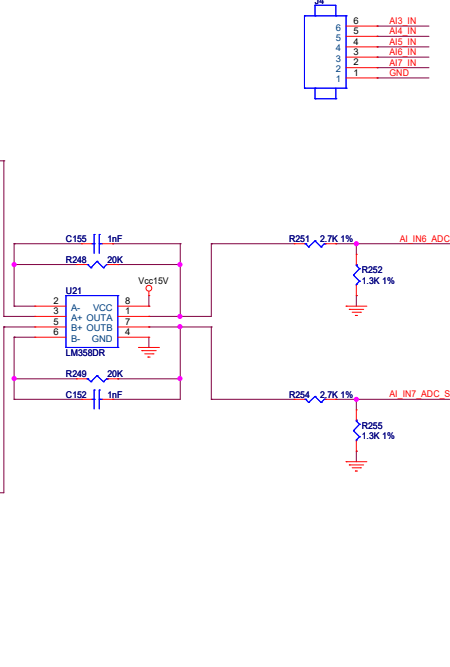
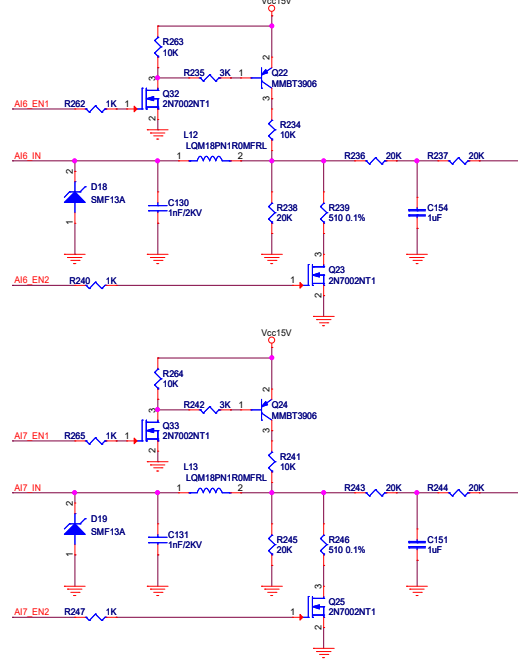
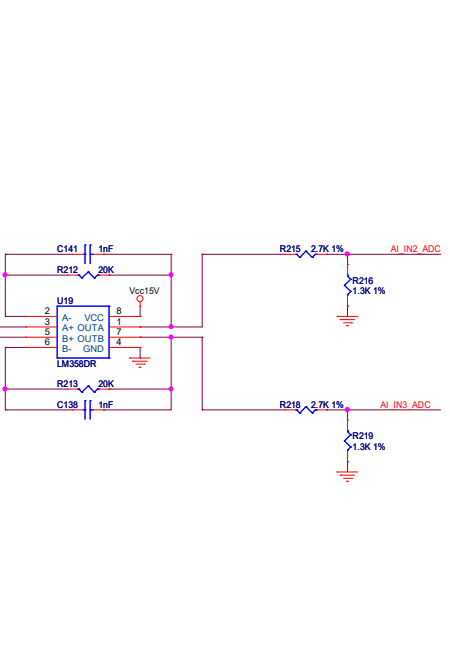
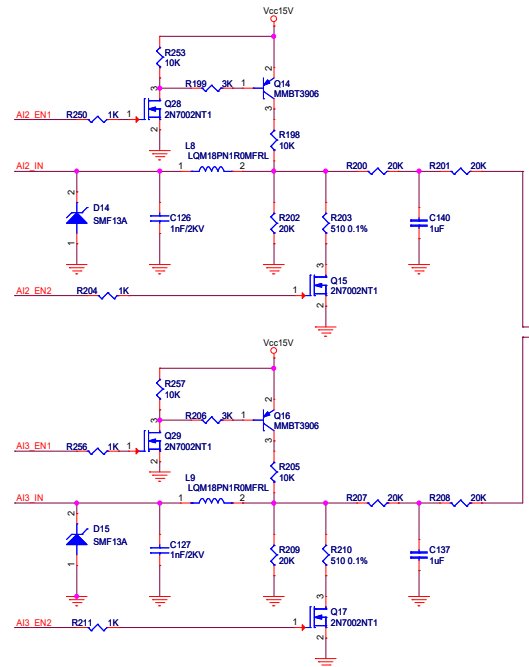
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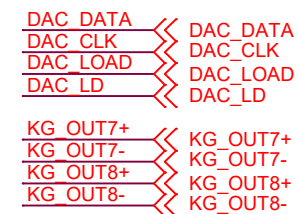
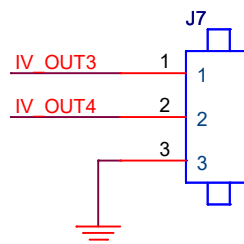
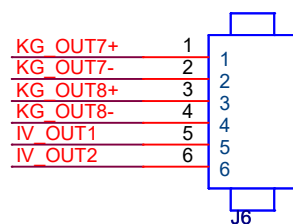
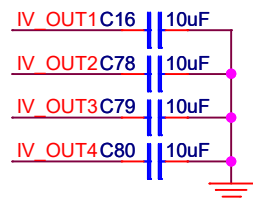
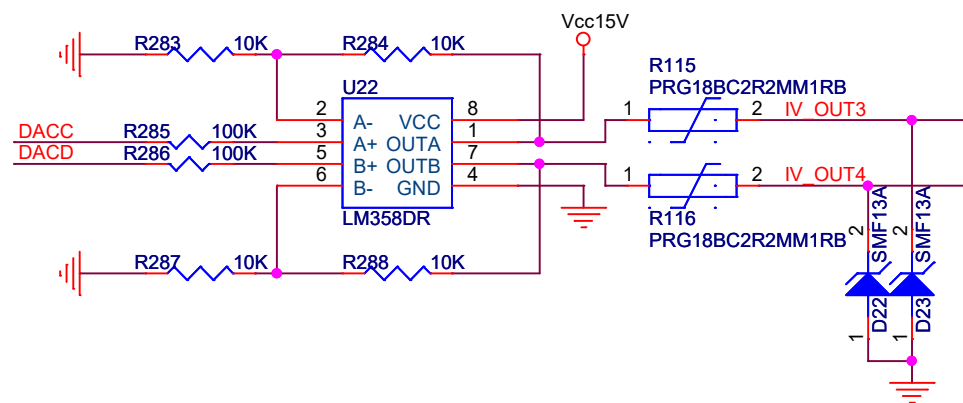
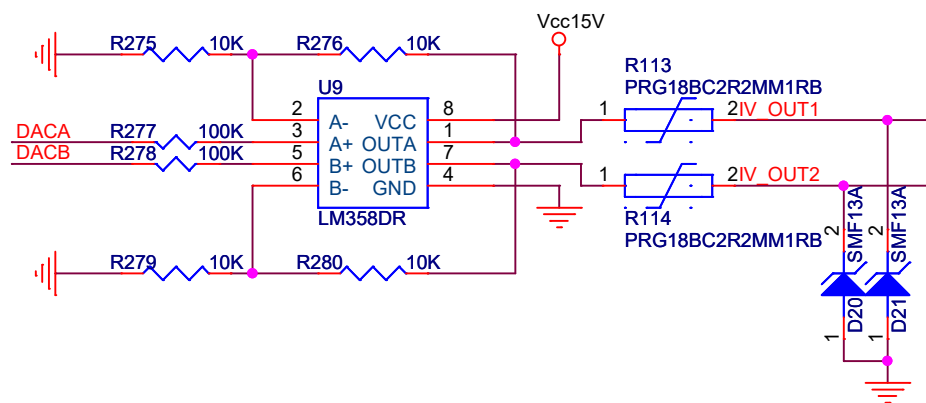
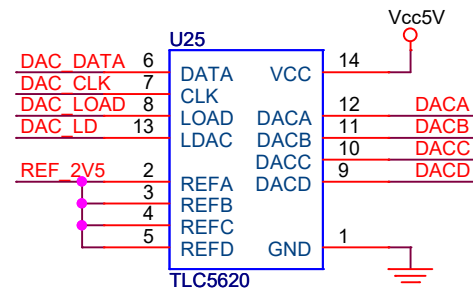
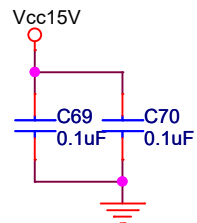
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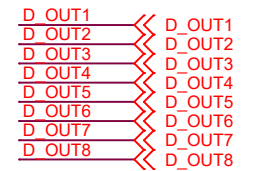
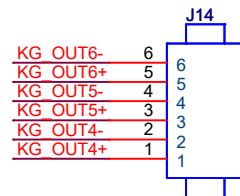
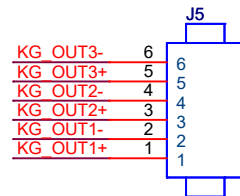
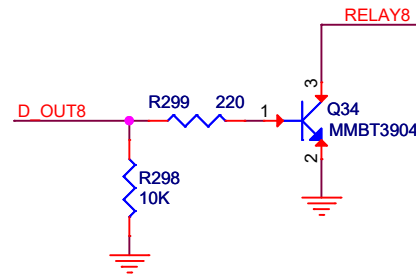
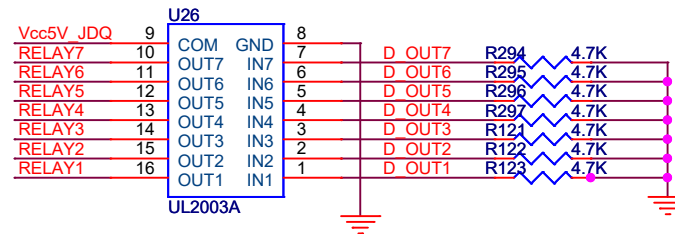
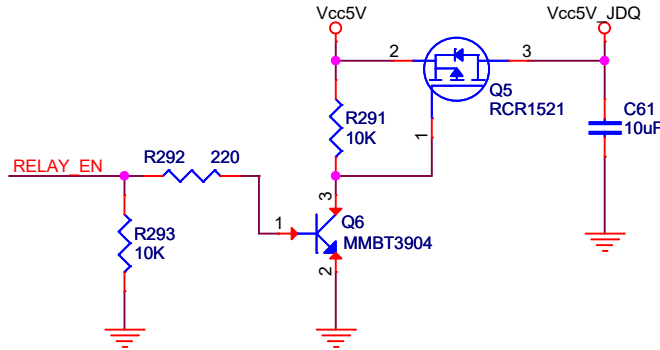
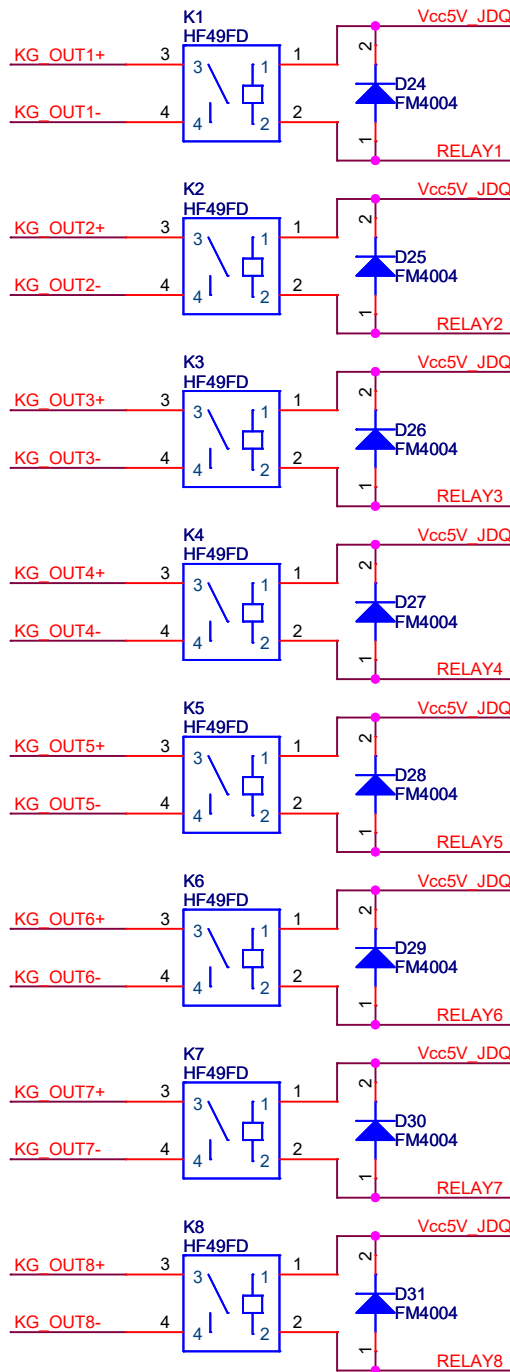
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AI_IN0_ADC	AI_IN0_ADC	AI0_EN1	AI0_EN1
AI_IN1_ADC	AI_IN1_ADC	AI0_EN2	AI0_EN2
AI_IN2_ADC	AI_IN2_ADC	AI1_EN1	AI1_EN1
AI_IN3_ADC	AI_IN3_ADC	AI1_EN2	AI1_EN2
AI_IN4_ADC	AI_IN4_ADC	AI2_EN1	AI2_EN1
AI_IN5_ADC	AI_IN5_ADC	AI2_EN2	AI2_EN2
AI_IN6_ADC	AI_IN6_ADC	AI3_EN1	AI3_EN1
AI_IN7_ADC	AI_IN7_ADC	AI3_EN2	AI3_EN2
AI0_IN	AI0_IN	AI4_EN1	AI4_EN1
AI1_IN	AI1_IN	AI4_EN2	AI4_EN2
AI2_IN	AI2_IN	AI5_EN1	AI5_EN1
		AI5_EN2	AI5_EN2
		AI6_EN1	AI6_EN1
		AI6_EN2	AI6_EN2
		AI7_EN1	AI7_EN1
		AI7_EN2	AI7_EN2



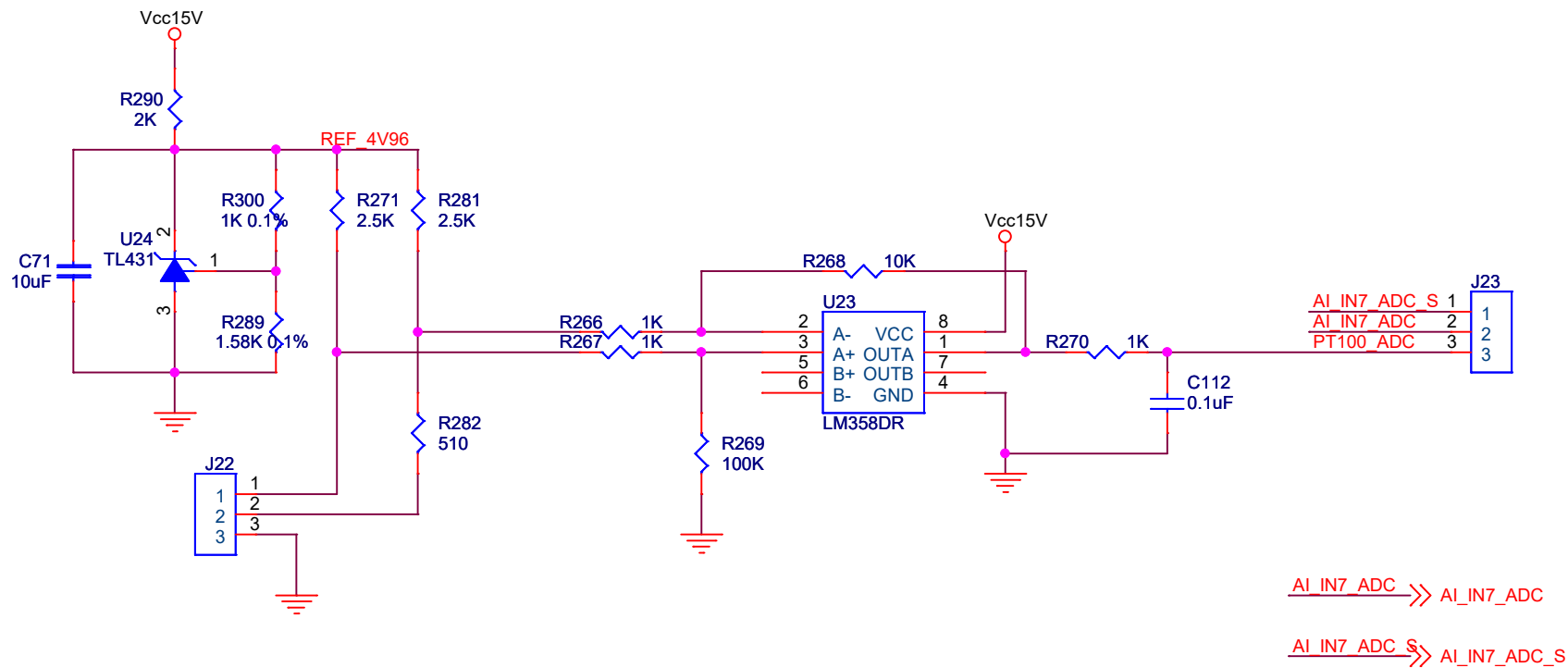




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