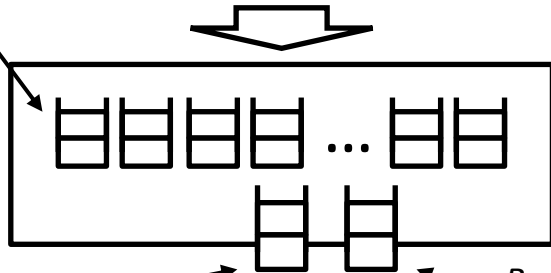


Flash
Controller

Network
Interface

In-Storage
Processor

*Per-buffer
FIFOs*



Burst data

*Burst ready
Buffer index*

DMA
Engine