**GR6613 BT chip UART to I2C update FW Function for 9706 Pen**

**日期20230320 V1**

1. **UART to I2C Bridge 流程說明**
2. **Bridge CMD Format**
3. **Bridge AP CMD Flow**
4. **驗證功能**
5. **UART to I2C Bridge 流程說明**

UART 0

RX

TX

GR6613

Host

UART TX

RX

Baud rate: 3000000

SIS Pen

RX

TX

UART Format

Convert to

I2C Format

I2C Format

Convert to

UART Format

I2C Master

SCK (GPIO15)

SDA(GPIO16)

Reset(GPIO4)

I2C Clock: 300KHz

Address : 0x5C

Write

Read

I2C Slave

SCK

SDA

Reset

UART 1

TX

RX

UART

RX

TX

Send set I2C Slave Message

Baud rate: 3000000

Parser GR UART TX output Header (OP code)

UART RX Done

OP code compare?

Mapping below op code (SIS CMD) to execute process:

(SISPEN\_BRIDGE\_INIT(Only I2C read ) ,

SISPEN\_BRIDGE\_I2C\_W\_R )

UART Date convert to I2C Write Format

Then execute I2C Write

Yes

No

I2C Write Done (ptyEventI2CWriteDone)

Is I2C status error?

Yes

No

Execute I2C Read

I2C Read Done (ptyEventI2CReadDone)

Is I2C status error?

Yes

No

I2C Read Date convert to UART RX Format

Then execute Send UART RX(ACK BE EF)

Then execute Send UART RX(NACK DEAD )

UART TX Done

Mapping below op code (Bridge CDM) to execute process:

(SISPEN\_BRIDGE\_HW\_RESET

SISPEN\_BRIDGE\_DEBUG\_DIS , SISPEN\_BRIDGE\_DEBUG\_EN

SISPEN\_BRIDGE\_I2C\_DIS\_EN)

UART TX Done

Then execute Send UART RX

Parser GR UART TX output Header (OP code)

UART RX Done

OP code compare?

Yes

No

Bridge to SIS PEN AP CMD

GR Bridge AP CMD

1. **Bridge CMD Format**

**Uart TX CMD**

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Type | byte 0 | byte 1 | byte 2 | byte 3 | byte 4 | byte 5 | byte 6 | byte 7 | byte 8~ 64 |
| TX Output | UART CMD ID | OP Code LSB | OP Code MSB | Length field LSB | Length field MSB | Reprot ID (0x09) | CRC16 | CMD | PAYLOAD |

**I2C Write CMD**

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| TYPE | byte 0 | byte 1 | byte 2 | byte 3 | byte 4 | byte 5 | byte 6 | byte 7 | byte 8 ~ 63 |
| OUTPUT | Output Register (LSB) | Output Register (MSB) | Length field (LSB) | Length field (MSB) | Report ID | Reserved (0x0) | CMD | CRC 16 | PAYLOAD |

GR6613 Uart header ( Byte0~4 ) ，Length = Reprot ID(byte5)+CRC16(byte6)+CMD(byte7)+PAYLOAD(byte8~64)， CRC16 = CMD + PAYLOAD

SIS Pen I2C CRC 16 = CMD + PAYLOAD ，Length = 2(Length) + 4(byte4~7) + payload

**I2C Read CMD**

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| TYPE | byte 0 | byte 1 | byte 2 | byte 3 | byte 4 | byte 5 | byte 6 | byte 7 | byte 8 ~ 63 |
| INPUT | Length field (LSB) | Length field (MSB) | Report ID | CRC 16 | PAYLOAD | | | | |

**Uart RX CMD**

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Type | byte 0 | byte 1 | byte 2 | byte 3 | byte 4 | byte 5 | byte 6 | byte 7 | byte 8 | byte 9~ 64 |
| RX Input | UART Event ID | Event OP Code | Length field LSB | Length field MSB | OP Code LSB | OP Code MSB | UART Status | Reprot ID (0x0A) | CRC16 | PAYLOAD |

GR6613 Uart header( Byte0~6 ) ，Length = OP Code LSB(byte4)+OP Code MSB(byte5)+Status(byte6)+ Rprot ID(byte7) + CRC16(byte8)+PAYLOAD(byte9~64)

CRC16 = PAYLOAD

SIS Pen I2C CRC 16 = PAYLOAD，Length = 2(Length) + 2(byte2~3) + payload

1. **Bridge AP CMD Flow**
2. SISPEN\_BRIDGE\_DEBUG\_DIS (OP code 0x8005) for bridge (Update FW Start , BT will control the Pen no sleep)
3. SISPEN\_BRIDGE\_I2C\_DIS (OP code 0x8008, CMD 0x00 I2C Disable) for bridge
4. SISPEN\_BRIDGE\_HW\_RESET (OP code 0x8007) for bridge
5. Delay 400ms (wait for the SIS Pen I2C master to switch to slave after reset)
6. SISPEN\_BRIDGE\_I2C\_EN (OP code 0x8008, CMD 0x01 I2C Enable) for bridge
7. SISPEN\_BRIDGE\_INIT (OP code 0x8001) for bridge (Check I2C transmission is normal)
8. SISPEN\_BRIDGE\_I2C\_W\_R (OP code 0x8004) for SIS Pen AP CMD
   1. 85 51 09
   2. 85 21 01
   3. 83/84/85/86 CMD
   4. 85 20 01
   5. 85 50 09
9. SISPEN\_BRIDGE\_I2C\_DIS\_EN (OP code 0x8008, CMD 0x00 I2C Disable) for bridge
10. SISPEN\_BRIDGE\_HW\_RESET (OP code 0x8007) for bridge
11. SISPEN\_BRIDGE\_DEBUG\_EN (OP code 0x8006) for bridge(Update FW End, BT will release control the Pen can sleep)

SISPEN\_BRIDGE\_DEBUG\_DIS (OP code 0x8005) for bridge

Start

Is ACK (BE EF) ?

Use SISPEN\_BRIDGE\_I2C\_W\_R (OP code 0x8004) to transfer

SIS Pen AP CMD (81/82/83/84/85/86)

參考注2

Yes

No

End

SISPEN\_BRIDGE\_HW\_RESET (OP code 0x8007) for bridge

SISPEN\_BRIDGE\_INIT (OP code 0x8001) for bridge

SISPEN\_BRIDGE\_DEBUG\_EN (OP code 0x8006) for bridge

Disable GR Print(UART 0) function

Enable GR Print(UART 0) function

Update FW End, BT will release control the Pen can sleep

Use GPIO 4 to pull the signal high(10ms) to reset the SiS Pen

Initialize the I2C setting

Confirm the sis pen is ready ,using I2C Read

Pen I2C is ready ,

Can use SIS Pen AP CMD (81/82/83/84/85/86) to transfer data

Delay 400ms

Delay 10ms

SISPEN\_BRIDGE\_I2C\_DIS (OP code 0x8008,

CMD 0x00 I2C disable)for bridge

Disable GR I2C master function

Wait for the SIS Pen I2C master to switch to slave after reset

SISPEN\_BRIDGE\_I2C\_EN (OP code 0x8008,

CMD 0x01 I2C Enable)for bridge

Enable GR I2C master function

SISPEN\_BRIDGE\_I2C\_DIS (OP code 0x8008,

CMD 0x00 I2C disable)for bridge

Disable GR I2C master function

SISPEN\_BRIDGE\_HW\_RESET (OP code 0x8007) for bridge

Use GPIO 4 to pull the signal high(10ms) to reset the SiS Pen

注1: CMD 通訊保持1Write(TX) 1Read(RX)的流程，CMD之間delay time >1ms

注2: Update Pen FW Flow可以參考文件SiS\_I2C\_Update Firmware flow\_7501說明.pptx

1. **驗證功能**

GR6613 + 6496C

Step1 Boot Flag clear 0x0001E000 ~ 0x0001EFFF

Step2 Burn FW bin to ROM 0x00004000 ~ 0x0001DFFF

Step3 Boot Flag Write 0x0001E000 ~ 0x0001EFFF

Step4 Compare ROM and FW bin 0x00004000 ~ 0x0001EFFF

進行驗證正常。