

# Zhuojia Shen

Department of Computer Science  
University of Rochester  
Rochester, NY, USA 14627

+1 (585) 629-2327  
zshen10@cs.rochester.edu  
<https://www.cs.rochester.edu/u/zshen10>

## Education

Sept. 2016 – Present	<b>University of Rochester</b> , Rochester, NY, USA Ph.D. in Computer Science <i>Advisor:</i> Prof. John Criswell
Sept. 2016 – Jan. 2019	<b>University of Rochester</b> , Rochester, NY, USA M.S. in Computer Science
Sept. 2012 – June 2016	<b>Beijing Institute of Technology</b> , Beijing, China B.S. in Computer Science & Technology

## Research Interests

Systems: Operating Systems, Compiler Transformations  
Security: Operating System Security, Embedded System Security, Memory Safety, Control-Flow Integrity, Compiler Transformations for Security

## Selected Publications

1. [Zhuojia Shen](#) and John Criswell.  
**InversOS: Efficient Control-Flow Protection for AArch64 Applications with Privilege Inversion.**  
*arXiv e-Print 2304.08717.*  
April 2023.
2. [Zhuojia Shen](#), Komail Dharsee, and John Criswell.  
**Randezvous: Making Randomization Effective on MCUs.**  
In *Proceedings of the 38th Annual Computer Security Applications Conference (ACSAC '22)*.  
Austin, TX, USA. December 2022.
3. Yufei Du, [Zhuojia Shen](#), Komail Dharsee, Jie Zhou, Robert J. Walls, and John Criswell.  
**Holistic Control-Flow Protection on Real-Time Embedded Systems with Kage.**  
In *Proceedings of the 31st USENIX Security Symposium (Security '22)*.  
Boston, MA, USA. August 2022.
4. [Zhuojia Shen](#), Komail Dharsee, and John Criswell.  
**Fast Execute-Only Memory for Embedded Systems.**  
In *Proceedings of the 2020 IEEE Secure Development Conference (SecDev '20)*.  
Atlanta, GA, USA (Virtual). September 2020.
5. Jie Zhou, Yufei Du, [Zhuojia Shen](#), Lele Ma, John Criswell, and Robert J. Walls.  
**Silhouette: Efficient Protected Shadow Stacks for Embedded Systems.**  
In *Proceedings of the 29th USENIX Security Symposium (Security '20)*.  
Boston, MA, USA (Virtual). August 2020.
6. [Zhuojia Shen](#), Jie Zhou, Divya Ojha, and John Criswell.  
**Restricting Control Flow During Speculative Execution with Venkman.**  
*arXiv e-Print 1903.10651.*  
March 2019.
7. [Zhuojia Shen](#), Jie Zhou, Divya Ojha, and John Criswell.  
**POSTER: Restricting Control Flow During Speculative Execution.**  
In *Proceedings of the 2018 ACM SIGSAC Conference on Computer and Communications Security (CCS '18)*.  
Toronto, ON, Canada. October 2018.
8. Xiaowan Dong, [Zhuojia Shen](#), John Criswell, Alan L. Cox, and Sandhya Dwarkadas.  
**Shielding Software from Privileged Side-Channel Attacks.**  
In *Proceedings of the 27th USENIX Security Symposium (Security '18)*.  
Baltimore, MD, USA. August 2018.
9. Xiaowan Dong, [Zhuojia Shen](#), John Criswell, Alan Cox, and Sandhya Dwarkadas.  
**Spectres, Virtual Ghosts, and Hardware Support.**  
In *Proceedings of the 7th Int'l Workshop on Hardware and Architectural Support for Security and Privacy (HASP '18)*.  
Los Angeles, CA, USA. June 2018.

## Work Experience

June 2020 – Aug. 2020	<b>Intern - Member of Technical Staff - VM Monitor</b> VMware, Palo Alto, CA, USA (Virtual) <i>Mentor:</i> Dr. Zheng Cui <ul style="list-style-type: none"><li>Designed and implemented a live patch generation tool for VMKernel</li><li>Implemented a live patch applying mechanism to verify patch correctness</li></ul>
May 2019 – Aug. 2019	<b>Intern - Member of Technical Staff - VM Monitor</b> VMware, Boston, MA, USA <i>Mentor:</i> Dr. Jiajun Cao <ul style="list-style-type: none"><li>Designed and implemented a generic interface for sharing Page-Modification Logging (PML), an Intel processor feature, among modules in the VM monitor</li><li>Evaluated the performance of vMotion utilizing the PML interface</li></ul>
May 2014 – Feb. 2015	<b>Co-Founder &amp; CTO</b> (part-time) Taoxue Information Technology Co., Ltd, Beijing, China <ul style="list-style-type: none"><li>Member of Taoxue Backend Team, main developer and maintainer of Taoxue web server</li><li>Provided technical support to Taoxue iOS and Android Client Teams</li></ul>

## Research Experience

Mar. 2022 – Present	<b>InversOS: Protecting AArch64 Applications with Privilege Inversion</b> <i>Advisor:</i> Prof. John Criswell <ul style="list-style-type: none"><li>Designed Privilege Inversion, a low-cost intra-address space isolation mechanism for AArch64</li><li>Designed and implemented InversOS, a Linux-based OS utilizing Privilege Inversion to protect applications from control-flow hijacking attacks</li><li>Evaluated the performance of InversOS with various benchmarks and applications</li></ul>
Sept. 2020 – Oct. 2022	<b>Rendezvous: Leakage-Resistant Randomization for Microcontrollers (MCUs)</b> <i>Advisor:</i> Prof. John Criswell <ul style="list-style-type: none"><li>Designed and implemented Rendezvous, a randomization-based defense scheme against leakage-equipped brute-forcing control-flow hijacking attacks on ARMv7/8-M MCUs</li><li>Evaluated the security of Rendezvous via statistical modeling and PoC/CVE exploits</li><li>Evaluated the performance of Rendezvous via benchmarks and applications on a real MCU</li></ul>
May 2021 – May 2022	<b>Kage: Holistic Control-Flow Protection for Embedded Real-Time Operating Systems</b> <i>Advisors:</i> Prof. John Criswell and Robert J. Walls <ul style="list-style-type: none"><li>Co-designed and implemented control-flow integrity with unique labels for ARMv7-M</li><li>Co-designed and implemented a code scanner for privileged instructions</li><li>Evaluated the performance of Kage using CoreMark benchmark</li></ul>
Mar. 2020 – Aug. 2020	<b>PicoXOM: Fast Execute-Only Memory for Embedded Systems using Debug Support</b> <i>Advisor:</i> Prof. John Criswell <ul style="list-style-type: none"><li>Designed and implemented PicoXOM, fast XOM for ARMv7/8-M using debug registers</li><li>Evaluated PicoXOM on performance, code size, and security</li></ul>
May 2019 – June 2020	<b>Silhouette: Efficient Protected Shadow Stacks for Embedded Systems</b> <i>Advisors:</i> Prof. John Criswell and Robert J. Walls <ul style="list-style-type: none"><li>Designed and implemented label-based control-flow integrity for ARMv7-M</li><li>Designed a solution for handling <code>setjmp/longjmp</code> that keeps the integrity of return addresses</li><li>Evaluated Silhouette's performance and code size on multiple benchmarks and applications</li></ul>
July 2018 – May 2019	<b>Venkman: Software-Based Defenses against Spectre Attacks</b> <i>Advisor:</i> Prof. John Criswell <ul style="list-style-type: none"><li>Co-designed and implemented a software-based defense to defeat existing and potential Spectre attacks that poison CPU's branch target buffer/return stack buffer and leak information via branches</li><li>Co-designed and implemented a software fault isolation technique that resists Store-to-Fetch Forwarding attacks on programs' code segment</li><li>Evaluated performance and code size overhead of Venkman on POWER architecture</li></ul>
July 2017 – June 2018	<b>Defenses against Privileged Side-Channel Attacks</b>

	<i>Advisors:</i> Prof. John Criswell, Sandhya Dwarkadas, and Alan L. Cox <ul style="list-style-type: none"> <li>• Co-designed and implemented a physical frame buffer queue in Apparition, a shielding system that protects applications from untrusted OSes, to obfuscate applications' memory allocation patterns</li> <li>• Modified the FreeBSD C library to allow applications to transparently utilize a secure memory allocator interface</li> <li>• Optimized the bit-masking software fault isolation to resist Spectre and Meltdown attacks</li> </ul>
Jan. 2017 – July 2017	<b>Structure Field Software Fault Isolation</b> <i>Advisor:</i> Prof. John Criswell <ul style="list-style-type: none"> <li>• Designed and implemented several FreeBSD kernel-level rootkits</li> <li>• Implemented the Padding Area MetaData (PAMD) shadow table in FreeBSD kernel space for heap objects</li> </ul>
Feb. 2014 – June 2014	<b>The Circular Wirelength Problem for 4-Dimensional Hypercubes</b> <i>Advisor:</i> Prof. Qinghui Liu <ul style="list-style-type: none"> <li>• Designed and implemented a distributed enumeration system to find out the minimum wirelength of hypercubes</li> <li>• Developed a GUI tool helping discover the characteristics of a hypercube with minimum wirelength</li> </ul>
Dec. 2013 – Apr. 2014	<b>Data Race Detector for Multi-Threaded Programs</b> <i>Advisor:</i> Prof. Weixing Ji <ul style="list-style-type: none"> <li>• Designed and implemented the segment AVL tree as an efficient realization of set ADT, used to record memory addresses dereferenced by a thread in a process</li> <li>• Conducted performance evaluation and profiling on several different implementations</li> </ul>

## Teaching Experience

Sept. 2018 – Dec. 2018	<b>Graduate Teaching Assistant</b> <i>Course:</i> CSC 256/456 Operating Systems <i>Instructor:</i> Prof. Sandhya Dwarkadas
Jan. 2018 – May 2018	<b>Graduate Teaching Assistant</b> <i>Course:</i> CSC 261/461 Database Systems <i>Instructor:</i> Dr. Tamal Biswas
Sept. 2017 – Dec. 2017	<b>Graduate Teaching Assistant</b> <i>Course:</i> CSC 256/456 Operating Systems <i>Instructor:</i> Prof. John Criswell
Mar. 2016 – June 2016	<b>Undergraduate Teaching Assistant</b> <i>Course:</i> Computational Theory & Algorithm Analysis Design <i>Course:</i> Combinatorial Mathematics <i>Instructor:</i> Prof. Qinghui Liu

## Honors & Awards

- Senior honors thesis, Beijing Institute of Technology, 2016
- Third-class People's Scholarship (four times), Beijing Institute of Technology, 2012 – 2015

## Skills

Programming languages:	C/C++, Java, C#, Bash, Python, Ruby, JavaScript, x86/ARM/PowerPC/MIPS Assembly, SQL
Software & tools:	Vim, Git, LLVM, Docker, Mutt, Eclipse, Sublime Text, Microsoft Office, L <sup>A</sup> T <sub>E</sub> X
Platforms:	Linux, Windows, FreeBSD, VMware ESXi