

Chapel On Accelerators

Rahul Ghangas (Australian National University)
Supervisor - Dr. Josh Milthorpe (Australian National University)



A Little About Me

- Rahul Ghangas
- Final semester student at ANU
- Working on Chapel for my honours thesis
- Have been a Chapel enthusiast for a while now



Why GPUs?







CPU - Xeon Scalable Processor GPU - Intel Xe



CPU - AMD Milan GPU - Nvidia Tesla

Source : hpcwire.com



The Two-Language Problem

C Host Code

```
cl device id device id: // compute device id
cl_context context; // compute context
cl command queue commands; // compute command queue
cl program program; // compute program
cl_kernel ko_vadd; // compute kernel
cl mem d a;
cl mem d b;
cl_mem d_c;
int i = 0;
for(i = 0: i < LENGTH: i++){
h_a[i] = rand() / (float)RAND_MAX;
h_b[i] = rand() / (float)RAND_MAX;
h e[i] = rand() / (float)RAND MAX:
h_g[i] = rand() / (float)RAND_MAX;
cl uint numPlatforms:
err = clGetPlatformIDs(0, NULL, &numPlatforms);
checkError(err, "Finding platforms");
if (numPlatforms == 0)
printf("Found 0 platforms!\n");
return EXIT_FAILURE;
cl platform id Platform[numPlatforms]:
err = clGetPlatformIDs(numPlatforms, Platform, NULL);
checkError(err, "Getting platforms");
```

Openci C kernel code

4



Needs native support for "productivity"

- Chapel's high level constructs offer a "productive" interface to parallel/distributed computing for any programmer
 - 1. Reduce Expressions
 - 2. Operator Promotion
 - 3. Forall/CoForall Loops

Reduction Operations

- Initial native GPU support, follows up on previous work[1]
- Making it work with the current state of the compiler
- Extending support for complex expressions

AMD Proposal

```
const D = {1..100};
var varA : int;
var varA : [D] int = 13;

on here.GPU do {
  varA = + reduce varB;
}
```

Our Proposal

```
const D = {1..100};
var varA : int;
var varB : [D] int = 12;
var varC : [D] int = 13;

on here.GPU do {
  varA = + reduce (varB + 3 * varC);
}
```

[1] Michael L. Chu, Ashwin M .Aji, Daniel Lowell, and Khaled Hamidouche. *GPGPU support in Chapel with the Radeon Open Compute Platform*. CHIUW 2017.



Inferred intents for OpenCL kernels

- Infer compile time constants and bake into the kernel
- Similarly, use run-time constants as __constant, allowing architectures
 to utilize cache instead of global memory.
- Another more aggressive improvement (experimental), split and transfer arrays to local memory based on usage for faster access



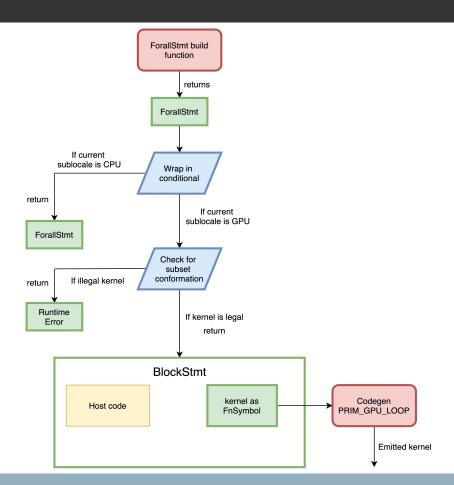
Current: Using LLVM GPGPU backends

- Chapel has been gradually moving towards making LLVM the default backend
- Our current focus is on NVPTX and AMDGPU backends for NVIDIA and AMD GPUs respectively, and SPIR-V for Intel GPUs
- The idea is to directly emit IR kernels and generate corresponding host code



Forall Loops

- Simply reuse compiler ForallStmt
- Minimally invasive on the compiler





Defining a subset of Chapel

- Disallow external function calls
- Disallow on blocks
- Disallow begin/cobegin statements
- Disallow nested parallel loops inside forall loops intended for GPU execution
- Restrict array usage to index expressions only



Defining Work group sizes

Approach 1

 Coforall wrapped forall loops to define the number of work groups spanning each dimension, and in turn, the work group size

Approach 2

 Propose language extensions to support defining work group sizes using with intents



Lastly – A toy implementation GPUArray(s)

- Lazily evaluated arithmetic operations
- Builds up an intermediate representation (currently Strings)
- Evaluated only for arrays that call compute()