

Charantej Reddy POCHIMIREDDY

Ph.D. candidate in Communication and Signal Processing

PERSONAL DATA

PLACE AND DATE OF BIRTH: Simhadripuram, India | 19 June 1992
ADDRESS: IIT Hyderabad, Hyderabad, India
PHONE: +91-8985863425
EMAIL: ee18rescho1010@iith.ac.in

RESEARCH INTERESTS

Adversarial Machine Learning, and Signal Processing

WORK EXPERIENCE

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| 05-12 2022 | Visiting researcher at SWINBURNE UNIVERSITY OF TECHNOLOGY, Australia
(part of Joint Ph.D. program)
Worked on problems in Signal processing and Graph learning. |
| 07-08 2019 | Research Intern at TCS INNOVATION LABS, Pune
Worked on generating time series data using generative adversarial networks. |
| 08-12 2017 | Senior Associate Software Engineer at MATHWORKS INDIA PVT. LTD., Hyderabad
Worked on traffic sign detection and recognition and logo recognition using convolutional neural networks. |
| 02-07 2017 | Image Processing Engineer at UURMI SYSTEMS PVT. LTD., Hyderabad
Worked on pedestrian detection using deep learning (in MATLAB) and compressing convolutional neural networks for real-time application. |
| 07-11 2016 | Application Support Engineer at MATHWORKS INDIA PVT. LTD., Bangalore
Helped customers to solve technical problems using MATLAB and Simulink and worked with the developers to apply the customer-facing knowledge to projects within the company. |

EDUCATION

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| SINCE 2018 | Joint Ph.D. in COMMUNICATION AND SIGNAL PROCESSING, IIT Hyderabad , Hyderabad and Swinburne University of Technology , Melbourne
Thesis: "Fast and Efficient DFT Computation for Signals with Structured Support"
Advisor: Dr. Aditya Siripuram Co-Advisor: Dr. Jingxin Zhang
CGPA: 9.5/10 |
| JUNE 2016 | Masters in COMMUNICATION AND SIGNAL PROCESSING, IIT Hyderabad , Hyderabad
Thesis: "Sparsity based spatio-temporal video quality assessment"
Advisor: Dr. Sumohana Channappayya
CGPA: 9.55/10 |
| MAY 2013 | Bachelors in ELECTRONICS AND COMMUNICATION, SNIST, JNTU Hyderabad , Hyderabad
CGPA: 77.02/100 |

PUBLICATIONS

- [1] Charantej Reddy P, Aditya Siripuram, and Brad Osgood. *Fast DFT Computation for Signals with Structured Support*. 2022. arXiv: [2211.15299](https://arxiv.org/abs/2211.15299) [eess.SP].
- [2] Charantej Reddy Pochimireddy, Aditya T. Siripuram, and Sumohana S. Channappayya. "Can Perceptual Guidance Lead to Semantically Explainable Adversarial Perturbations?" In: *IEEE Journal of Selected Topics in Signal Processing* (2023), pp. 1–11. DOI: [10.1109/JSTSP.2023.3258253](https://doi.org/10.1109/JSTSP.2023.3258253).
- [3] P Charantej Reddy, Aditya Siripuram, and Brad Osgood. "Some results on convolution idempotents". In: *2020 IEEE International Symposium on Information Theory (ISIT)*. IEEE. 2020, pp. 1462–1467.
- [4] P Charantej Reddy, V S S Prabhu Tej, and Aditya Siripuram. "Fast DFT computation for signals with spectral support". In: *2021 National Conference on Communications (NCC)*. 2021, pp. 1–6. DOI: [10.1109/NCC52529.2021.9530137](https://doi.org/10.1109/NCC52529.2021.9530137).
- [5] P Charantej Reddy, V S S Prabhu Tej, Aditya Siripuram, and Brad Osgood. "Computing the Discrete Fourier Transform of signals with spectral frequency support". In: *2021 IEEE International Symposium on Information Theory (ISIT)*. 2021, pp. 2381–2386. DOI: [10.1109/ISIT45174.2021.9518104](https://doi.org/10.1109/ISIT45174.2021.9518104).
- [6] B Subbareddy, P Charantej Reddy, Aditya Siripuram, and Jingxin Zhang. "A survey of signal processing based graph learning techniques". In: *2019 1st International Conference on Industrial Artificial Intelligence (IAI)*. IEEE. 2019, pp. 1–6.

AWARDS AND RECOGNITIONS

- Recipient of the **Qualcomm Innovation Fellowship** (QIF-2021).
- Mentor for BTech EE student team that was placed sixth worldwide in IEEE VIP Cup 2019.

COMPUTER SKILLS

Programming: C, PYTHON
Tools: MATLAB, Simulink, Tensorflow, Pytorch

INTERESTS AND ACTIVITIES

Technology, Cricket

REFERENCES

Dr. Aditya Siripuram

Assistant Professor
Electrical Engineering Department, IIT Hyderabad
Address: Room no:C-512,IIT Hyderabad,Hyderabad
Email: staditya@iith.ac.in
Tel: (+91) 8555014746

Dr. Sumohana S. Channappayya

Professor
Electrical Engineering Department, IIT Hyderabad
Address: Room no:C-509,IIT Hyderabad,Hyderabad
Email: sumohana@iith.ac.in
Tel: (+91) 9963046110

Dr. Jingxin Zhang

Associate Professor

School of Software and Electrical Engineering, SUT

Address: Room no:EN701e, John Street, Hawthorn, Victoria, Australia 3122

Email: jingxinzhang@swin.edu.au

Tel: (+61) 3 9214 5765