1. Description

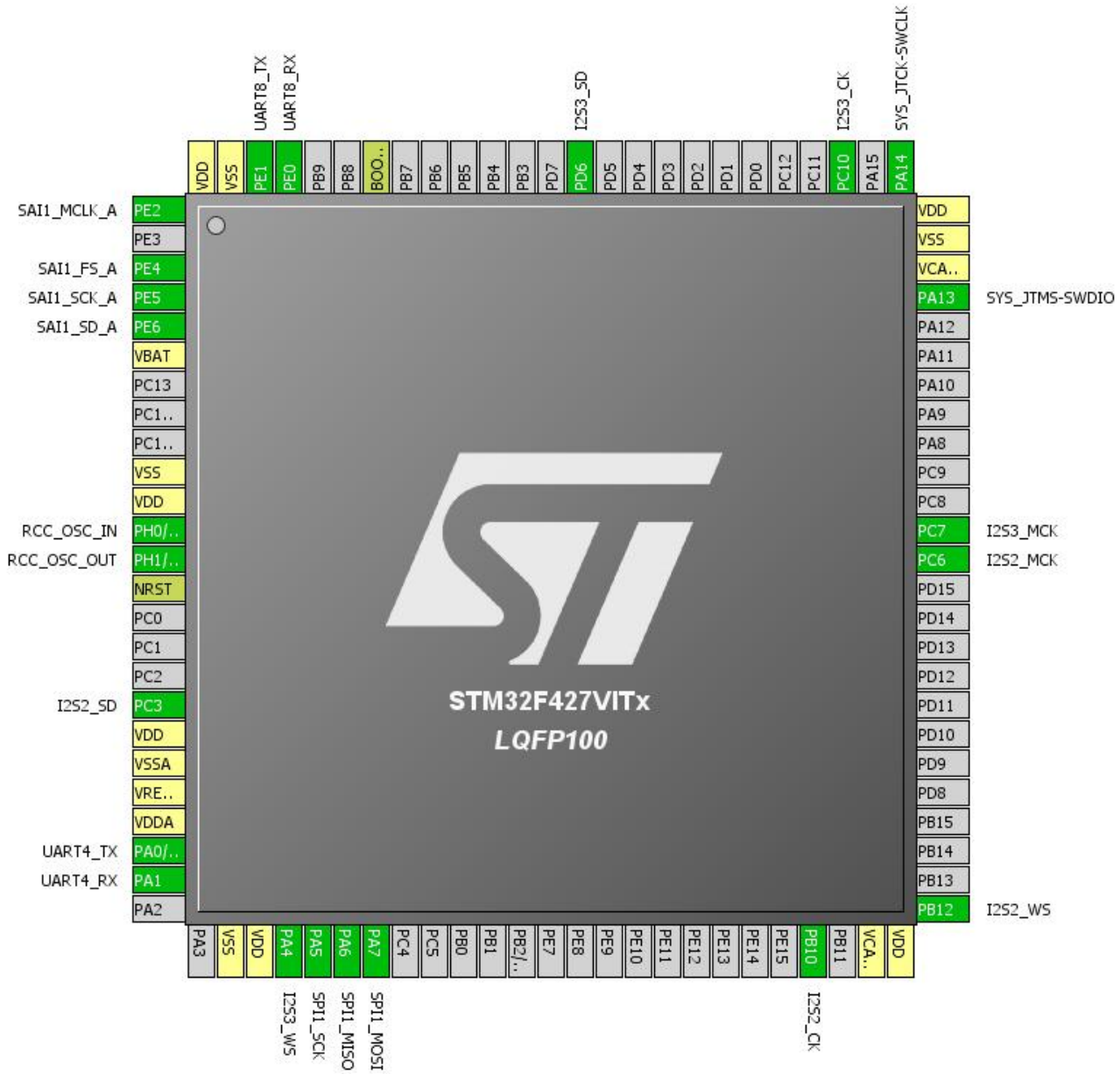
1.1. Project

Project Name	STM32F427_cube
Generated with:	STM32CubeMX 4.0.0
Date	03/27/2014

1.2. MCU

MCU Serie	STM32F4
MCU Line	STM32F427/437
MCU name	STM32F427VITx
MCU Package	LQFP100
MCU Pin number	100

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
I2S2	Mode: Half-Duplex Master	I2S2_CK	PB10
		I2S2_SD	PC3
		I2S2_WS	PB12
	Master Clock Output	I2S2_MCK	PC6
I2S3	Mode: Half-Duplex Master	I2S3_CK	PC10
		I2S3_SD	PD6
		I2S3_WS	PA4
	Master Clock Output	I2S3_MCK	PC7
RCC	High Speed Clock (HSE): Crystal/Ceramic Resonator	RCC_OSC_IN	PH0/OSC_IN
		RCC_OSC_OUT	PH1/OSC_OUT
SAI1	SAI_A Mode: Master with Master Clock Out	SAI1_SD_A	PE6
		SAI1_SCK_A	PE5
		SAI1_FS_A	PE4
		SAI1_MCLK_A	PE2
SPI1	Mode: Full-Duplex Master	SPI1_MISO	PA6
		SPI1_MOSI	PA7
		SPI1_SCK	PA5
SYS	Debug: Serial Wire Debug (SWD)	SYS_JTCK-SWCLK	PA14
		SYS_JTMS-SWDIO	PA13
UART4	Mode: Asynchronous	UART4_RX	PA1
		UART4_TX	PA0/WKUP
UART8	Mode: Asynchronous	UART8_RX	PE0
		UART8_TX	PE1

4. Pins Configuration

Pins	Pos	Functions
PE2	1	SAI1_MCLK_A
PE4	3	SAI1_FS_A
PE5	4	SAI1_SCK_A
PE6	5	SAI1_SD_A
PH0/OSC_IN	12	RCC_OSC_IN
PH1/OSC_OUT	13	RCC_OSC_OUT
PC3	18	I2S2_SD
PA0/WKUP	23	UART4_TX
PA1	24	UART4_RX
PA4	29	I2S3_WS
PA5	30	SPI1_SCK
PA6	31	SPI1_MISO
PA7	32	SPI1_MOSI
PB10	47	I2S2_CK
PB12	51	I2S2_WS
PC6	63	I2S2_MCK
PC7	64	I2S3_MCK
PA13	72	SYS_JTMS-SWDIO
PA14	76	SYS_JTCK-SWCLK
PC10	78	I2S3_CK
PD6	87	I2S3_SD
PE0	97	UART8_RX
PE1	98	UART8_TX

5. Power Plugin report

5.1. Microcontroller Selection

Serie	STM32F4
Line	STM32F427/437
MCU	STM32F427VITx
Datasheet	024030_Rev3

5.2. Parameter Selection

Temperature	25
Vdd	3.3

6. Software Project

6.1. Project Settings

Name	Value
Project Name	STM32F427_cube
Project Folder	G:\plume\PCB
Toolchain / IDE	EWARM 6.70
Firmware Package Name and Version	STM32Cube FW_F4 V1.1.0

6.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	Yes

6.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed