Motilal Nehru National Institute of Technology Allahabad Department of Computer Science and Engineering MCA III-Sem, End-Sem Exam, December 2016 Operating System (CA3301)

Time 3 Hour

All questions are compulsory. Assume any missing data and mention it at the top of answer.

M.M. 60

- Q 1 Consider a pre-emptive priority scheduling algorithm based on dynamically changing priorities. Larger priority numbers imply higher priority. When a process is waiting for the CPU (in the ready queue, but not running), its priority changes at a rate δ; when it is running, its priority changes at a rate μ. All processes are given a priority of 0 when they enter the ready queue. The parameters δ and μ can be set to give many different scheduling algorithms.
 - 1. What is the algorithm that results from $\mu > \delta > 0$?
 - 2. What is the algorithm that results from $\delta < \mu < 0$?
- Consider a memory system with a cache access time of 10ns and a memory access time of 200ns, including the time to check the cache. What hit rate H would be required in order to achieve an effective access time 10% greater than the cache access time?
- Consider the following page reference string:

 1, 2, 3, 4, 2, 1, 5, 6, 2, 1, 2, 3, 7, 6, 3, 2, 1, 2, 3, 6.

 How many page faults' would occur for the following replacement algorithms, assuming five frames? Remember all frames are initially empty, so your first unique pages will all cost one fault each.
 - LRU replacement
 - FIFO replacement
 - Optimal replacement
- Q 4 Suppose that you wish to design a virtual memory system with the following 3*2 characteristics:
 - The size of a page table entry is 4 bytes.
 - Each page table must fit into a single physical frame.
 - The system must be able to support virtual address spaces as large as 256 GB.
 - 1. Suppose that you decide to use a multi-level paging scheme with no more than two levels of page tables. What is the minimum page size that your system must have?
 - 2. Suppose instead that you are willing to use a three-level paging scheme. What is the minimum page size that your system must have in this case?
- Consider a system with three smoker processes and one agent process. Each smoker continuously rolls a cigarette and then smokes it. But to roll and smoke a cigarette the smoker needs three ingredients: tobacco, paper and matches. One of the smoker processes has paper another has tobacco and the third has matches. The agent has an infinite supply of all three materials. The agent places two of the ingredients on the cigarette, signalling the agent on completion. The agent then makes and smokes a the three ingredients, and the cycle repeats. Write a program to synchronize the agent and the smokers using synchronization.
 - Consider a demand-paged computer system where the degree of multiprogramming is 3*2 currently fixed at four. The system was recently measured to determine utilization of marks CPU and the paging disk. The results are one of the following alternatives. For each

case, what is happening? Can the degree of multiprogramming be increased to increase the CPU utilization? Is the paging helping?

- 1. CPU utilization 13 percent; disk utilization 97 percent
- 2. CPU utilization 87 percent; disk utilization 3 percent
- 3. CPU utilization 13 percent; disk utilization 3 percent

Name and explain the four conditions of the deadlock.

4 marks

Assume that a task is divided into four equal-sized segments and that the system builds an eight-entry page descriptor table for each segment. Thus, the system has a combination of segmentation and paging. Assume also that the page size is 2 Kbytes.

1+2+2 marks

- a. What is the maximum size of each segment?
- b. What is the maximum logical address space for the task?
- c. Assume that an element in physical location 00021ABC is accessed by this task. What is the format of the logical address that the task generates for it? What is the maximum physical address space for the system?

Q9

Explain the following.

1*5 marks

- Thrashing 1.
- Working set model
- Multiprogramming vs. timesharing
- TLB
- ULT and KLT

Q10

You have been hired by Allahabad Development Authority (ADA) to automate the 10 marks flow of traffic on a one-lane bridge between Jhunsi and Allahabad that has been the site of numerous collisions. ADA wants you to implement the following rules:

- Traffic can flow in only a single direction on the bridge at a time.
- Any number of cars can be on the bridge at the same time, as long as they are all traveling in the same direction.
- To avoid starvation, you must implement the "five car rule": once 5 or more consecutive Allahabad bound cars have entered the bridge, if there are any Jhunsi bound cars waiting then no more Allahabad bound cars may enter the bridge until some Jhunsi bound cars have crossed. A similar rule also applies once 5 or more consecutive Jhunsi bound cars have entered the bridge.