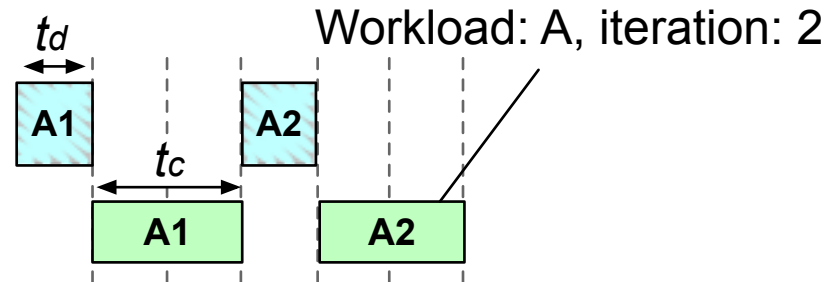


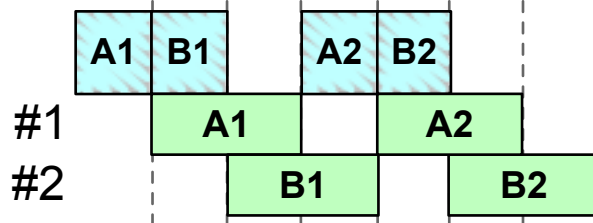
(a) No parallelism

Memory Access Pipeline

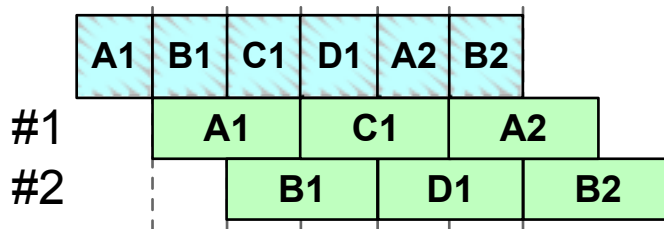
Logic Pipelines #1



(b) Parallelism
w/o pipelining



(c) Parallelism
w/ pipelining
($t_c = 2t_d$)



(d) Parallelism
w/ pipelining
($t_c < 2t_d$)

