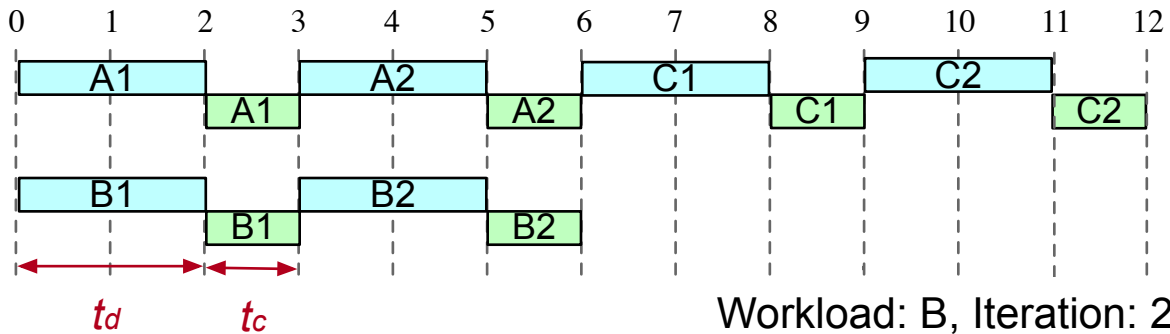
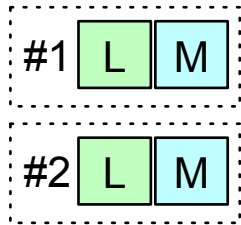
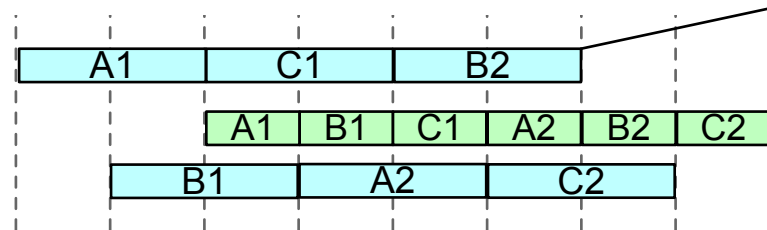
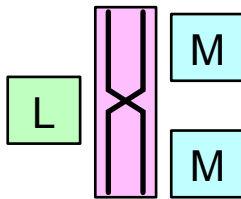


Multi-core



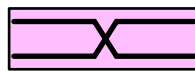
PULSE



Logic pipeline



Memory pipeline



Scheduler