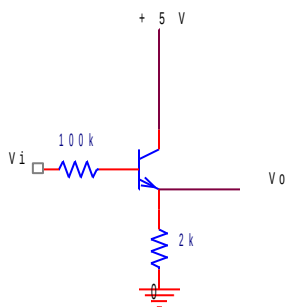


Sample Test 3

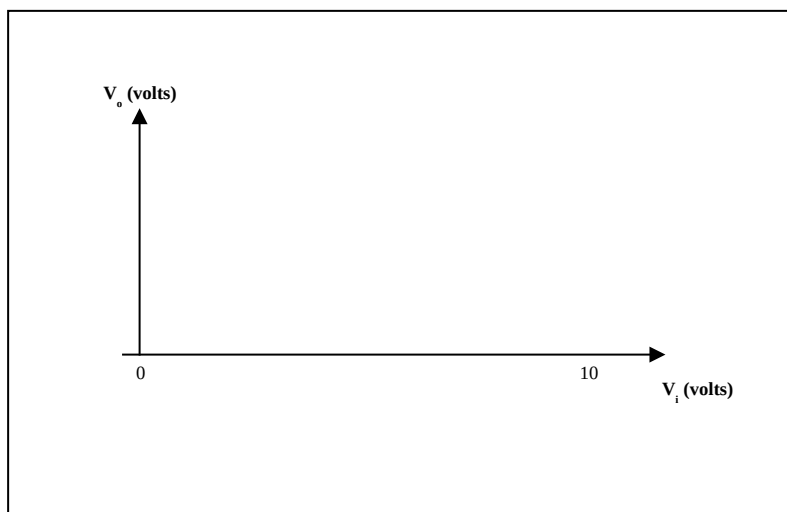
For all problems assume (1) $V_{BE(ON)} = V_{EB(ON)} = 0.7$ volts, (2) $V_{CE(SAT)} = V_{EC(SAT)} = 0.2$ volts

Q1 For the transistor circuit shown,

- a) (4) Sketch the transfer characteristic V_i vs. V_o for $0 < V_i < 10$ volts. Use the graph axes supplied. Assume $\beta = 100$, $V_A = \infty$.

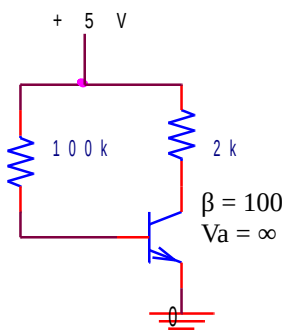


Part (a)



Q2 For the circuits shown find the indicated quantities.

Part (a)

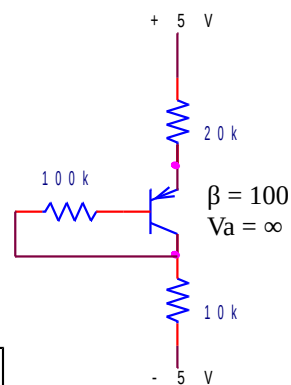


$$I_B = \underline{\hspace{2cm}}$$

$$I_C = \underline{\hspace{2cm}}$$

$$V_{CE} = \underline{\hspace{2cm}}$$

Part (b)



$$I_B = \underline{\hspace{2cm}}$$

$$I_C = \underline{\hspace{2cm}}$$

$$V_{EC} = \underline{\hspace{2cm}}$$

Sample Test 3

For all problems assume (1) $V_{BE(ON)} = V_{EB(ON)} = 0.7$ volts, (2) $V_{CE(SAT)} = V_{EC(SAT)} = 0.2$ volts

Q3 The circuit shown is called a complementary push-pull amplifier stage. For the input (V_i) shown sketch the corresponding output (V_o).

