

① Fig 9-18(b) Init PORTB, Read Switches, Turn On LEDs.

REGB EQU 0x01

ORG 0x20

INIT: MOVLW B'10101010' ; SW + LED

MOVWF TRISB

BCF INTCON 2, 7 ; Enable pull-up

LOOP: MOVFF PORTB, REGB

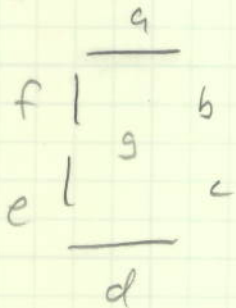
CMF REGB, F ; Active Low

RRNCF REGB, F

MOVFF REGB, PORTB

BRA LOOP

②



dp g f e d c b a

1 1 0 0 0 1 1 0

Active Low

CLRF TRISD

LOOP: MOVLW 0xC6

MOVWF PORTD

CALL DELAY-100ms

SETF PORTD

CALL DELAY-100ms

BRA LOOP

(3) A/D Conv + Timer 2 High priority

ORG 0x00

GOTO MAIN

ORG 0x08

BTFSK PIR1, ADIF

GOTO AD-ISR

BTFSK PIR1, TMR2IF

GOTO TMR2-ISR

RETFIE FAST

ORG 0x20

MAIN: BSF IPR1, ADIP

BSF IPR1, TMR2IP

BSF PIE1, ADIE

BSF PIE1, TMR2IP