

## II. Common Source Amplifier w/ Source Resistor

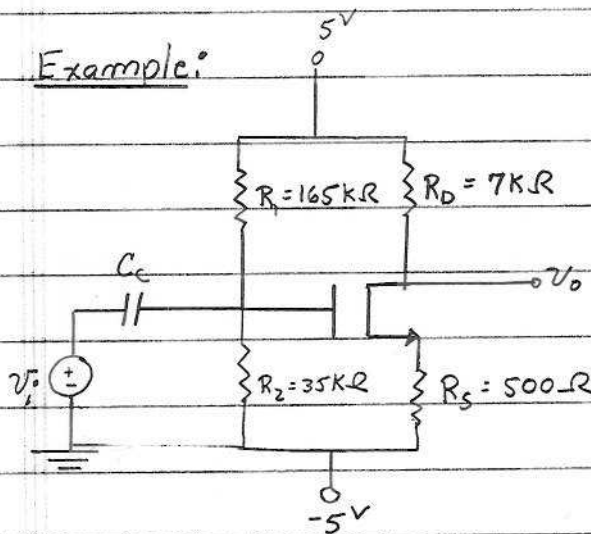
### Advantage of Source Resistor

- Stabilizes Q pt. for variation in transistor parameters

### Disadvantage:

- Reduces the small signal gain of amplifier

### Example:



$$V_{TN} = 0.8V$$

$$K_n = 1 \text{ mA/V}^2$$

$$\lambda = 0$$

### 1. DC Analysis

$$V_G = \left( \frac{35}{165+35} \right) 10 - 5 = -3.25V$$

$$I_D = \frac{V_S - (-5V)}{R_S} = \frac{V_G - V_{GS} + 5}{R_S} = K_n (V_{GS} - V_{TN})^2$$

$$V_G - V_{GS} + 5 = R_S K_n (V_{GS} - V_{TN})^2$$

$$-V_{GS} + 1.75 = \frac{1}{2} (V_{GS} - 0.8)^2 = \frac{1}{2} \{ V_{GS}^2 - 1.6 V_{GS} + 0.64 \}$$

$$V_{GS}^2 + 0.4 V_{GS} - 2.86 = 0$$

$$V_{GS} = -0.2 \pm 1.7 = \boxed{1.5V} \text{ or } -1.9V$$

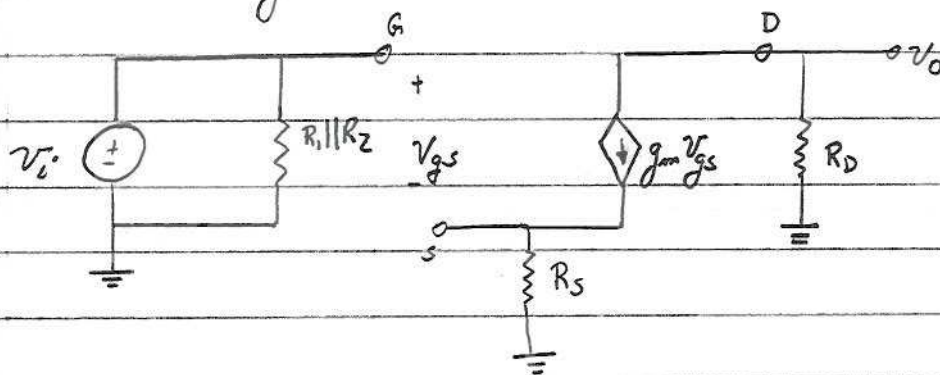
$$I_D = 1 \text{ mA/V}^2 (1.5 - 0.8)^2 = \boxed{0.5 \text{ mA}}$$

$$V_{DS} = V_D - V_S = 10 - (0.5 \text{ mA}) 7.5 \text{ K} = 6.25 \text{ V}$$

$$g_m = 2 K_n (V_{GS} - V_{TN}) = 2 (1) (1.5 - 0.8) = 1.4 \text{ mA/V}$$

$$r_o = \frac{1}{\lambda I_{DQ}} = \infty$$

2. The small signal model is:



3. The output voltage is:

$$v_o = -g_m v_{gs} R_D$$

$$\text{KVL: } -v_i + v_{gs} + g_m v_{gs} R_S = 0$$

$$v_{gs} = v_i \left( \frac{1}{1 + g_m R_S} \right)$$

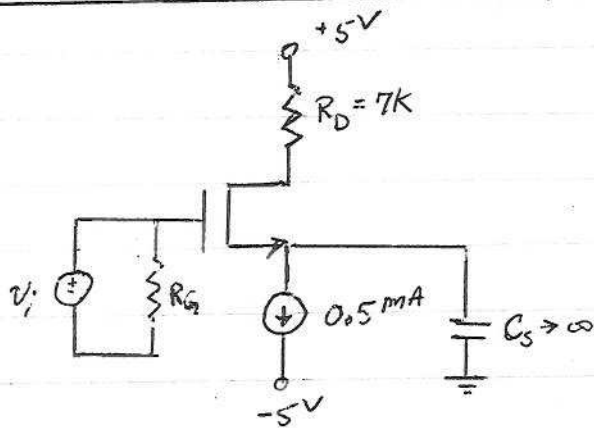
4. The small signal gain is:

$$A_v = \frac{v_o}{v_i} = \frac{-g_m R_D}{1 + g_m R_S} = \frac{-(1.4)(7)}{1 + (1.4)(0.5)} = -5.76$$

Note:

	$K_n \text{ (mA/V}^2\text{)}$	$g_m \text{ (mA/V)}$	$A_v$	
50% change	0.8	1.17	-5.17	16% change
	1.0	1.40	-5.76	
	1.2	1.62	-6.27	

# Common Source w/ Bypass Capacitor



$$K_n = 1 \text{ mA/V}^2$$

$$V_{TN} = 0.8 \text{ V}$$

$$\lambda = 0$$

## ① DC Analysis

$$I_{DQ} = K_n (V_{GSQ} - V_{TN})^2$$

$$0.5 \text{ mA} = (1 \text{ mA/V}^2) (V_{GSQ} - 0.8)^2$$

$$V_{GSQ} = 1.51 \text{ V} = V_G - V_S$$

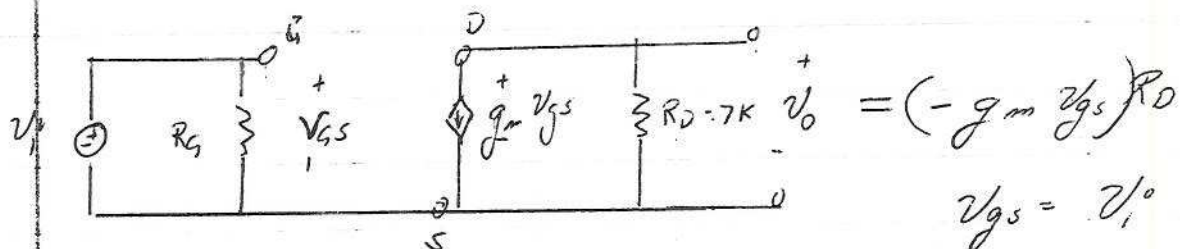
$$V_{DSQ} = V_{DD} - I_D R_D - V_S$$

$$= 5 - (0.5) 7 - (-1.51)$$

$$= 3.01 \text{ V}$$

$$V_{DSQ} > V_{DS(SAT)} = V_{GS} - V_{TN} = 1.51 - 0.8 = 0.71 \text{ V}$$

## ② Small Signal Model



$$v_{gs} = v_i$$

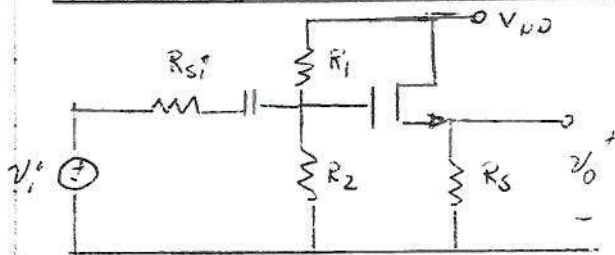
## ③ Voltage gain

$$g_m = 2 \sqrt{K_n I_{DQ}} = 1.414$$

$$A_v = v_o / v_i = -g_m R_D = -9.9$$



## Source Follower Example



$$V_{DD} = 12\text{V}$$

$$V_{TN} = 1.5\text{V}$$

$$R_1 = 162\text{K}$$

$$k_n = 4\text{mA/V}^2$$

$$R_2 = 463\text{K}$$

$$\lambda = 0.01\text{V}^{-1}$$

$$R_S = 0.75\text{K}$$

$$R_{Si} = 4\text{K}$$

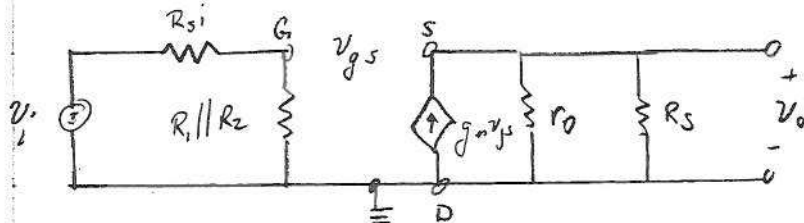
### ① DC ANALYSIS

CAN you show  $I_{DQ} = 7.97\text{mA}$ ,  $V_{GSQ} = 2.91\text{V}$ ?

### ② Small Signal model

$$g_m = 2\sqrt{k_n I_{DQ}} = 2\sqrt{(4)(7.97)} = \underline{11.3\text{mA/V}^2}$$

$$r_o = \frac{1}{\lambda I_{DQ}} = \frac{1}{0.01(7.97)} = \underline{12.5\text{K}\Omega}$$

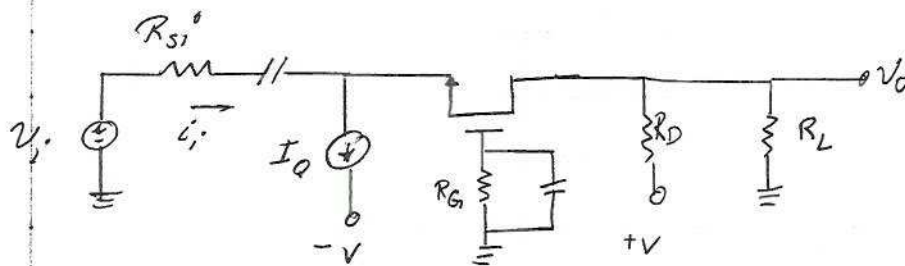


$$R_i = R_1 \parallel R_2 = 162 \parallel 463 = \underline{120\text{K}\Omega}$$

$$A_v = \frac{R_S \parallel r_o}{\frac{1}{g_m} + R_S \parallel r_o} \left( \frac{R_i}{R_i + R_{Si}} \right)$$

$$= \frac{(0.75 \parallel 12.5)}{\frac{1}{11.3} + 0.75 \parallel 12.5} \left( \frac{120}{120 + 4\text{K}} \right) = \underline{\underline{0.86}}$$

# Common GATE Example (Ex 4.10)



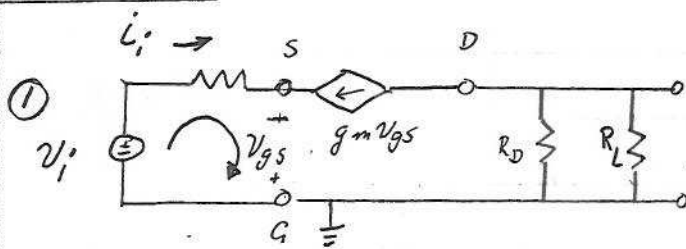
$$V_{TN} = 1V$$

$$K_n = 1 \text{ mA/V}^2$$

$$\lambda = 0$$

Given:  $I_Q = 1 \text{ mA}$ ,  $V^+ = 5V$ ,  $V^- = -5V$ ,  $R_G = 100 \text{ k}\Omega$   
 $R_D = 4 \text{ k}\Omega$ ,  $R_L = 10 \text{ k}\Omega$   
 $i_i = (100 \mu\text{A}) \sin \omega t$   
 $R_{Si} = 50 \Omega$

Find  $v_o$



$$v_o = -g_m v_{gs} (R_D \parallel R_L)$$

$$-v_i + i_i R_{Si} - v_{gs} = 0$$

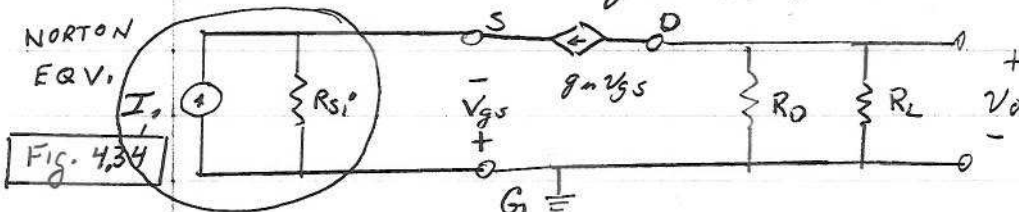
$$-v_i - g_m v_{gs} R_{Si} - v_{gs} = 0$$

$$v_{gs} = -\frac{v_i}{1 + g_m R_{Si}}$$

$$\textcircled{2} \quad g_m = 2 \sqrt{K_n I_{Q2}} = 2 \sqrt{(1)(1)} = 2 \text{ mA/V}$$

$$\textcircled{3} \quad v_o = v_i \left( \frac{g_m (R_D \parallel R_L)}{1 + g_m R_{Si}} \right) = (i_i R_{Si}) \left( \frac{g_m (R_D \parallel R_L)}{1 + g_m R_{Si}} \right)$$

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$$v_o = (100 \mu\text{A})(50) \left( \frac{2(4 \parallel 10)}{1 + 2(0.05)} \right) = \boxed{26 \text{ mV} \sin \omega t}$$

~5.2

TEXT SOLUTION  
IS WRONG!