#### Sequential Analysis

# ELEC 311 Digital Logic and Circuits Dr. Ron Hayne

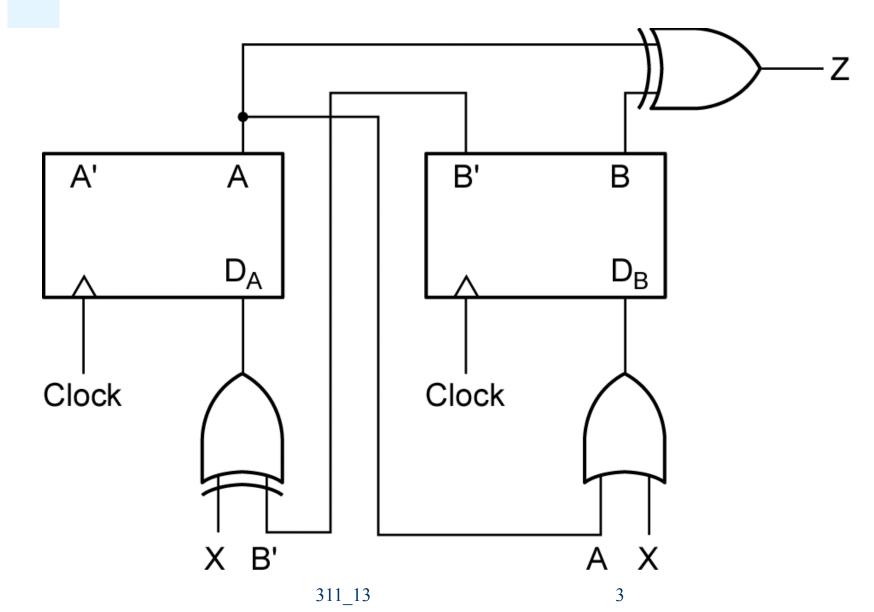
Images Courtesy of Cengage Learning

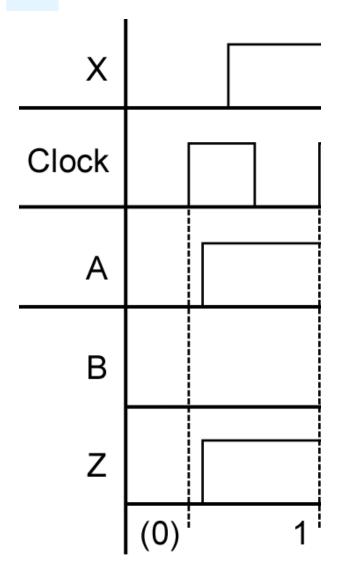


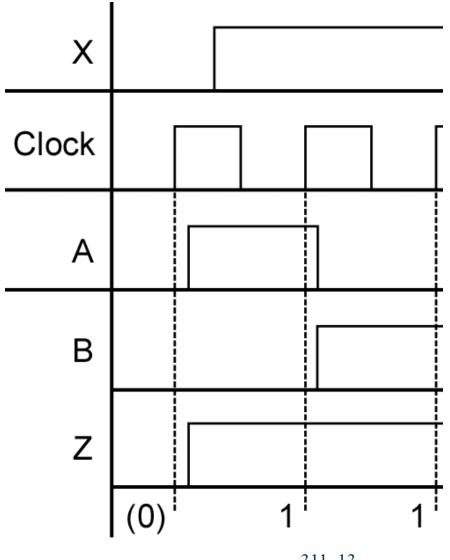
#### State Tables and Graphs

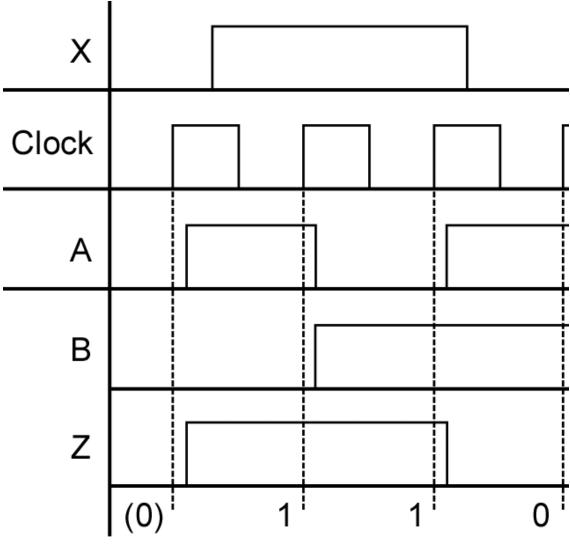
Present	Next State		Output
State	X = 0	X = 1	Z
S0	<b>S</b> 1	S0	0
<b>S</b> 1	S3	S2	0
S2	S3	S0	0
S3	S3	S0	1

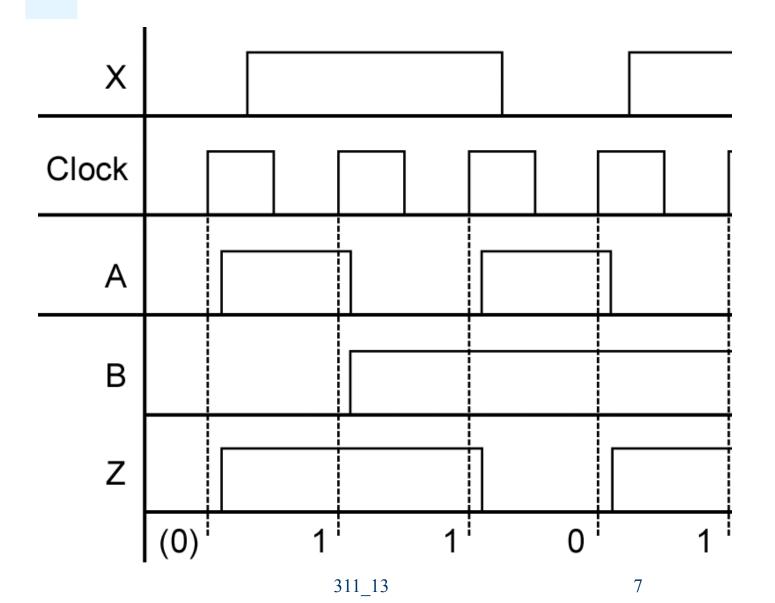
#### Analysis Example

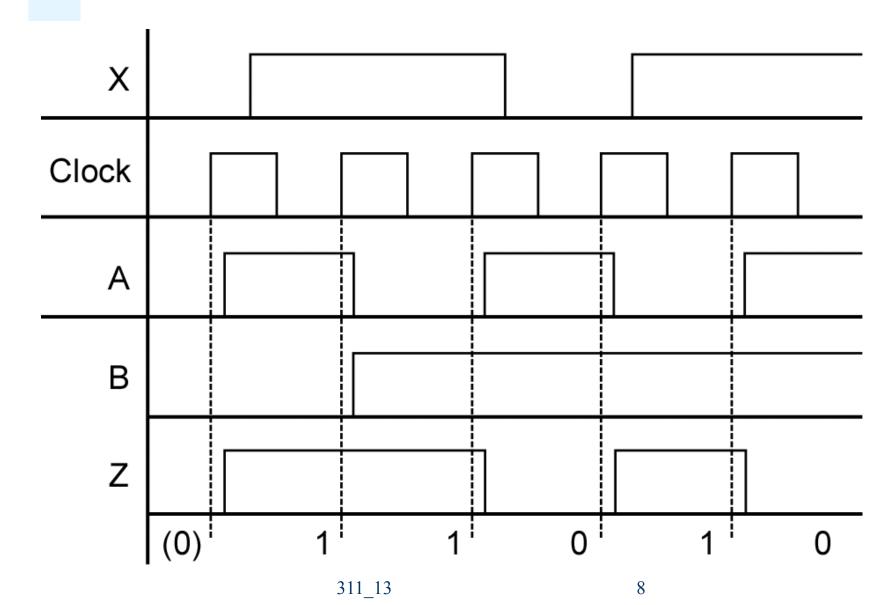




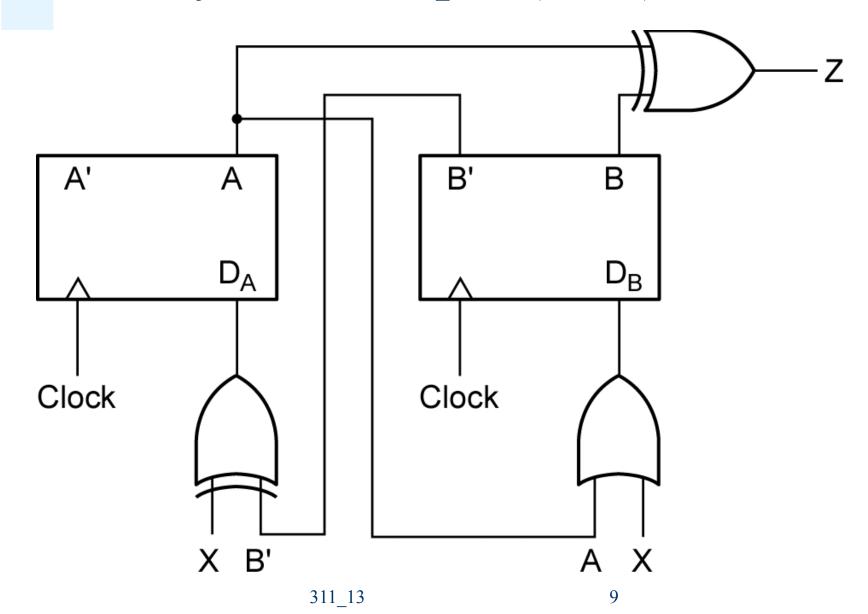


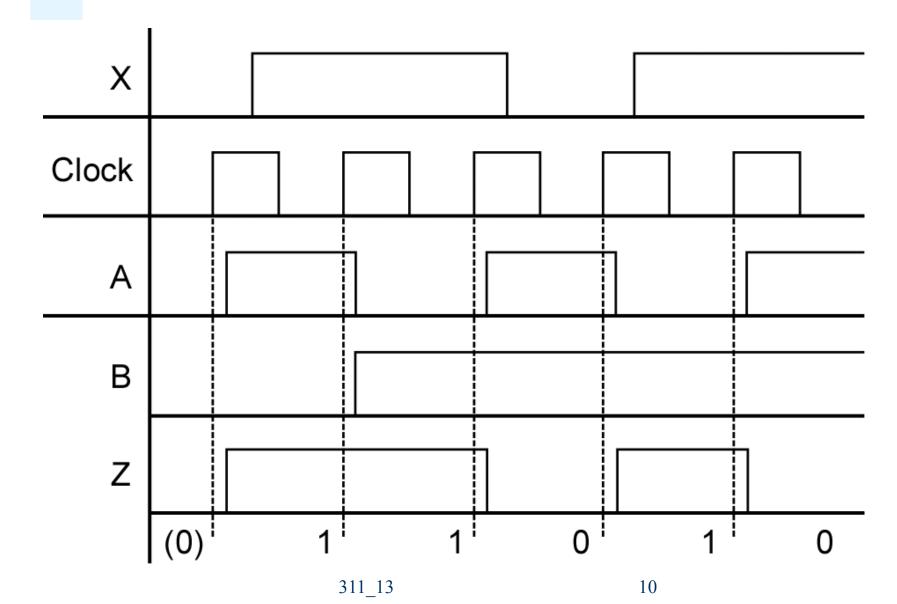




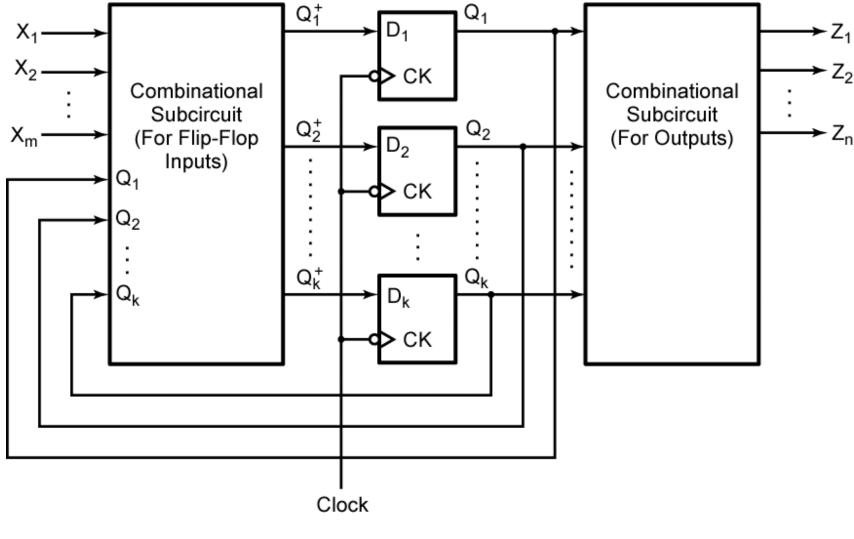


#### Analysis Example (cont)





#### Sequential Circuits



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#### Summary

- Sequential Analysis
  - State Tables
  - State Graphs
  - Timing Diagrams

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