individual computers are networks

individual computers are (kinda) networks of...
processors
memories

I/O devices

so what topology (layout) do those networks have?

the "bus" (1) **USB** core controller graphics memory core core controller controller keyboard mouse memory memory memory

example: 80386 signal pins

name	purpose	
CLK2	clock for bus	timing
W/R#	write or read?	
D/C#	data or control?	metadata
M/IO#	memory or I/O?	
INTR	interrupt request	
	other metadata signals	
BE0#-BE3#	(4) byte enable	address
A2-A31	(30) address bits	
DO-D31	(32) data signals	data

example: AMD EPYC (1 socket)

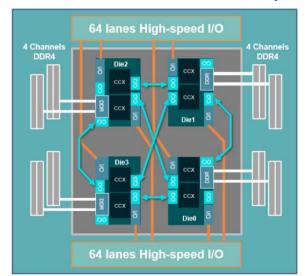


Fig. 21. Single-socket AMD EPYCTM system (SP3).

Figure from Burd et al,

"'Zepllin': An SoC for Multichip Architectures" (IEEE JSSC Vol 54, No 1)

example: Intel Skylake-SP

