# last time (1)

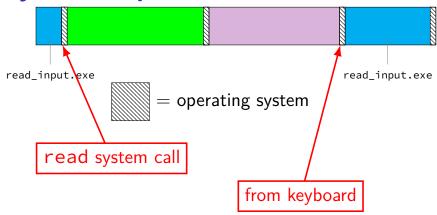
exceptions reviewed

```
exceptions: way hardware runs OS
```

```
lots of things trigger hardware to do this program request (system call), I/O device, problematic event in program (bad memory access, etc.)
```

signal handling/safety/blocking

## keyboard input timeline



# exception patterns with I/O(1)

### input — available now:

exception: device says "I have input now"

handler: OS stores input for later

exception (syscall): program says "I want to read input"

handler: OS returns that input

#### input — not available now:

exception (syscall): program says "I want to read input"

handler: OS runs other things (context switch)

exception: device says "I have input now"

handler: OS retrieves input

handler: (possibly) OS switches back to program that wanted it

# exception patterns with I/O (2)

### output — ready now:

exception (syscall): program says "I want to output this' handler: OS sends output to deive

#### output — not ready now

exception (syscall): program says "I want to output" handler: OS realizes device can't accept output yet (other things happen) exception: device says "I'm ready for output now"

handler: OS sends output requested earlier

ļ

## slightly more on quiz

accepted some alternate interpretations for whether "waiting" switches away from program imagined: ask OS to wait, it switches away but not crazy to think of checking something in a loop (especially b/c of some chat labs from CSO1 I didn't think about)

bunch of comments specifying "divide by zero" as likely I guess I need to rethink using the word "likely"?

# anonymous feedback (1)

"In regards to the quizzes, I would respectfully request that the content better reflect what is taught in the lectures, slides, and readings. I've talked to numerous classmates and there is general consensus that what is on the quizzes often requires knowledge of deeper nuances not discussed or easily found in the previously mentioned resources. If there was a way for the grading to be more lenient or for quiz corrections to be offered as well I think that would help alleviate some anxiety that many of us feel towards this course as well as foster an environment that allows for adequate learning of concepts rather than the confusion we feel at the present moment."

"The quizzes were quite challenging, as the questions seemed a bit confusing. It wasn't clear whether we were expected to find the answers in our readings or lecture slides. Despite attending every class, I found it difficult to answer all the questions."

# last time (2)

### signal handling

```
way OS calls program to handle "special" event
no handler — often default exits program
sigaction: register function to be called on signal
kill: trigger signal in specified program
```

signal unsafety — libraries not written to be interrupted+called again malloc()  $\rightarrow$  sighandler  $\rightarrow$  malloc()

#### blocking signals

```
normally: signals 'delivered' (run handler, crash program, etc.) while blocked, become 'pending' instead can check for/remove pending signals ('sigwait') or can unblock signal to let handler run
```

# last time (2)

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signal handling
```

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```

```
void handle_usr1(int num) {
   write(1, "Y", 1);
    kill(2000, SIGUSR2);
int main() {
    struct sigaction act;
    ... // initialize act
    act.sa_handler = &handle_usr1;
    sigaction(SIGUSR1, &act, NULL);
    sleep(60); // wait for pid 2000 to start
    kill(2000, SIGUSR1);
    while (1) pause();
```

#### pid 1000

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```

```
void handle_usr1(int num) {
    write(1, "X", 1);
    kill(1000, SIGUSR1):
void handle usr2(int num) {
    write(1, "Z", 1);
    kill(1000, SIGTERM);
    _exit(0);
int main() {
    struct sigaction act;
    ... // initialize act
    act.sa handler = &handle usr1;
    sigaction(SIGUSR1, &act, NULL);
    act.sa_handler = &handle_usr2;
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```

### on the lab

probably should supply pid-reading code next year adding paragraph about scanf issue in PID section of lab was not enough

issue: scanf a nubmer from "12(newline)" leaves "(newline)" so fgets works right away, sends before everyone ready

wasn't clear enough that munmap ('detach') was cleanup-code stuff

many issues re: segfaults, some options in general:

-g -fsanitize=address (get line number of bug, usually)
use debugger (automatic breakpoint at segfault+all other signals)

### timing assignment

time how long each of these take:

empty function call simple system call running another program starting signal handler sending signal and back

## a note on kill() timing

kill(THIS PROCESS, ...) — signal handler runs before kill() returns

kill(OTHER PROCESS, ...) — signal handler may/may not start before kill() returns

# anonymous feedback (2)

"The link to the Spring 2023 quizzes on the study materials tab of the course site is forbidden. Are we supposed to have access to this?"

Yes, should be working now.

"I submitted the anonymous feedback about all but 3 TAs leaving, and I was talking about the 3:30 lab."

will investigate what's going on (and think about trying to make TA help better organized in labs)

"Hi, I know you said that live examples aren't practical, but they really help me and others learn where we might make mistakes in our code. Also, as you explain them, I think we might understand the structure and how things interact in our code better. Looking at examples in reading only is pretty difficult to grasp."

if I "just" do live examples, they'd probably be scripted so few mistakes probably some sort of pacing issue

# anonymous feedback (3)

"Please go slower in lecture! I know we have limited time, but so many people are still writing/typing as you go to the next slide."

"I would appreciate it if you could not assume we know everything and explain the concepts to us. Assuming that we know a lot of this material is making us lost in lectures and assignments because as you dive into complex topics, we are falling behind on the basics. If you could be more explicit in your explanations and questions it would be highly appreciated."

"Charles, I just wanted to let you know that I don't think a single person I've talked to that's enrolled in this class feels confident about what they know so far. I think you're a very smart professor, and you're good at conveying information, but I feel like we are moving really fast for the amount of new content being introduced. I really feel like reviewing some of the content from the previous lecture would go a long way, maybe just a question or two at the beginning of class. I've been ahead on the readings since the class started and there are still points during lecture where I feel totally lost, followed by assignments that sometimes cover things I feel like weren't explained in depth in either the readings or the lecture. I really appreciate the time you give for students to ask questions, and I think you're fairly good at answering them, but I feel like a lot of us end up so confused we don't even know what questions to ask."

## opening a file?

```
open("/u/creiss/private.txt", 0_RDONLY)
say, private file on portal
```

on Linux: makes system call

kernel needs to decide if this should work or not

### how does OS decide this?

argument: needs extra metadata

what would be wrong using...

system call arguments?

where the code calling open came from?

### user IDs

most common way OSes identify what domain process belongs to:

(unspecified for now) procedure sets user IDs every process has a user ID

user ID used to decide what process is authorized to do

### **POSIX** user IDs

also some other user IDs — we'll talk later

```
uid_t geteuid(); // get current process's "effective" user ID
process's user identified with unique number
kernel typically only knows about number
effective user ID is used for all permission checks
```

### **POSIX** user IDs

```
uid_t geteuid(); // get current process's "effective" user ID
process's user identified with unique number
kernel typically only knows about number
effective user ID is used for all permission checks
```

standard programs/library maintain number to name mapping /etc/passwd on typical single-user systems network database on department machines

also some other user IDs — we'll talk later

## **POSIX** groups

```
gid_t getegid(void);
    // process's"effective" group ID
int getgroups(int size, gid_t list[]);
    // process's extra group IDs
POSIX also has group IDs
like user IDs: kernel only knows numbers
    standard library+databases for mapping to names
also process has some other group IDs — we'll talk later
```

### id

```
cr4bd@power4
: /net/zf14/cr4bd ; id
uid=858182(cr4bd) gid=21(csfaculty)
         groups=21(csfaculty),325(instructors),90027(cs4414)
id command displays uid, gid, group list
names looked up in database
    kernel doesn't know about this database
    code in the C standard library
```

## groups that don't correspond to users

example: video group for access to monitor

put process in video group when logged in directly don't do it when SSH'd in

## groups that don't correspond to users

example: video group for access to monitor

put process in video group when logged in directly don't do it when SSH'd in

...but: user can keep program running with video group in the background after logout?

### **POSIX** file permissions

POSIX files have a very restricted access control list

```
one user ID + read/write/execute bits for user "owner" — also can change permissions one group ID + read/write/execute bits for group default setting — read/write/execute
```

on directories, 'execute' means 'search' instead

## permissions encoding

```
permissions encoded as 9-bit number, can write as octal: XYZ octal divides into three 3-bit parts:
user permissions (X), group permissions (Y), other permission (Z)
each 3-bit part has a bit for 'read' (4), 'write' (2), 'execute' (1)
```

700 — user read+write+execute; group none; other none

451 — user read; group read+execute; other none

### **chmod** — exact permissions

```
chmod 700 file
chmod u=rwx,og= file
user read write execute; group/others no accesss
chmod 451 file
chmod u=r,g=rx,o= file
user read; group read/execute; others no access
```

## chmod — adjusting permissions

chmod u+rx foo
add user read and execute permissions
leave other settings unchanged
chmod o-rwx,u=rx foo
remove other read/write/execute permis

remove other read/write/execute permissions set user permissions to read/execute leave group settings unchanged

## POSIX/NTFS ACLs

more flexible access control lists

list of (user or group, read or write or execute or ...)

supported by NTFS (Windows)

a version standardized by POSIX, but usually not supported

## **POSIX ACL** syntax

```
# group students have read+execute permissions
group:students:r-x
# group faculty has read/write/execute permissions
group:faculty:rwx
# user mst3k has read/write/execute permissions
user:mst3k:rwx
# user tj1a has no permissions
user:tj1a:---
# POSIX acl rule:
    # user take precedence over group entries
```

#### **POSIX ACLs on command line**

```
getfacl file
setfacl -m 'user:tj1a:---' file
add line to ACL
setfacl -x 'user:tj1a' file
REMOVE line from acl
setfacl -M acl.txt file
add to acl, but read what to add from a file
setfacl -X acl.txt file
remove from acl. but read what to remove from a file
```

## authorization checking on Unix

checked on system call entry no relying on libraries, etc. to do checks

```
files (open, rename, ...) — file/directory permissions processes (kill, ...) — process UID = user UID ...
```

## keeping permissions?

which of the following would still be secure?

- A. performing authorization checks in the standard library in addition to system call handlers
- B. performing authorization checks in the standard library instead of system call handlers
- C. making the user ID a system call argument rather than storing it persistently in the OS's memory

#### superuser

```
user ID 0 is special

superuser or root

(non-Unix) or Administrator or SYSTEM or ...
```

some system calls: only work for uid 0 shutdown, mount new file systems, etc.

automatically passes all (or almost all) permission checks

#### superuser v kernel mode

superuser : OS :: kernel mode : hardware

programs running as superuser still in user mode just change in how OS acts on system calls, etc.

## how does login work?

```
somemachine login: jo
password: ******
io@somemachine$ Is
this is a program which...
checks if the password is correct, and
changes user IDs, and
runs a shell
```

## how does login work?

```
somemachine login: jo
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this is a program which...
checks if the password is correct, and
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runs a shell
```

## Unix password storage

```
typical single-user system: /etc/shadow only readable by root/superuser
```

department machines: network service

Kerberos / Active Directory:
server takes (encrypted) passwords
server gives tokens: "yes, really this user"

can cryptographically verify tokens come from server

#### aside: beyond passwords

```
/bin/login entirely user-space code
only thing special about it: when it's run
could use any criteria to decide, not just passwords
physical tokens
biometrics
...
```

## how does login work?

```
somemachine login: jo
password: ******
io@somemachine$ Is
this is a program which...
checks if the password is correct, and
changes user IDs, and
runs a shell
```

#### changing user IDs

```
int setuid(uid_t uid);
if superuser: sets effective user ID to arbitrary value
     and a "real user ID" and a "saved set-user-ID" (we'll talk later)
```

system starts in/login programs run as superuser voluntarily restrict own access before running shell, etc.

#### sudo

#### set-user-ID sudo

extra metadata bit on executables: set-user-ID

if set: exec() syscall changes effective user ID to owner's ID

sudo program: owned by root, marked set-user-ID

marking setuid: chmod u+s

## uses for setuid programs

#### mount USB stick

setuid program controls option to kernel mount syscall make sure user can't replace sensitive directories make sure user can't mess up filesystems on normal hard disks make sure user can't mount new setuid root files

control access to device — printer, monitor, etc. setuid program talks to device + decides who can

write to secure log file setuid program ensures that log is append-only for normal users

bind to a particular port number  $<1024\,$  setuid program creates socket, then becomes not root

## set-user ID programs are very hard to write

```
what if stdin, stdout, stderr start closed?
what if signals setup weirldy?
what if the PATH env. var. set to directory of malicious programs?
what if argc == 0?
what if dynamic linker env. vars are set?
what if some bug allows memory corruption?
```

#### privilege escalation

privilege escalation — vulnerabilities that allow more privileges

code execution/corruption in utilities that run with high privilege e.g. buffer overflow, command injection

login, sudo, system services, ... bugs in system call implementations

logic errors in checking delegated operations

#### program memory

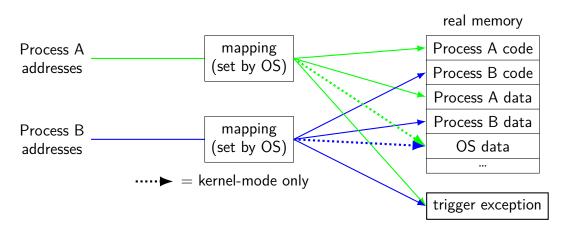
Used by OS				
Stack				
Heap / other dynamic				
Heap / other dynamic Writable data				

0xffff FFFF FFFF
0xfffF 8000 0000 0000
0x7F...

0x0000 0000 0040 0000

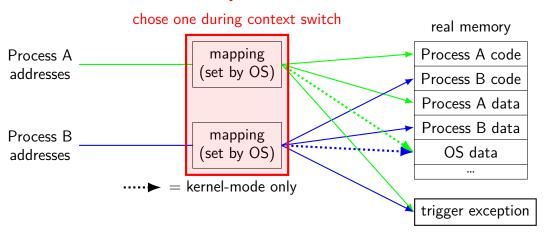
#### address spaces

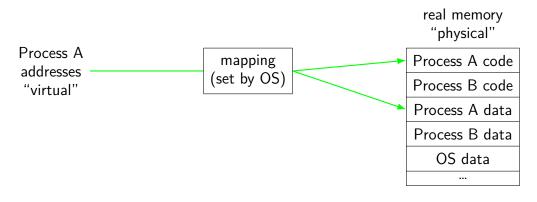
#### illuision of dedicated memory

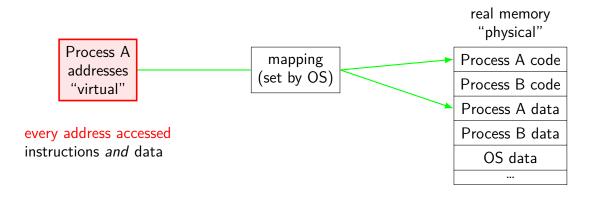


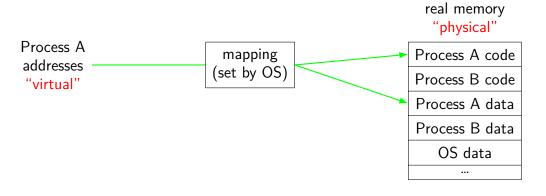
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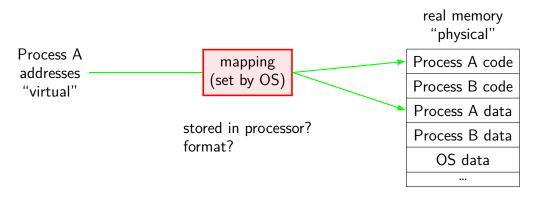


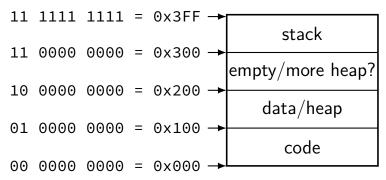


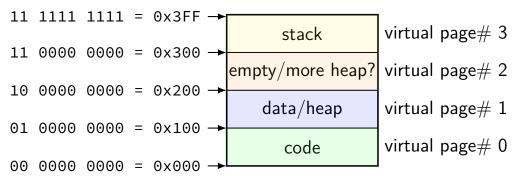


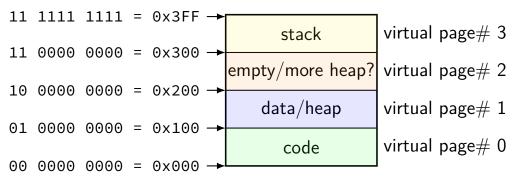


program addresses are 'virtual' real addresses are 'physical' can be different sizes!

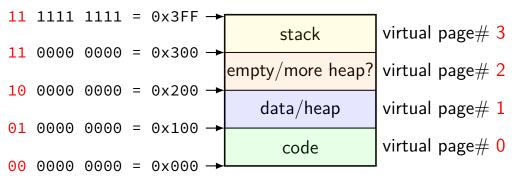




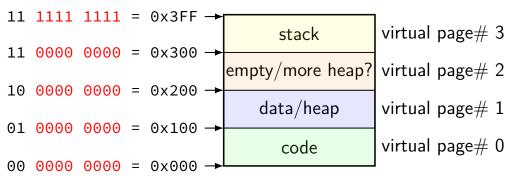




divide memory into pages  $(2^8$  bytes in this case) "virtual" = addresses the program sees



page number is upper bits of address (because page size is power of two)



rest of address is called page offset

## toy physical memory

## program memory virtual addresses

11	0000	0000	to
11	1111	1111	
10	0000	0000	to
10	1111	1111	
01	0000	0000	to
01	1111	1111	
00	0000	0000	to
00	1111	1111	

# real memory physical addresses

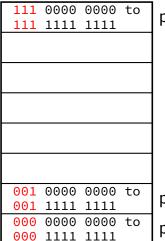
, ,			
111	0000	0000	to
111	1111	1111	
001	0000	0000	to
001	1111	1111	
000	0000	0000	to
000	1111	1111	

## toy physical memory

## program memory

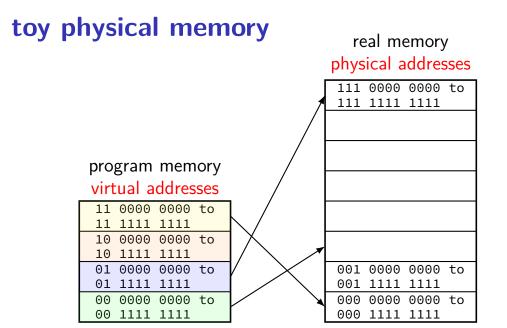
V c	.uui u	aa. cs	505
11	0000	0000	to
11	1111	1111	
10	0000	0000	to
10	1111	1111	
01	0000	0000	to
01	1111	1111	
00	0000	0000	to
00	1111	1111	

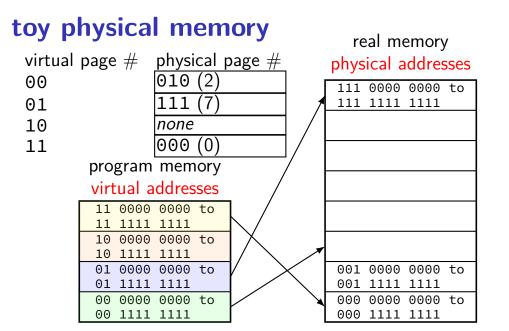
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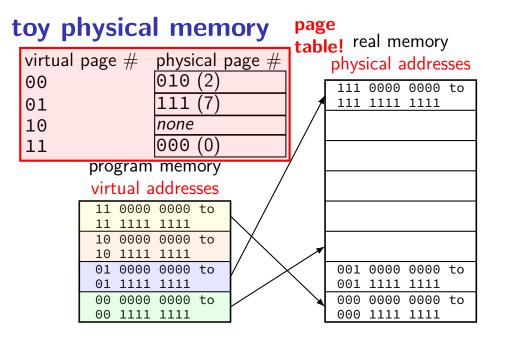


physical page 7

physical page 1 physical page 0



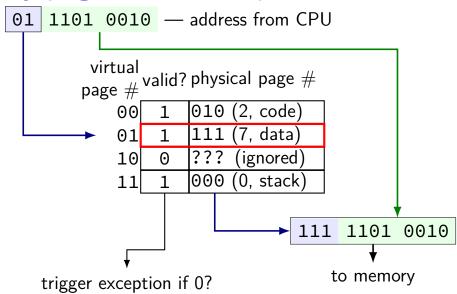




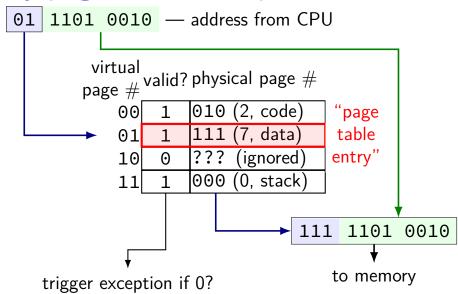
## toy page table lookup

```
virtual page # valid? physical page # 00 1 010 (2, code) 01 1 111 (7, data) 10 0 ??? (ignored) 11 1 000 (0, stack)
```

# toy page table lookup

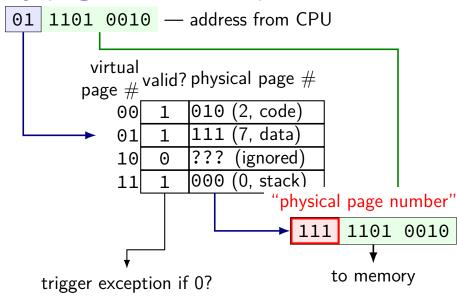


## toy page table lookup

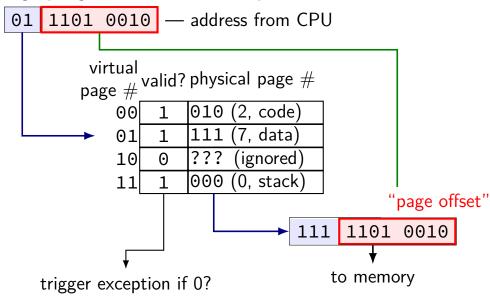


#### t "virtual page number" | ookup 1101 0010 — address from CPU virtual page # valid? physical page #010 (2, code) 00 (7, data) 01 10 (ignored) 000 (0, stack) 11 1101 0010 to memory trigger exception if 0?

# toy page table lookup



#### toy pag "page offset" ookup



#### on virtual address sizes

virtual address size = size of pointer?

often, but — sometimes part of pointer not used

example: typical x86-64 only use 48 bits rest of bits have fixed value

virtual address size is amount used for mapping

#### address space sizes

amount of stuff that can be addressed = address space size based on number of unique addresses

e.g. 32-bit virtual address =  $2^{32}$  byte virtual address space

e.g. 20-bit physical addresss =  $2^{20}$  byte physical address space

#### address space sizes

- amount of stuff that can be addressed = address space size based on number of unique addresses
- e.g. 32-bit virtual address =  $2^{32}$  byte virtual address space
- e.g. 20-bit physical addresss =  $2^{20}$  byte physical address space
- what if my machine has 3GB of memory (not power of two)?

  not all addresses in physical address space are useful
  most common situation (since CPUs support having a lot of memory)

## exercise: page counting

suppose 32-bit virtual (program) addresses

and each page is 4096 bytes ( $2^{12}$  bytes)

how many virtual pages?

## exercise: page counting

suppose 32-bit virtual (program) addresses

and each page is 4096 bytes ( $2^{12}$  bytes)

how many virtual pages?

#### exercise: page table size

```
suppose 32-bit virtual (program) addresses suppose 30-bit physical (hardware) addresses each page is 4096 bytes (2^{12} bytes) pgae table entries have physical page \#, valid bit, bit
```

how big is the page table (if laid out like ones we've seen)?

#### exercise: page table size

```
suppose 32-bit virtual (program) addresses suppose 30-bit physical (hardware) addresses each page is 4096 bytes (2^{12} bytes) pgae table entries have physical page \#, valid bit, bit
```

how big is the page table (if laid out like ones we've seen)?

issue: where can we store that?

#### exercise: address splitting

and each page is 4096 bytes ( $2^{12}$  bytes)

split the address 0x12345678 into page number and page offset:

#### exercise: address splitting

and each page is 4096 bytes ( $2^{12}$  bytes)

split the address 0x12345678 into page number and page offset:

where can processor store megabytes of page tables? in memory

page table entry layout (chosen by processor)

valid (bit 15) physical page # (bits 4–14) other bits and/or unused (bit 0-3)

where can processor store megabytes of page tables? in memory

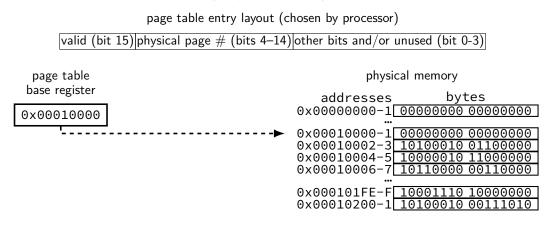
page table entry layout (chosen by processor)

valid (bit 15)|physical page # (bits 4–14)|other bits and/or unused (bit 0-3)

page table base register

0x00010000

where can processor store megabytes of page tables? in memory



where can processor store megabytes of page tables? in memory

page table entry layout (chosen by processor) valid (bit 15) physical page # (bits 4–14) other bits and/or unused (bit 0-3) physical memory page table base register addresses bytes 0x00000000-1 00000000 00000000 0x00010000  $0 \times 00010002 - 3 10100010$  $0 \times 00010004 - 5\Gamma$ 0x00010006-7 0110000 0x000101FE-F 10001110 0x00010200-1 10100010 001

where can processor store megabytes of page tables? in memory

page table entry layout (chosen by processor) valid (bit 15) physical page # (bits 4–14) other bits and/or unused (bit 0-3) physical memory page table base register addresses bytes 0x00000000-1 00000000 00000000 0x00010000 0x00010000-1 00000000  $0 \times 00010002 - 3 10100010$  $0 \times 00010004 - 5\Gamma$ 0x00010006-7 0x000101FE-F 10001110 0x00010200-1 10100010 0011101

where can processor store megabytes of page tables? in memory

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where can processor store megabytes of page tables? in memory

valid (bit 15) physical page # (bits 4–14) other bits and/or unused (bit 0-3) page table physical memory base register addresses bytes 0x0000000-1 00000000 00000000 0x00010000 0x00010000-1 00000000 00000000  $0 \times 00010002 - 3$ page table (logically) 0x00010004-5 10000010 0x00010006-7 10110000 00110000 virtual page # valid? physical page # 0000 0000 0000 0000 0x000101FE-F 10001110 0000 0001 0x00010200-1 10100010 00111010 0000 0010 0000 0011 0000 0011 1111 1111 00 1110 1000

page table entry layout (chosen by processor)

where can processor store megabytes of page tables? in memory

valid (bit 15) physical page # (bits 4–14) other bits and/or unused (bit 0-3) page table physical memory base register addresses bytes 0x0000000-1 00000000 00000000 0x00010000 0x00010000-1 00000000 00000000 0x00010002-3 page table (logically) 0x00010004-5 0000010 0x00010006-7 0110000 00110000 virtual page # valid? physical page # 0000 0000 0000 0000 0x000101FE-F 10001110 0000 0001 0x00010200-1 10100010 00111010 0000 0010 0000 0011 0000 0011 1111 1111 00 1110 1000

page table entry layout (chosen by processor)

where can processor store megabytes of page tables? in memory

page table entry layout (chosen by processor) valid (bit 15) physical page # (bits 4–14) other bits and/or unused (bit 0-3) page table physical memory base register addresses bytes 0x0000000-1 00000000 00000000 0x00010000 0x00010000-1 00000000  $0 \times 00010002 - 3$ page table (logically) 0x00010004-5 0x00010006-7 virtual page # valid? physical page # 0000 0000  $0 \times 000101 FE - F 10001$ 0000 0001 0x00010200-1 10100010 001 0000 0010 0000 0011 0000 001 1111 1111 1110 1000

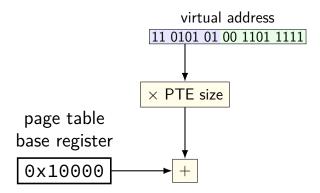
where can processor store megabytes of page tables? in memory

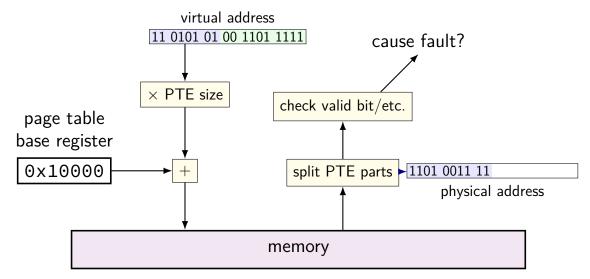
valid (bit 15) physical page # (bits 4–14) other bits and/or unused (bit 0-3) page table physical memory base register addresses bytes 0x00000000-1 00000000 00000000 0x00010000 0x00010000-1 00000000 000000000  $0 \times 00010002 - 3$ page table (logically) 0x00010004-5 10000010 0x00010006-7 10110000 001 virtual page # valid? physical page # 0000 0000 0000 0000 0x000101FE-F 10001110 0000 0001 0x00010200-1 10100010 00111010 0000 0010 0000 0011 0000 0011 1111 1111 00 1110 1000

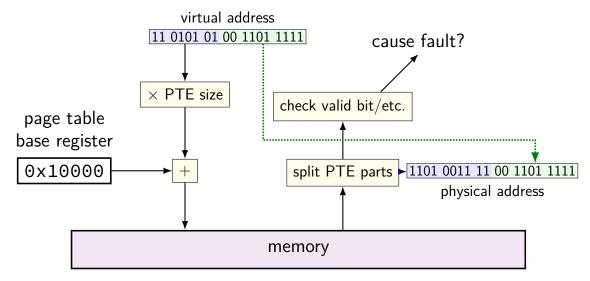
page table entry layout (chosen by processor)

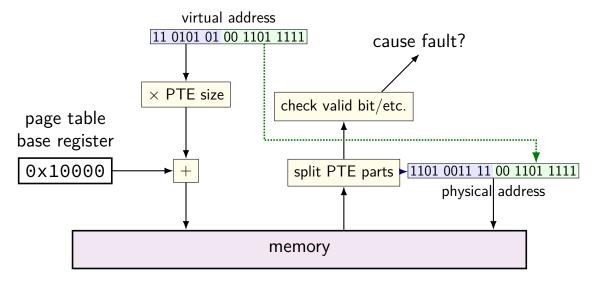
virtual address

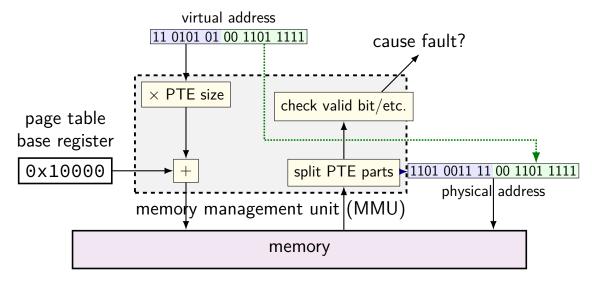
11 0101 01 00 1101 1111

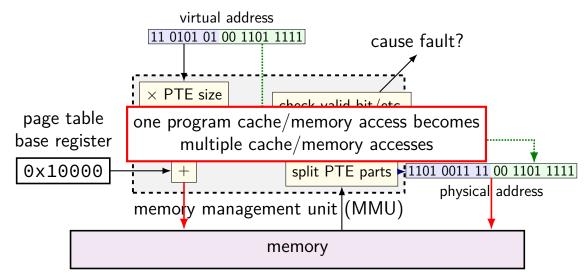


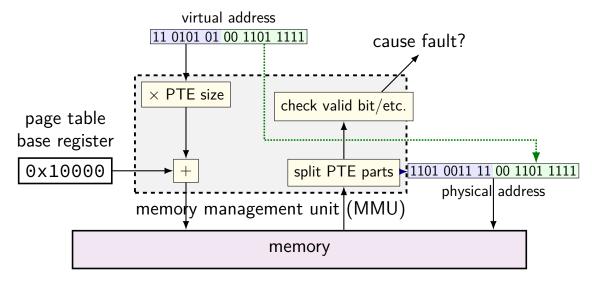












## exercise setup

5-bit virtual addresses, 6-bit physical addresses, 8-byte pages

virtual	valid?	physical
page #	valiu!	page #
00	1	010
01	1	111
10	0	000
11	1	000

physical addresses	byt	es		
0x00-3				
0x04-7	44	55	66	77
0x08-B	88	99	AΑ	ВВ
0x0C-F	C	DD	EE	FF
0x10-3	1A	2A	ЗА	4A
0x14-7	1В	2B	3B	4B
0x18-B				
0x1C-F	1C	2C	3C	4C

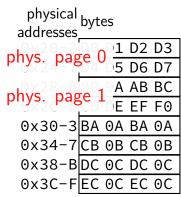
physical addresses	byt	es		
0x20-3	D0	D1	D2	D3
0x24-7				
0x28-B	89	9A	ΑB	ВС
0x2C-F	CD	DE	EF	F0
0x30-3	ВА	0A	ВА	0A
0x34-7	СВ	0B	СВ	0B
0x38-B	DC	0C	DC	0C
0x3C-F	EC	0C	EC	0C

#### exercise setup

5-bit virtual addresses, 6-bit physical addresses, 8-byte pages

virtual	valid?	physical
page #	valid!	page #
00	1	010
01	1	111
10	0	000
11	1	000

physical addresses	byt	es		
0x00-3			22	33
0x04-7	44	55	66	77
0x08-B	88	99	ΑА	ВВ
0x0C-F				
0x10-3	1A	2A	ЗА	4A
0x14-7	1В	2B	3B	4B
0x18-B	1C	2C	3C	4C
0x1C-F	1C	2C	3C	4C



#### exercise

5-bit virtual addresses, 6-bit physical addresses, 8-byte pages

```
(virtual addresses) 0x18 = ????; 0x03 = ????; 0x0A = ????; 0x13 = ???
```

virtual	valid?	physical
page #	valiu!	page #
00	1	010
01	1	111
10	0	000
11	1	000

physical addresses	bytes	
0x00-3	00 11 22	2 33
0x04-7	44 55 60	6 77
0x08-B	88 99 A	A BB
0x0C-F	CC DD EI	E FF
0x10-3	1A 2A 3/	A 4A
0x14-7	1B 2B 3I	B 4B
0x18-B	1C 2C 30	C 4C
0×1C-F	1C 2C 30	C 4C

physical addresses	byt	es		
0x20-3	D0	D1		
0x24-7	D4	D5	D6	D7
0x28-B	89	9A	ΑB	ВС
0x2C-F	CD	DE	EF	F0
0x30-3				
0x34-7				
0x38-B				
0x3C-F	EC	0C	EC	0C

#### exercise

5-bit virtual addresses, 6-bit physical addresses, 8-byte pages

```
(virtual addresses) 0x18 = ; 0x03 = ???; 0x0A = ???; 0x13 = ???
```

```
virtual valid? physical page # 00 1 010 000 11 111 10 0 000 11 1 1000
```

physical addresses	byt	es		
0x00-3	00	11	22	33
0x04-7	44	55	66	77
0x08-B	88	99	AA	ВВ
0x0C-F	CC	DD	EE	FF
0x10-3	1A	2A	ЗА	4A
0x14-7	1B	2B	3B	4B
0x18-B	1C	2C	3C	4C
0×1C-F	10	20	30	10

physical addresses	byt	es		
0x20-3	D0	D1		
0x24-7	D4	D5	D6	D7
0x28-B	89	9A	ΑB	ВС
0x2C-F				
0x30-3	ВА	0A	ВА	0A
0x34-7				
0x38-B	DC	0C	DC	0C
0x3C-F	EC	0C	EC	0C

#### exercise

5-bit virtual addresses, 6-bit physical addresses, 8-byte pages

```
(virtual addresses) 0x18 = ; 0x03 = ; 0x0A = ???; 0x13 = ??? page table
```

```
virtual page # physical page # page # 00 1 010 011 111 10 0 000 11 1 1 1000
```

physical	byt	es		
addresses				
0x00-3	00	11	22	33
0x04-7	44	55	66	77
0x08-B	88	99	AA	ВВ
0x0C-F	CC	DD	EE	FF
0x10-3	1A	2A	ЗА	4A
0x14-7	1B	2B	3B	4B
0x18-B	1C	2C	3C	4C
0x1C-F	1C	2C	3C	4C

byt	es		
D0	D1		
89	9A	ΑB	ВС
ВА	0A	ВА	0A
DC	0C	DC	0C
EC	0 <sub>C</sub>	EC	0C
	D0 D4 89 CD BA CB	D4 D5 89 9A CD DE BA 0A CB 0B DC 0C	bytes  D0 D1 D2  D4 D5 D6  89 9A AB  CD DE EF  BA 0A BA  CB 0B CB  DC 0C DC  EC 0C EC

#### exercise

5-bit virtual addresses, 6-bit physical addresses, 8-byte pages

```
(virtual addresses) 0x18 = ; 0x03 = ; 0x0A = ; 0x13 = ??? page table
```

```
virtual valid? physical page # 00 1 010 000 11 111 10 0 000 11 1 1000
```

physical addresses	byt	es		
0x00-3			22	33
0x04-7	44	55	66	77
0x08-B	88	99	AA	ВВ
0x0C-F	CC	DD	EE	FF
0x10-3	1A	2A	ЗА	4A
0x14-7	1B	2B	3B	4B
0x18-B	1C	2C	3C	4C
0×1C-F	10	20	30	<u>4</u> C

physical addresses	byt	es		
0x20-3	D0	D1		
0x24-7	D4	D5	D6	D7
0x28-B	89	9A	ΑB	ВС
0x2C-F	CD	DE	EF	F0
0x30-3	ВА	0A	ВА	0A
0x34-7	СВ	0B	СВ	0B
0x38-B	DC	0C	DC	0C
0x3C-F	EC	0C	EC	0C

#### exercise

5-bit virtual addresses, 6-bit physical addresses, 8-byte pages

```
(virtual addresses) 0x18 = ; 0x03 = ; 0x0A = ; 0x13 = page table
```

virtual page # valid? physical page # 00 1 010 011 111 10 0 000 11 1 1 1000

physical addresses	byt	es		
addresses				
0x00-3				
0x04-7	44	55	66	77
0x08-B	88	99	AA	ВВ
0x0C-F	CC	DD	EE	FF
0x10-3	1A	2A	ЗА	4A
0x14-7				
0x18-B	1C	2C	3C	4C
0x1C-F	1C	2C	3C	4C

physical bytes addresses 0x20-3 D0 D1 D2 D3 0x24-7 D4 D5 D6 D7 0x28-B|89 9A AB BC 0x2C-FCD DE EF F0 0x30-3|BA 0A BA 0A 0x34-7 CB 0B CB 0B 0x38-BDC 0C DC 0C 0x3C-FEC 0C EC 0C

physical addresses	byte	es			physical addresses	byt	es		
0x00-3			22	33	0x20-3			D2	D3
0x04-7	44	55	66	77	0x24-7	F4	F5	F6	F7
0x08-B	88	99	AΑ	ВВ	0x28-B	89	9A	ΑB	ВС
0x0C-F	CC	DD	EE	FF	0x2C-F	CD	DE	EF	F0
0x10-3	1A	2A	ЗА	4A	0x30-3	ВА	0Α	ВА	0Α
0x14-7	1B	2B	3B	4B	0x34-7	СВ	0B	СВ	0B
0x18-B	1C	2C	3C	4C	0x38-B	DC	0C	DC	ОС
0x1C-F	1C	2C	3C	4C	0x3C-F	EC	0C	EC	0C

6-bit virtual addresses, 6-bit physical; 8 byte pages, 1 byte PTE page tables 1 page; PTE: 3 bit PPN (MSB), 1 valid bit, 4 other; page table base register 0x20; translate virtual address 0x31

physical bytes addresses 0x00-3|00 11 22 33 0x04-7|44 55 66 77 0x08-B|88 99 AA BB 0x0C-FICC DD EE FF 0x10-3|1A 2A 3A 4A 0x14-7|1B 2B 3B 4B 0x18-B|1C 2C 3C 4C 0x1C-F|1C 2C 3C 4C

```
physical bytes
addresses
0x20-3|D0 D1 D2 D3
0x24-7|F4 F5 F6 F7
0x28-B|89 9A AB BC
0x2C-FCD DE EF F0
0x30-3|BA 0A BA 0A
0x34-7|CB 0B CB 0B
0x38-BDC 0C DC 0C
0x3C-FEC 0C EC 0C
```

```
0x31 = 11 0001
PTE \ addr:
0x20 + 6 \times 1 = 0x26
PTE \ value:
0xF6 = 1111 0110
PPN 111, valid 1
M[111 001] = M[0x39]
\rightarrow 0x0C
```

6-bit virtual addresses, 6-bit physical; 8 byte pages, 1 byte PTE page tables 1 page; PTE: 3 bit PPN (MSB), 1 valid bit, 4 other;

page table base register 0x20; translate virtual address 0x31

```
physical bytes
                       physical bytes
                                           0x31 = 11 0001
addresses
                      addresses
                                           PTE addr:
0x00-3|00 11 22 33
                      0x20-3|D0 D1 D2 D3
                                           0x20 + 6 \times 1 = 0x26
0x04-7|44 55 66 77
                      0x24-7|F4 F5 F6 F7
0x08-B|88 99 AA BB
                      0x28-B|89 9A AB BC
                                          PTE value:
                      0x2C-FCD DE EF F0
0x0C-FICC DD EE FF
                                           0xF6 = 1111 0110
0x10-3|1A 2A 3A 4A
                      0x30-3|BA 0A BA 0A
                                           PPN 111, valid 1
0x14-7|1B 2B 3B 4B
                      0x34-7|CB 0B CB 0B
                                           M[111 \ 001] = M[0x39]
                      0x38-BDC 0C DC 0C
0x18-Bl1C 2C 3C 4C
                                           \rightarrow 0x0C
0x1C-F|1C 2C 3C 4C
                      0x3C-FEC 0C EC 0C
```

6-bit virtual addresses, 6-bit physical; 8 byte pages, 1 byte PTE page tables 1 page; PTE: 3 bit PPN (MSB), 1 valid bit, 4 other; page table base register 0x20; translate virtual address 0x31

physical bytes addresses 0x00-3|00 11 22 33 0x04-7|44 55 66 77 0x08-B|88 99 AA BB 0x0C-FICC DD EE FF 0x10-3|1A 2A 3A 4A 0x14-7|1B 2B 3B 4B 0x18-Bl1C 2C 3C 4C 0x1C-F|1C 2C 3C 4C

physical bytes addresses 0x20-3|D0 D1 D2 D3 0x24-7|F4 F5 F6 F7 0x28-B|89 9A AB BC 0x2C-FCD DE EF F0 0x30-3|BA 0A BA 0A 0x34-7|CB 0B CB 0B 0x38-BDC 0C DC 0C 0x3C-FEC 0C EC 0C

0x31 = 11 0001  $PTE \ addr$ :  $0x20 + 6 \times 1 = 0x26$   $PTE \ value$ : 0xF6 = 1111 0110  $PPN 111, \ valid 1$  M[111 001] = M[0x39]  $\rightarrow 0x0C$ 

physical addresses	byte	es			physical bytes addresses
0x00-3			22	33	
0x04-7	44	55	66	77	0x24-7 F4 F5 F6 F7
0x08-B	88	99	AΑ	ВВ	0x28-B89 9A AB BC
0x0C-F	CC	DD	EE	FF	0x2C-FCD DE EF F0
0x10-3	1A	2A	3A	4A	0x30-3 BA 0A BA 0A
0x14-7	1B	2B	3B	4B	0x34-7 CB 0B CB 0B
0x18-B	1C	2C	3C	4C	0x38-BDC 0C DC 0C
0x1C-F	1C	2C	3C	4C	0x3C-FEC 0C EC 0C

physical addresses	byt	es			
					,
0x00-3	00	11	22	33	
0x04-7	44	55	66	77	
0x08-B	88	99	AΑ	ВВ	
0x0C-F	CC	DD	EE	FF	
0x10-3					
0x14-7	1В	2B	3B	4B	
0x18-B	1C	2C	3C	4C	
0x1C-F	1C	2C	3C	4C	

```
physical bytes
addresses
0x20-3|D0 D1 D2 D3
0x24-7|F4 F5 F6 F7
0x28-Bl89 9A AB BC
0x2C-FCD DE EF F0
0x30-3|BA 0A BA 0A
0x34-7 CB 0B CB 0B
0x38-BDC 0C DC 0C
0x3C-F|EC 0C EC 0C
```

```
0x12 = 01 0010
PTE \ addr:
0x20 + 2 \times 1 = 0x22
PTE \ value:
0xD2 = 1101 0010
PPN 110, valid 1
M[110 010] = M[0x32]
\rightarrow 0xBA
```

physical addresses	bvt	es			
					, ;
0x00-3					
0x04-7	44	55	66	77	
0x08-B	88	99	AΑ	ВВ	
0x0C-F	CC	DD	EE	FF	
0x10-3	1A	2A	3A	4A	
0x14-7	1В	2B	3B	4B	
0x18-B	1C	2C	3C	4C	
0x1C-F	1C	2C	3C	4C	

```
physical bytes
addresses
0x20-3|D0 D1 D2 D3
0x24-7|F4 F5 F6 F7
0x28-Bl89 9A AB BC
0x2C-FCD DE EF F0
0x30-3|BA 0A BA 0A
0x34-7|CB 0B CB 0B
0x38-BDC 0C DC 0C
0x3C-F|EC 0C EC 0C
```

```
0x12 = 01 0010

PTE addr:
0x20 + 2 \times 1 = 0x22

PTE value:
0xD2 = 1101 0010

PPN 110, valid 1

M[110 010] = M[0x32]

\rightarrow 0xBA
```

physical bytes addresses 0x00-3 00 11 22 33 0x04-7 44 55 66 77 0x08-B 88 99 AA BB 0x0C-F CC DD EE FF 0x10-3 1A 2A 3A 4A 0x14-7 1B 2B 3B 4B 0x18-B 1C 2C 3C 4C 0x1C-F 1C 2C 3C 4C	physical	hvt	<b>P</b> S			
0x04-7 44 55 66 77 0x08-B 88 99 AA BB 0x0C-F CC DD EE FF 0x10-3 1A 2A 3A 4A 0x14-7 1B 2B 3B 4B 0x18-B 1C 2C 3C 4C						, 6
0x08-B 88 99 AA BB 0x0C-F CC DD EE FF 0x10-3 1A 2A 3A 4A 0x14-7 1B 2B 3B 4B 0x18-B 1C 2C 3C 4C						
0x0C-F CC DD EE FF 0x10-3 1A 2A 3A 4A 0x14-7 1B 2B 3B 4B 0x18-B 1C 2C 3C 4C	0x04-7	44	55	66	77	
0x10-3 1A 2A 3A 4A 0x14-7 1B 2B 3B 4B 0x18-B 1C 2C 3C 4C	0x08-B	88	99	AΑ	ВВ	
0x14-7 1B 2B 3B 4B 0x18-B 1C 2C 3C 4C	0x0C-F	CC	DD	EE	FF	
0x18-B 1C 2C 3C 4C						
	0x14-7	1В	2B	3B	4B	
0x1C-F1C 2C 3C 4C	0x18-B	1C	2C	3C	4C	
	0x1C-F	1C	2C	3C	4C	

```
physical bytes
addresses
0x20-3|D0 D1 D2 D3
0x24-7|F4 F5 F6 F7
0x28-Bl89 9A AB BC
0x2C-FCD DE EF F0
0x30-3BA 0A BA 0A
0x34-7|CB 0B CB 0B
0x38-BDC 0C DC 0C
0x3C-F|EC 0C EC 0C
```

$$0x12 = 01 0010$$
 $PTE \ addr:$ 
 $0x20 + 2 \times 1 = 0x22$ 
 $PTE \ value:$ 
 $0xD2 = 1101 0010$ 
 $PPN 110, valid 1$ 
 $M[110 010] = M[0x32]$ 
 $\rightarrow 0xBA$ 

my desktop: 39-bit physical addresses; 48-bit virtual addresses

4096 byte pages

my desktop: 39-bit physical addresses; 48-bit virtual addresses

4096 byte pages

top 16 bits of 64-bit addresses not used for translation

my desktop: 39-bit physical addresses; 48-bit virtual addresses

4096 byte pages

exercise: how many page table entries? (assuming page table like shown before)

exercise: how large are physical page numbers?

my desktop: 39-bit physical addresses; 48-bit virtual addresses

4096 byte pages

exercise: how many page table entries? (assuming page table like shown before)

exercise: how large are physical page numbers?

my desktop: 39-bit physical addresses; 48-bit virtual addresses

4096 byte pages

exercise: how many page table entries? (assuming page table like shown before)

exercise: how large are physical page numbers?

page table entries are 8 bytes (room for expansion, metadata) trick: power of two size makes table lookup faster

would take up  $2^{39}$  bytes?? (512GB??)

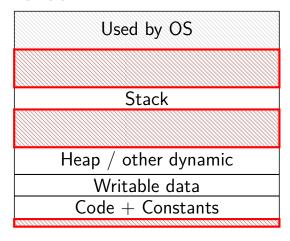
## huge page tables

huge virtual address spaces!

impossible to store PTE for every page

how can we save space?

#### holes



most pages are invalid

#### saving space

basic idea: don't store (most) invalid page table entries use a data structure other than a flat array want a map — lookup key (virtual page number), get value (PTE) options?

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```

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```
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options?
```

#### hashtable

actually used by some historical processors but never common

#### tree data structure

but not quite a search tree

#### search tree tradeoffs

lookup usually implemented in hardware

lookup should be simple

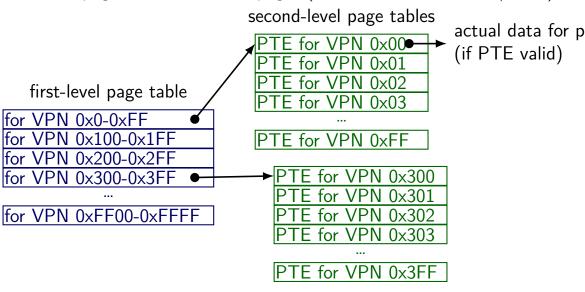
solution: lookup splits up address bits (no complex calculations)

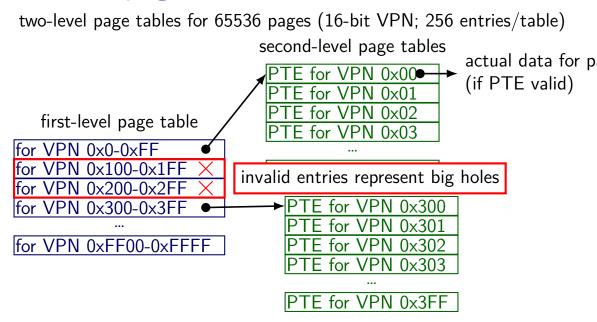
lookup should not involve many memory accesses

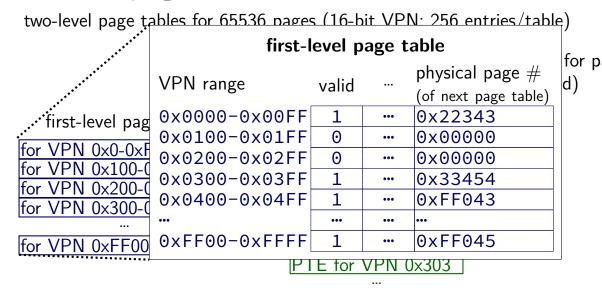
doing two memory accesses is already very slow solution: tree with many children from each node

(far from binary tree's left/right child)

two-level page tables for 65536 pages (16-bit VPN; 256 entries/table)

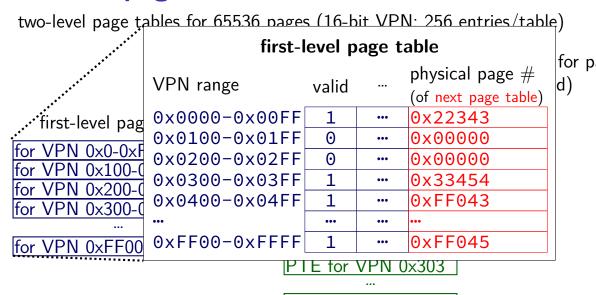






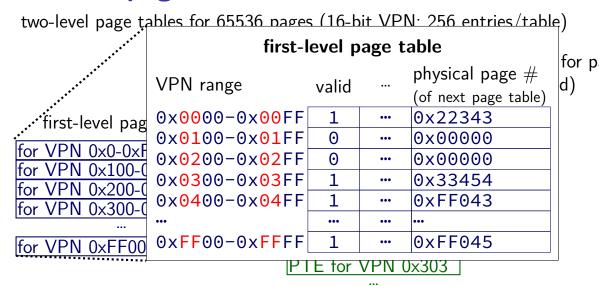
TE for VPN 0x3FF

70



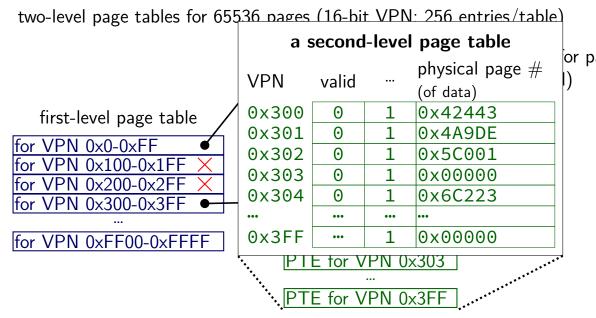
TE for VPN 0x3FF

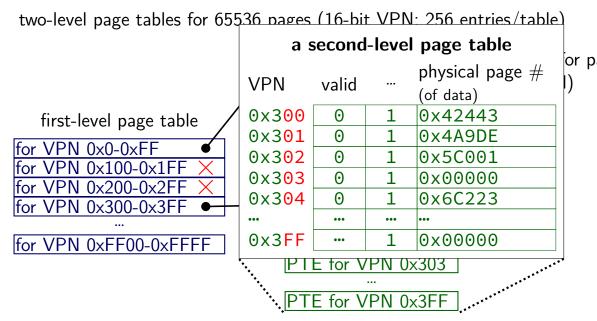
70



TE for VPN 0x3FF

70





two-level page tables for 65536 pages (16-bit VPN; 256 entries/table) second-level page tables actual data for p for VPN 0x00 (if PTE valid) first-level page table for VPN  $0 \times 0 - 0 \times FF$ tor VPN  $0 \times 100 - 0 \times 1$  FF IPTE for VPN 0xFF VPN 0x200-0x2FF for VPN 0x300-0x3FF VPN 0x300 for VPN 0xFF00-0xFFFF VPN 0x302 TE for VPN 0x303

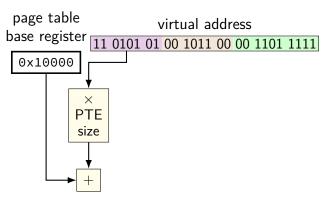
for VPN 0x3FF

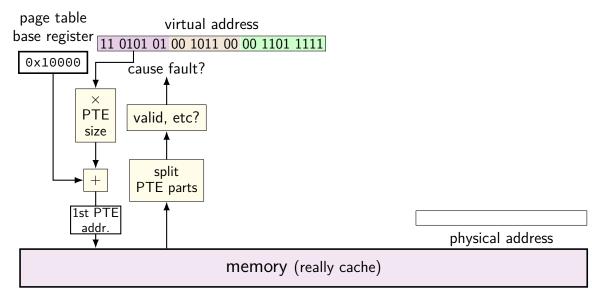
virtual address

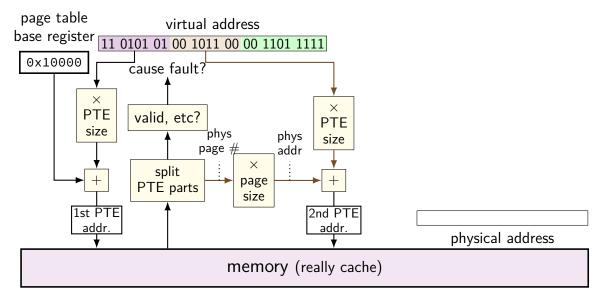
11 0101 01 00 1011 00 00 1101 1111

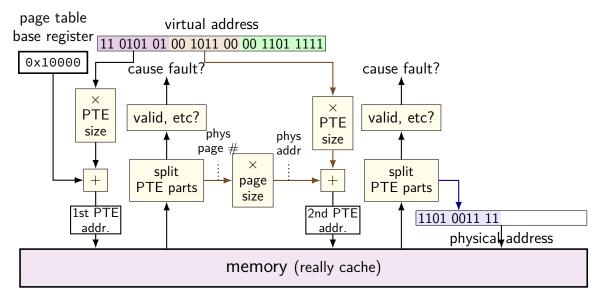
VPN — split into two parts (one per level)

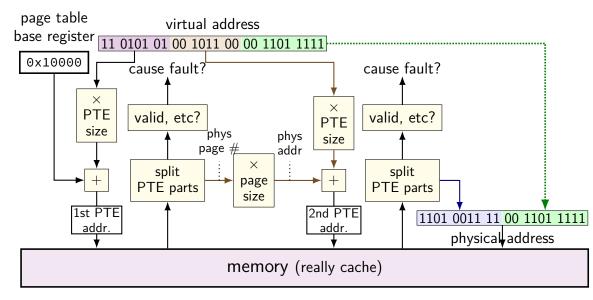
this example: parts equal sized — common, but not required

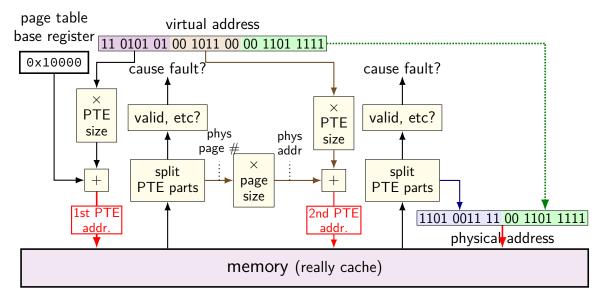


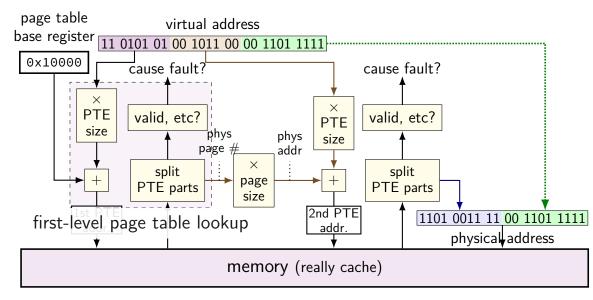


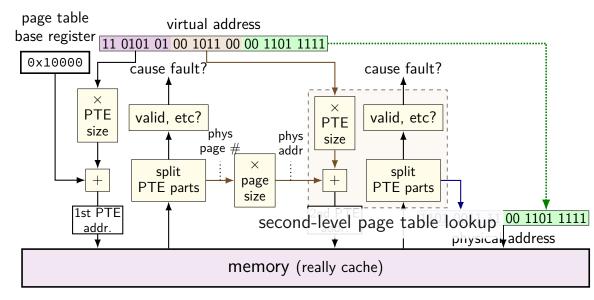


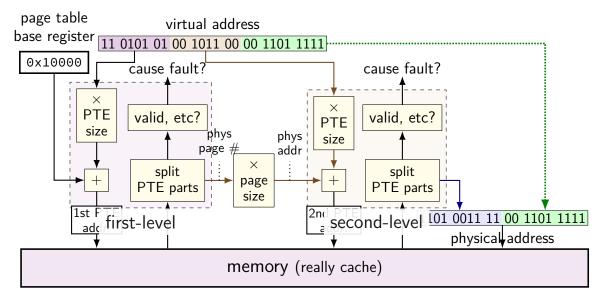


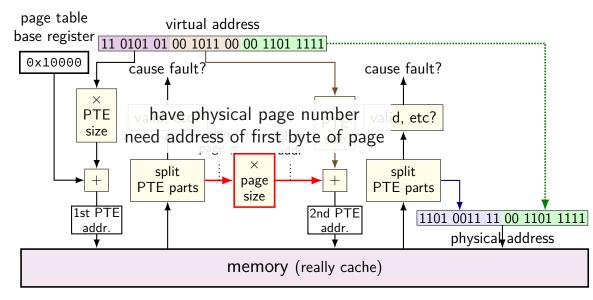


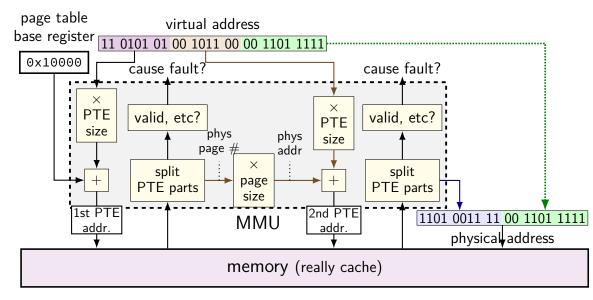




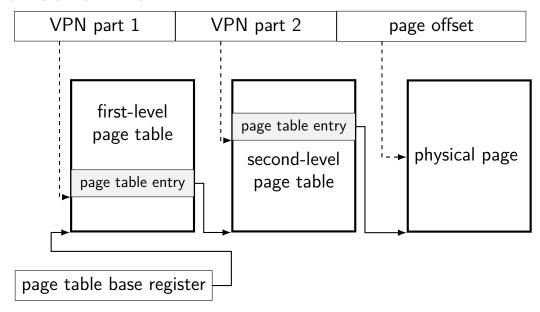








### another view



### multi-level page tables

VPN split into pieces for each level of page table

top levels: page table entries point to next page table usually using physical page number of next page table

bottom level: page table entry points to destination page

validity checks at each level

## x86-64 page table splitting

48-bit virtual address

12-bit page offset (4KB pages)

36-bit virtual page number, split into four 9-bit parts

page tables at each level:  $2^9$  entries, 8 bytes/entry deliberate choice: each page table is one page

### note on VPN splitting

indexes used for lookup parts of the virtual page number (there are not multiple VPNs)

#### emacs.exe

Emacs (run by user mst3k)

Used by OS
Stack
Heap / other dynamic
Writable data
${\sf emacs.exe} \; ({\sf Code} + {\sf Constants})$

#### emacs.exe

Emacs (run by user mst3k)

Used by OS Stack Heap / other dynamic Writable data emacs.exe (Code + Constants)

OS's memory

part of context switch is changing the page table

extra privileged instructions

part of context switch is changing the page table

extra privileged instructions

where in memory is the code that does this switching?

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extra privileged instructions

where in memory is the code that does this switching? probably have a page table entry pointing to it hopefully marked kernel-mode-only

part of context switch is changing the page table extra privileged instructions

where in memory is the code that does this switching? probably have a page table entry pointing to it hopefully marked kernel-mode-only

code better not be modified by user program otherwise: uncontrolled way to "escape" user mode

# emacs (two copies)

Emacs (run by user mst3k)

Used by OS	
Stack	
Heap / other dynamic	,,,,,,
Writable data	
emacs.exe (Code + Constants	5)

Emacs (run by user xyz4w)

. ,
Used by OS
Stack
Heap / other dynamic
Writable data
emacs.exe (Code $+$ Constants)

# emacs (two copies)

Emacs (run by user mst3k)	Emacs (run by user xyz4w)
Used by OS	Used by OS
Stack	Stack
Heap / other dynamic	Heap / other dynamic
Writable data	Writable data
$emacs.exe\; (Code + Constants)$	emacs.exe (Code $+$ Constants)

same data?

### two copies of program

would like to only have one copy of program

what if mst3k's emacs tries to modify its code?

would break process abstraction:

"illusion of own memory"

### permissions bits

```
page table entry will have more permissions bits can access in user mode? can read from? can write to? can execute from?
```

### checked by MMU like valid bit

page table (logically)

virtual page #	valid?	user?	write?	exec?	physical page #
0000 0000	0	0	0	0	00 0000 0000
0000 0001	1	1	1	0	10 0010 0110
0000 0010	1	1	1	0	00 0000 1100
0000 0011	1	1	0	1	11 0000 0011
•••					
1111 1111	1	0	1	0	00 1110 1000

# assignment

physical addresses	byte	es			physical addresses	byt	es		
0x00-3	00	11	22	33	0x20-3	00	91	72	13
0x04-7	44	55	66	77	0x24-7	D4	F5	36	07
0x08-B	88	99	AA	ВВ	0x28-B	89	9A	AB	ВС
0x0C-F	CC	DD	EE	FF	0x2C-F	CD	DE	EF	F0
0x10-3	1A	2A	ЗА	4A	0x30-3	ВА	0A	ВА	0A
0x14-7	1B	2B	3B	4B	0x34-7	DB	0B	DB	0B
0x18-B	1C	2C	3C	4C	0x38-B	EC	0C	EC	0C
0x1C-F	1C	2C	3C	4C	0x3C-F	FC	0C	FC	0C

physical addresses	byte	es		
0x00-3	00	11	22	33
0x04-7	44	55	66	77
0x08-B	88	99	AΑ	ВВ
0x0C-F	CC	DD	EE	FF
0x10-3	1A	2A	3A	4A
0x14-7	1В	2B	3B	4B
0x18-B	1C	2C	3C	4C
0x1C-F	1C	2C	3C	4C

physical addresses	byt	es		
0x20-3	00	91		
0x24-7	D4	F5	36	07
0x28-B	89	9A	ΑB	ВС
0x2C-F	CD	DE	EF	F0
0x30-3	ВА	0A	ВА	0A
0x34-7	DB	0B	DB	0B
0x38-B	EC	0C	EC	0C
0x3C-F	FC	0C	FC	0C

physical addresses	byte	es			phy addre	sical esses	byt	es		
0x00-3			22	33		0-3			72	13
0x04-7	44	55	66	77	0x2	4-7	D4	F5	36	07
0x08-B	88	99	AΑ	ВВ	0x2	8-B	89	9A	ΑB	ВС
0x0C-F	CC	DD	EE	FF	0x2	C-F	CD	DE	EF	F0
0x10-3	1A	2A	ЗА	4A	0x3	0-3	ВА	0A	ВА	0Α
0x14-7	1B	2B	3B	4B	0x3	4-7	DB	0B	DB	0B
0x18-B	1C	2C	3C	4C	0x3	8-B	EC	0C	EC	0C
0x1C-F	1C	2C	3C	4C	0x3	C-F	FC	0 <sub>C</sub>	FC	0C

physical addresses	byte	es			physical addresses	byt	es		
0x00-3			22	33	0x20-3			72	13
0x04-7	44	55	66	77	0x24-7	D4	F5	36	07
0x08-B	88	99	AA	ВВ	0x28-B	89	9A	ΑB	ВС
0x0C-F	CC	DD	EE	FF	0x2C-F	CD	DE	EF	F0
0x10-3	1A	2A	ЗА	4A	0x30-3	ВА	0A	ВА	0Α
0x14-7	1B	2B	3B	4B	0x34-7	DB	0B	DB	0B
0x18-B	1C	2C	3C	4C	0x38-B	EC	0C	EC	оC
0x1C-F	1C	2C	3C	4C	0x3C-F	FC	0C	FC	0C

physical addresses	byte	es			physical addresses	byt	es		
0x00-3			22	33	0x20-3			72	13
0x04-7	44	55	66	77	0x24-7	D4	F5	36	07
0x08-B	88	99	AA	ВВ	0x28-B	89	9A	ΑB	ВС
0x0C-F	CC	DD	EE	FF	0x2C-F	CD	DE	EF	F0
0x10-3	1A	2A	ЗА	4A	0x30-3	ВА	0A	ВА	0Α
0x14-7	1B	2B	3B	4B	0x34-7	DB	0B	DB	0B
0x18-B	1C	2C	3C	4C	0x38-B	EC	0C	EC	оC
0x1C-F	1C	2C	3C	4C	0x3C-F	FC	0C	FC	0C

physical addresses	byte	es			physica addresses	byt	es		
0x00-3			22	33	0x20-3			72	13
0x04-7	44	55	66	77	0x24-7	D4	F5	36	07
0x08-B	88	99	AΑ	ВВ	0x28-E	89	9A	ΑB	ВС
0x0C-F	CC	DD	ΕE	FF	0x2C-F	CD	DE	EF	F0
0x10-3	1A	2A	ЗА	4A	0x30-3	ВА	0A	ВА	0A
0x14-7	1В	2B	3B	4B	0x34-7	DB	0B	DB	0B
0x18-B	1C	2C	3C	4C	0x38-E	EC	0C	EC	0C
0x1C-F	1C	2C	3C	4C	0x3C-F	FC	0C	FC	0C

## 2-level splitting

- 9-bit virtual address
- 6-bit physical address

- 8-byte pages  $\rightarrow$  3-bit page offset (bottom bits)
- 9-bit VA: 6 bit VPN + 3 bit PO
- 6-bit PA: 3 bit PPN + 3 bit PO

- 8 entry page tables  $\rightarrow$  3-bit VPN parts
- 9-bit VA: 3 bit VPN part 1; 3 bit VPN part 2

physical addresses	byte	es			physical bytes addresses
0x00-3			22	33	
0x04-7	44	55	66	77	0x24-7D4 D5 D6 D7
0x08-B	88	99	AΑ	ВВ	0x28-B89 9A AB BC
0x0C-F	CC	DD	ΕE	FF	0x2C-FCD DE EF F0
0x10-3	1A	2A	3A	4A	0x30-3BA 0A BA 0A
0x14-7	1В	2B	3B	4B	0x34-7 DB 0B DB 0B
0x18-B	1C	2C	3C	4C	0x38-BEC 0C EC 0C
0x1C-F	1C	2C	3C	4C	0x3C-FFC 0C FC 0C

	physical bytes addresses
33	0x20-3D0 D1 D2 D3
77	0x24-7D4 D5 D6 D7
ВВ	0x28-B89 9A AB BC
FF	0x2C-FCD DE EF F0
4A	0x30-3BA 0A BA 0A
4B	0x34-7DB 0B DB 0B
4C	0x38-BEC 0C EC 0C
4C	0x3C-FFC 0C FC 0C
	77 BB FF 4A 4B 4C

9-bit virtual addresses, 6-bit physical; 8 byte pages, 1 byte PTE page tables 1 page; PTE: 3 bit PPN (MSB), 1 valid bit, 4 unused; page table base register 0x08; translate virtual address 0x0FB

physical bytes addresses	physical addresses
0x00-3 <mark>00 11 22 33</mark>	0x20-3
0x04-744 55 66 77	0x24-7
0x08-B88 99 AA BB	0x28-B
0x0C-FCC DD EE FF	0x2C-F
0x10-3 1A 2A 3A 4A	0x30-3
0x14-7 1B 2B 3B 4B	0x34-7
0x18-B1C 2C 3C 4C	0x38-B
0x1C-F1C 2C 3C 4C	0x3C-F

physical bytes addresses 0x20-3 D0 D1 D2 D3 0x24-7 D4 D5 D6 D7 0x28-B 89 9A AB BC 0x2C-F CD DE EF F0 0x30-3 BA 0A BA 0A 0x34-7 DB 0B DB 0B 0x38-B EC 0C EC 0C 0x3C-F FC 0C FC 0C

2 D3
5 D7
BC
F0
\ 0A
0 B
0C
0C
1

physical bytes addresses	physical bytes addresses
0x00-300 11 22 33	0x20-3 D0 D1 D2 D3
0x04-744 55 66 77	0x24-7D4 D5 D6 D7
0x08-B88 99 AA BB	0x28-B89 9A AB B0
0x0C-FCC DD EE FF	0x2C-FCD DE EF F
0x10-31A 2A 3A 4A	0x30-3BA 0A BA 0A
0x14-7 1B 2B 3B 4B	0x34-7 DB 0B DB 0E
0x18-B1C 2C 3C 4C	0x38-BEC 0C EC 00
0x1C-F1C 2C 3C 4C	0x3C-FFC 0C FC 00

physical addresses	byte	es				physical addresses	byt	es		
0x00-3			22	33		0x20-3	D0	D1	D2	D3
0x04-7	44	55	66	77		0x24-7	D4	D5	D6	D7
0x08-B	88	99	AΑ	ВВ		0x28-B	89	9A	ΑB	ВС
0x0C-F	CC	DD	EE	FF		0x2C-F	CD	DE	EF	F0
0x10-3	1A	2A	5A	4A		0x30-3	ВА	0Α	ВА	0Α
0x14-7	1B	2B	3B	4B		0x34-7	DB	0B	DB	0B
0x18-B	1C	2C	3C	4C		0x38-B	EC	0C	EC	0C
0x1C-F	1C	2C	3C	4C	]	0x3C-F	FC	0C	FC	0C

physical addresses	byte	es		
0x00-3	00	11	22	33
0x04-7	44	55	66	77
0x08-B	88	99	AΑ	ВВ
0x0C-F	CC	DD	EE	FF
0x10-3	1A	2A	3A	4A
0x14-7	1B	2B	3B	4B
0x18-B	1C	2C	3C	4C
0x1C-F[	1C	2C	3C	4C

physical addresses	byt	es		
0x20-3	D0	D1	D2	D3
0x24-7	D4	D5	D6	D7
0x28-B	89	9A	ΑB	ВС
0x2C-F	CD	DE	EF	F0
0x30-3	ВА	0A	ВА	0A
0x34-7	DB	0B	DB	0B
0x38-B	EC	0C	EC	0C
0x3C-F	FC	0C	FC	0C

physical addresses	byt	es		
0x00-3	00	11	22	33
0x04-7	44	55	66	77
0x08-B				
0x0C-F	CC	DD	ΕE	FF
0x10-3	1A	2A	3A	4A
0x14-7				
0x18-B				
0x1C-F	1 <u>C</u>	2C	3C	4C

physical iddresses	byt	es		
0x20-3	D0	D1	D2	D3
0x24-7	D4	D5	D6	D7
0x28-B	89	9A	ΑB	ВС
0x2C-F	CD	DE	EF	F0
0x30-3	ВА	0A	ВА	0A
0x34-7	DB	0B	DB	0B
0x38-B	EC	0C	EC	0C
0x3C-F	FC	0C	FC	0C

9-bit virtual addresses, 6-bit physical; 8 byte pages, 1 byte PTE page tables 1 page; PTE: 3 bit PPN (MSB), 1 valid bit, 4 unused page table base register 0x08; translate virtual address 0x00B

physical bytes addresses						
0x00-3	00	11	22	33		
0x04-7	44	55	66	77		
0x08-B	88	99	AΑ	ВВ		
0x0C-F						
0x10-3	1A	2A	3A	4A		
0x14-7	1B	2B	3B	4B		
0x18-B	1C	2C	3C	4C		
0x1C-F	1C	2C	3C	4C		

physical addresses	byt	es		
0x20-3	D0	D1	D2	D3
0x24-7				
0x28-B	89	9A	AB	ВС
0x2C-F	CD	DE	EF	F0
0x30-3	ВА	0A	ВА	0A
0x34-7	DB	0B	DB	0B
0x38-B	EC	0C	EC	0C
0x3C-F	FC	0C	FC	0C

9-bit virtual addresses, 6-bit physical; 8 byte pages, 1 byte PTE page tables 1 page; PTE: 3 bit PPN (MSB), 1 valid bit, 4 unused page table base register 0x08; translate virtual address 0x1CB

physical addresses	byte	es			physical addresses	byt	es		
0x00-3			22	33	0x20-3			D2	D3
0x04-7	44	55	66	77	0x24-7	D4	D5	D6	D7
0x08-B	88	99	AA	ВВ	0x28-B	89	9A	AB	ВС
0x0C-F	CC	DD	EE	FF	0x2C-F	CD	DE	EF	F0
0x10-3	1A	2A	ЗА	4A	0x30-3	ВА	0A	ВА	0A
0x14-7	1B	2B	3B	4B	0x34-7	DB	0B	DB	0B
0x18-B	1C	2C	3C	4C	0x38-B	EC	0C	EC	0C
0x1C-F	1C	2C	3C	4C	0x3C-F	FC	0C	FC	0C

10-bit virtual addresses, 6-bit physical; 16 byte pages, 2 byte PTE

page tables 1 page; PTE 1st byte: (MSB) 2-bit PPN, valid bit; rest unused

physical bytes addresses					
addresses					
0x00-3					
0x04-7	44	55	66	77	
0x08-B					
0x0C-F	CC	DD	EE	FF	
0x10-3	1A	2A	ЗА	4A	
0x14-7					
0x18-B	1C	2C	3C	4C	
0x1C-F	AC	ВС	DC	EC	

```
physical addresses

0x20-3 D0 E1 D2 D3

0x24-7 D4 E5 D6 E7

0x28-B 89 9A AB BC

0x2C-F CD DE EF F0

0x30-3 BA 0A BA 0A

0x34-7 DB 0B DB 0B

0x38-B EC 0C EC 0C

0x3C-F FC 0C FC 0C
```

10-bit virtual addresses, 6-bit physical; 16 byte pages, 2 byte PTE

page tables 1 page; PTE 1st byte: (MSB) 2-bit PPN, valid bit; rest unused

physical bytes addresses						
0x00-3	90	11	22	33		
0x04-7	14	55	66	77		
0x08-B	38	99	AΑ	ВВ		
0x0C-F						
0x10-3	LΑ	2A	3A	4A		
0x14-7	LΒ	2B	3B	4B		
0x18-B	LĊ	2C	3C	4C		
0x1C-F	١C	ВС	DC	EC		

```
physical bytes
addresses
0x20-3D0 E1 D2 D3
0x24-7D4 E5 D6 E7
0x28-Bl89 9A AB BC
0x2C-FCD DE EF F0
0x30-3|BA 0A BA 0A
0x34-7DB 0B DB 0B
0x38-B|EC 0C EC 0C
0x3C-FIFC 0C FC 0C
```

10-bit virtual addresses, 6-bit physical; 16 byte pages, 2 byte PTE

page tables 1 page; PTE 1st byte: (MSB) 2-bit PPN, valid bit; rest unused

physical bytes addresses						
0x00-3	90	11	22	33		
0x04-7	14	55	66	77		
0x08-B	38	99	AΑ	ВВ		
0x0C-F						
0x10-3	LΑ	2A	3A	4A		
0x14-7	LΒ	2B	3B	4B		
0x18-B	LĊ	2C	3C	4C		
0x1C-F	١C	ВС	DC	EC		

```
physical bytes
addresses
0x20-3D0 E1 D2 D3
0x24-7D4 E5 D6 E7
0x28-Bl89 9A AB BC
0x2C-FCD DE EF F0
0x30-3|BA 0A BA 0A
0x34-7DB 0B DB 0B
0x38-B|EC 0C EC 0C
0x3C-FIFC 0C FC 0C
```

10-bit virtual addresses, 6-bit physical; 16 byte pages, 2 byte PTE

page tables 1 page; PTE 1st byte: (MSB) 2-bit PPN, valid bit; rest unused

physical bytes addresses						
addresses						
0x00-3						
0x04-7	44	55	66	77		
0x08-B	88	99	AA	ВВ		
0x0C-F						
0x10-3						
0x14-7						
0x18-B	1C	2C	3C	4C		
0x1C-F	AC	BC	DC	EC		

```
physical bytes addresses 0x20-3 D0 E1 D2 D3 0x24-7 D4 E5 D6 E7 0x28-B 89 9A AB BC 0x2C-F CD DE EF F0 0x30-3 BA 0A BA 0A 0x34-7 DB 0B DB 0B 0x38-B EC 0C EC 0C 0x3C-F FC 0C FC 0C
```

10-bit virtual addresses, 6-bit physical; 16 byte pages, 2 byte PTE

page tables 1 page; PTE 1st byte: (MSB) 2-bit PPN, valid bit; rest unused

physical bytes addresses						
0x00-3	90	11	22	33		
0x04-7	14	55	66	77		
0x08-B	38	99	AΑ	ВВ		
0x0C-F						
0x10-3	LΑ	2A	3A	4A		
0x14-7	LΒ	2B	3B	4B		
0x18-B	LĊ	2C	3C	4C		
0x1C-F	١C	ВС	DC	EC		

```
physical bytes
addresses
0x20-3D0 E1 D2 D3
0x24-7D4 E5 D6 E7
0x28-Bl89 9A AB BC
0x2C-FCD DE EF F0
0x30-3|BA 0A BA 0A
0x34-7DB 0B DB 0B
0x38-B|EC 0C EC 0C
0x3C-FIFC 0C FC 0C
```

10-bit virtual addresses, 6-bit physical; 16 byte pages, 2 byte PTE

page tables 1 page; PTE 1st byte: (MSB) 2-bit PPN, valid bit; rest unused

physical bytes addresses						
0x00-3	00	11	22	33		
0x04-7	44	55	66	77		
0x08-B	88	99	AΑ	ВВ		
0x0C-F	CC	DD	EE	FF		
0x10-3	1A	2A	3A	4A		
0x14-7	1B	2B	3B	4B		
0x18-B	1C	2C	3C	4C		
0x1C-F	AC	ВС	DC	EC		

```
physical bytes addresses 0x20-3 D0 E1 D2 D3 0x24-7 D4 E5 D6 E7 0x28-B 89 9A AB BC 0x2C-F CD DE EF F0 0x30-3 BA 0A BA 0A 0x34-7 DB 0B DB 0B 0x38-B EC 0C EC 0C 0x3C-F FC 0C FC 0C
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physical bytes addresses						
0x00-3	90	11	22	33		
0x04-7	14	55	66	77		
0x08-B	38	99	AΑ	ВВ		
0x0C-F						
0x10-3	LΑ	2A	3A	4A		
0x14-7	LΒ	2B	3B	4B		
0x18-B	LĊ	2C	3C	4C		
0x1C-F	١C	ВС	DC	EC		

```
physical bytes
addresses
0x20-3D0 E1 D2 D3
0x24-7D4 E5 D6 E7
0x28-Bl89 9A AB BC
0x2C-FCD DE EF F0
0x30-3|BA 0A BA 0A
0x34-7DB 0B DB 0B
0x38-B|EC 0C EC 0C
0x3C-FIFC 0C FC 0C
```

10-bit virtual addresses, 6-bit physical; 16 byte pages, 2 byte PTE

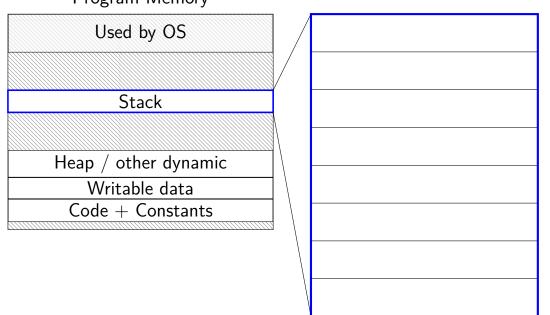
page tables 1 page; PTE 1st byte: (MSB) 2-bit PPN, valid bit; rest unused

physical bytes addresses					
addresses					
0x00-3					
0x04-7	44	55	66	77	
0x08-B					
0x0C-F	CC	DD	EE	FF	
0x10-3	1A	2A	ЗА	4A	
0x14-7					
0x18-B	1C	2C	3C	4C	
0x1C-F	AC	ВС	DC	EC	

```
physical bytes addresses 0x20-3 D0 E1 D2 D3 0x24-7 D4 E5 D6 E7 0x28-B 89 9A AB BC 0x2C-F CD DE EF F0 0x30-3 BA 0A BA 0A 0x34-7 DB 0B DB 0B 0x38-B EC 0C EC 0C 0x3C-F FC 0C FC 0C
```

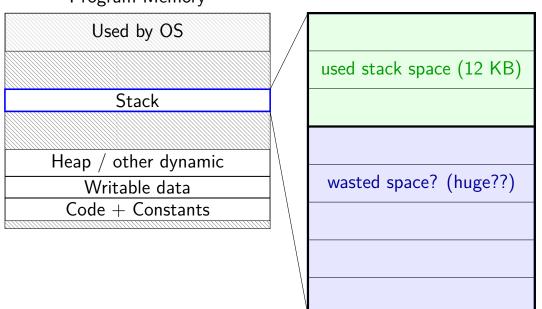
### space on demand

Program Memory



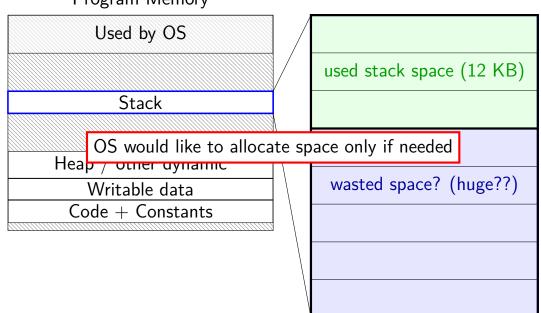
### space on demand

Program Memory



#### space on demand

Program Memory



%rsp = 0x7FFFC000

```
// requires more stack space
A: pushq %rbx

B: movq 8(%rcx), %rbx
C: addq %rbx, %rax
...
```

VPN	valid?	page
		page
•••	•••	•••
0x7FFFB	0	
0x7FFFC	1	0x200DF
0x7FFFD	1	0x12340
0x7FFFE	1	0x12347
0x7FFFF	1	0x12345
•••	•••	•••

%rsp = 0x7FFFC000

```
// requires more stack space
A: pushq %rbx
page fault!
B: movq 8(%rcx), %rbx
C: addq %rbx, %rax
...
```

VPN	valid?	physical page
VEIN	valiu:	page
•••	•••	•••
0x7FFFB	0	
0x7FFFC	1	0x200DF
0x7FFFD	1	0x12340
0x7FFFE	1	0x12347
0x7FFFF	1	0x12345
•••	•••	•••

pushq triggers exception hardware says "accessing address 0x7FFBFF8" OS looks up what's should be there — "stack"

%rsp = 0x7FFFC000

```
// requires more stack space
A: pushq %rbx restarted

B: movq 8(%rcx), %rbx
C: addq %rbx, %rax
...
```

VPN	valid?	physical page
VIIV	valiu:	page
•••	•••	•••
0x7FFFB	1	0x200D8
0x7FFFC	1	0x200DF
0x7FFFD	1	0x12340
0x7FFFE	1	0x12347
0x7FFFF	1	0x12345
•••	•••	•••

in exception handler, OS allocates more stack space OS updates the page table then returns to retry the instruction

note: the space doesn't have to be initially empty

only change: load from file, etc. instead of allocating empty page

loading program can be merely creating empty page table everything else can be handled in response to page faults no time/space spent loading/allocating unneeded space

#### mmap

```
Linux/Unix has a function to "map" a file to memory
int file = open("somefile.dat", O_RDWR);
    // data is region of memory that represents file
char *data = mmap(..., file, 0);
   // read byte 6 from somefile.dat
char seventh_char = data[6];
   // modifies byte 100 of somefile.dat
data[100] = 'x';
    // can continue to use 'data' like an array
```

## swapping almost mmap

```
access mapped file for first time, read from disk (like swapping when memory was swapped out)
```

write "mapped" memory, write to disk eventually (like writeback policy in swapping) use "dirty" bit

extra detail: other processes should see changes all accesses to file use same physical memory

### **Linux maps: list of maps**

```
$ cat /proc/self/maps
00400000-0040b000 r-xp 00000000 08:01 48328831
                                                         /bin/cat
0060a000-0060b000 r-p 0000a000 08:01 48328831
                                                         /bin/cat
0060b000-0060c000 rw-p 0000b000 08:01 48328831
                                                         /bin/cat
01974000-01995000 rw-p 00000000 00:00 0
                                                         [heap]
7f60c718b000-7f60c7490000 r-p 00000000 08:01 77483660
                                                         /usr/lib/locale/locale—archive
7f60c7490000-7f60c764e000 r-xp 00000000 08:01 96659129
                                                         /lib/x86_64-linux-gnu/libc-2.1
7f60c764e000-7f60c784e000 ----p 001be000 08:01 96659129
                                                         /lib/x86_64-linux-gnu/libc-2.1
7f60c784e000-7f60c7852000 r-p 001be000 08:01 96659129
                                                         /lib/x86_64-linux-gnu/libc-2.1
7f60c7852000-7f60c7854000 rw-p 001c2000 08:01 96659129
                                                         /lib/x86 64-linux-gnu/libc-2.1
7f60c7854000-7f60c7859000 rw-p 00000000 00:00 0
7f60c7859000-7f60c787c000 r-xp 00000000 08:01 96659109
                                                         /lib/x86_64-linux-gnu/ld-2.19.
7f60c7a39000-7f60c7a3b000 rw-p 00000000 00:00 0
7f60c7a7a000-7f60c7a7b000 rw-p 00000000 00:00 0
7f60c7a7b000-7f60c7a7c000 r-p 00022000 08:01 96659109
                                                         /lib/x86_64-linux-gnu/ld-2.19.
7f60c7a7c000-7f60c7a7d000 rw-p 00023000 08:01 96659109
                                                         /lib/x86_64-linux-gnu/ld-2.19.s
7f60c7a7d000-7f60c7a7e000 rw-p 00000000 00:00 0
7ffc5d2b2000-7ffc5d2d3000 rw-p 00000000 00:00 0
                                                         [stack]
7ffc5d3b0000-7ffc5d3b3000 r—p 00000000 00:00 0
                                                         [vvar]
7ffc5d3b3000-7ffc5d3b5000 r-xp 00000000 00:00 0
                                                         vdsol
fffffffff600000-ffffffffff601000 r-xp 00000000 00:00 0
                                                         [vsyscall]
```

### Linux maps: list of maps

```
$ cat /proc/self/maps
00400000-0040b000 r-xp 00000000 08:01 48328831
                                                        /bin/cat
0060a000-0060b000 r-p 0000a000 08:01 48328831
                                                         /bin/cat
0060b000-0060c000 rw-p 0000b000 08:01 48328831
                                                         /bin/cat
01974000 - 01995000 \text{ rw-p} 00000000 00:00 0
                                                         [heap]
7f60c718b000_7f60c7490000
                                                         <u>usr/lib/locale/lo</u>cale—archive
7f60c74900 OS tracks list of struct vm_area_struct with:
                                                                          gnu/libc-2.1
7f60c764e0
                                                                          gnu/libc-2.1
          (shown in this output):
7f60c784e0
                                                                          gnu/libc-2.1
7f60c78520
                                                                          gnu/libc-2.1
             virtual address start, end
7f60c78540
                                                                          gnu/ld-2.19.s
7f60c78590
             permissions
7f60c7a390
7f60c7a7a0
             offset in backing file (if any)
7f60c7a7b0
                                                                          gnu/ld-2.19.s
7f60c7a7c0
             pointer to backing file (if any)
                                                                          gnu/ld-2.19.s
7f60c7a7d0
7ffc5d2b20
7ffc5d3b00
           (not shown):
7ffc5d3b30
ffffffffff
             info about sharing of non-file data
```

#### page tricks generally

deliberately make program trigger page/protection fault

but don't assume page/protection fault is an error

have seperate data structures represent logically allocated memory e.g. "addresses 0x7FFF8000 to 0x7FFFFFFF are the stack"

page table is for the hardware and not the OS

### hardware help for page table tricks

information about the address causing the fault
e.g. special register with memory address accessed
harder alternative: OS disassembles instruction, look at registers

(by default) rerun faulting instruction when returning from exception

precise exceptions: no side effects from faulting instruction or after e.g. pushq that caused did not change %rsp before fault e.g. can't notice if instructions were executed in parallel

### swapping

early motivation for virtual memory: swapping

using disk (or SSD, ...) as the next level of the memory hierarchy how our textbook and many other sources presents virtual memory

OS allocates program space on disk own mapping of virtual addresses to location on disk

DRAM is a cache for disk

### swapping

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OS allocates program space on disk own mapping of virtual addresses to location on disk

DRAM is a cache for disk

#### swapping components

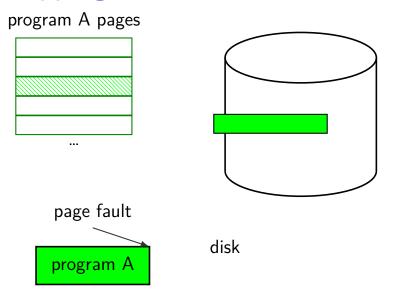
```
"swap in" a page — exactly like allocating on demand!
    OS gets page fault — invalid in page table
    check where page actually is (from virtual address)
    read from disk
    eventually restart process
"swap out" a page
    OS marks as invalid in the page table(s)
    copy to disk (if modified)
```

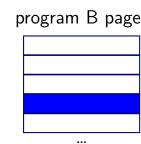
HDD reads and writes: milliseconds to tens of milliseconds minimum size: 512 bytes writing tens of kilobytes basically as fast as writing 512 bytes

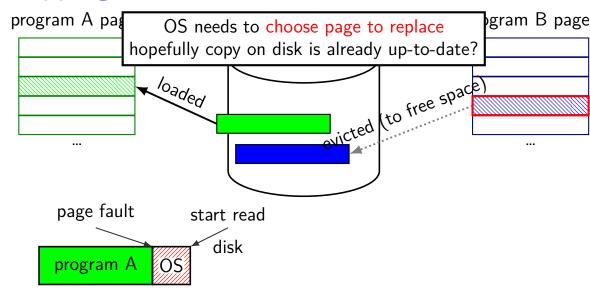
HDD reads and writes: milliseconds to tens of milliseconds minimum size: 512 bytes writing tens of kilobytes basically as fast as writing 512 bytes

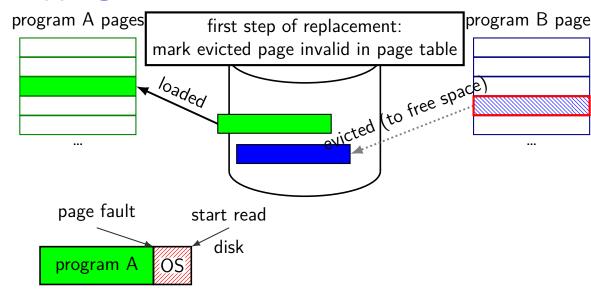
HDD reads and writes: milliseconds to tens of milliseconds minimum size: 512 bytes writing tens of kilobytes basically as fast as writing 512 bytes

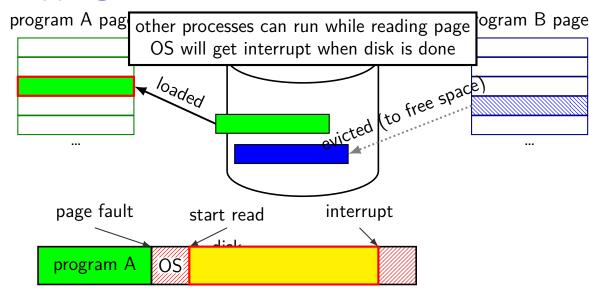
HDD reads and writes: milliseconds to tens of milliseconds minimum size: 512 bytes writing tens of kilobytes basically as fast as writing 512 bytes

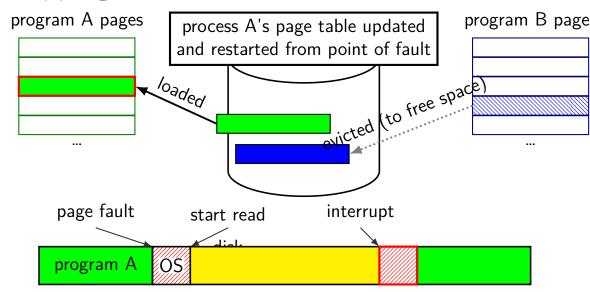












# do we really need a complete copy?

bash	new copy of bash
Used by OS	Used by OS
Stack	Stack
Stuck	Stack
Heap / other dynamic	Heap / other dynamic
Writable data	Writable data
Code + Constants	Code + Constants

bash	new copy of bash			
Used by OS	Used by OS			
Stack	Stack			
Heap / other dynamic	Heap / other dynamic			
Writable data	Writable data			
Code + Constants	Code + Constants			

shared as read-only

bash	new copy of bash		
Used by OS	Used by OS		
Stack	Stack		
Heap / other dynamic	Heap $/$ other dynamic		
Writable data	Writable data		
$Code + Constants_{can't}$ be $shared?$ $Code + Constants$			

```
sharing writeable data is fine — until either process modifies it example: default value of global variables might typically not change (or OS might have preloaded executable's data anyways)
```

can we detect modifications?

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```

can we detect modifications?

trick: tell CPU (via page table) shared part is read-only processor will trigger a fault when it's written

**VPN** 

valid? write?

•••

0x00601 0x00602 0x00603 0x00604 0x00605

		F-0-
•••	•••	•••
1	1	0x12345
1	1	0x12347
1	1	0x12340
1	1	0x200DF
1	1	0x200AF
•••	•••	•••

VPN	
•••	
0x00601	
0x00602	
0x00603	
0x00604	
0x00605	
•••	

valid? write? page			
•••	•••	•••	
1	0	0x12345	
1	0	0x12347	
1	0	0x12340	
1	0	0x200DF	
1	0	0x200AF	
•••	•••	•••	

•••
0x00601
0x00602
0x00603
0x00604
0x00605

VPN

valid? write?	writa?	physical		
valiu:	wille:	page		

•••	•••	•••
1	0	0x12345
1	0	0x12347
1	0	0x12340
1	0	0x200DF
1	0	0x200AF
•••	•••	•••

copy operation actually duplicates page table both processes share all physical pages but marks pages in both copies as read-only

VPN	valid?	write	? Page	VPN
			1	1
•••	•••	***	•••	•••
0x00601	1	0	0x12345	0x00601
0x00602	1	0	0x12347	0x00602
0x00603	1	0	0x12340	0x00603
0x00604	1	0	0x200DF	<u>0x00604</u>
0x00605	1	0	0x200AF	0x00605
•••	•••	•••	•••	•••

VPN	valid? write?			
VIIV	valid? write? page			
•••	•••	•••	•••	
0x00601	1	0	0x12345	
0x00602	1	0	0x12347	
0x00603	1	0	0x12340	
0x00604	1	0	0x200DF	
0x00605	1	0	0x200AF	
•••	•••	•••	•••	

nhysical

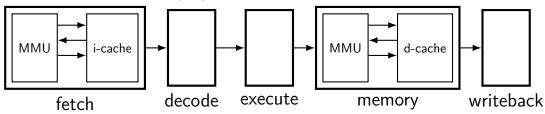
when either process tries to write read-only page triggers a fault — OS actually copies the page

VPN valid? write? pa		physical	VPN	valid?	valid? write? page		
VIIN	valiu:	WIILE	page	V I IN	valiu:	WITE	page
•••	•••	•••	•••	•••	•••	•••	•••
0x00601	1	0	0x12345	0x00601	1	0	0x12345
0x00602	1	0	0x12347	0x00602	1	0	0x12347
0x00603	1	0	0x12340	0x00603	1	0	0x12340
0x00604	1	0	0x200DF	<u>0x00604</u>	1	0	0x200DF
0x00605	1	0	0x200AF	0x00605	1	1	0x300FD
•••	•••	•••	•••	•••	•••	•••	•••

after allocating a copy, OS reruns the write instruction

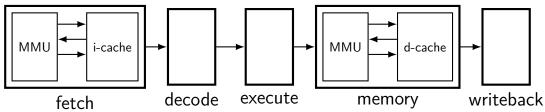
# backup slides

## MMUs in the pipeline



up to four memory accesses per instruction

## MMUs in the pipeline



up to four memory accesses per instruction challenging to make this fast (topic for a future date)

bash	new copy of bash
Used by OS	Used by OS
Stack	Stack
Stuck	Stack
Heap / other dynamic	Heap / other dynamic
Writable data	Writable data
Code + Constants	Code + Constants

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Used by OS	Used by OS			
Stack	Stack			
Heap / other dynamic	Heap / other dynamic			
Writable data	Writable data			
Code + Constants	Code + Constants			

shared as read-only

bash	new copy of bash		
Used by OS	Used by OS		
Stack	Stack		
Heap / other dynamic	Heap $/$ other dynamic		
Writable data	Writable data		
Code + Constants $Can't$ be $Code + Constants$			

```
sharing writeable data is fine — until either process modifies it example: default value of global variables might typically not change (or OS might have preloaded executable's data anyways)
```

can we detect modifications?

```
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**VPN** 

physical valid? write?

•••

0x00601 0x00602 0x00603 0x00604 0x00605

		F-0-
•••	•••	•••
1	1	0x12345
1	1	0x12347
1	1	0x12340
1	1	0x200DF
1	1	0x200AF
•••	•••	•••

VPN
•••
0x00601
0x00602
0x00603
0x00604
0x00605
•••

physical valid? write? page					
•••	•••				
1	0	0x12345			
1	0	0x12347			
1	0	0x12340			
1	0	0x200DF			
1	0	0x200AF			
•••	•••	•••			

VPN

valid? write? page				
	•••	•••	•••	
	1	0	0x12345	

•••	•••
0	0x12345
	0x12347
0	0x12340
	0x200DF
0	0x200AF
•••	•••
	0 0 0

copy operation actually duplicates page table both processes share all physical pages but marks pages in both copies as read-only

VPN	valid? write? page			VPN
•••	•••	•••		•••
0x00601	1	0	0x12345	0×00
0x00602	1	0	0x12347	0×00
0x00603	1	0	0x12340	0×00
0x00604	1	0	0x200DF	0×00
0x00605	1	0	0x200AF	0×00
•••	•••	•••	•••	•••

VPN	valid? write?				
VIIV	valiu:	WIILE	<sup>!</sup> page		
•••	•••	•••	•••		
0x00601	1	0	0x12345		
0x00602	1	0	0x12347		
0x00603	1	0	0x12340		
0x00604	1	0	0x200DF		
0x00605	1	0	0x200AF		
•••	•••	•••	•••		

when either process tries to write read-only page triggers a fault — OS actually copies the page

VPN	physical valid? write?		physical	VPN	physical valid? write?		
VIIN	valiu:	wiite:	page	VIIV	vallu:	wille:	page
•••	•••	•••	•••	•••	•••	•••	•••
0x00601	1	0	0x12345	0x00601	1	0	0x12345
0x00602	1	0	0x12347	0x00602	1	0	0x12347
0x00603	1	0	0x12340	0x00603	1	0	0x12340
0x00604	1	0	0x200DF	0x00604	1	0	0x200DF
0x00605	1	0	0x200AF	0x00605	1	1	0x300FD
•••	•••	•••	•••	•••	•••	•••	•••

after allocating a copy, OS reruns the write instruction