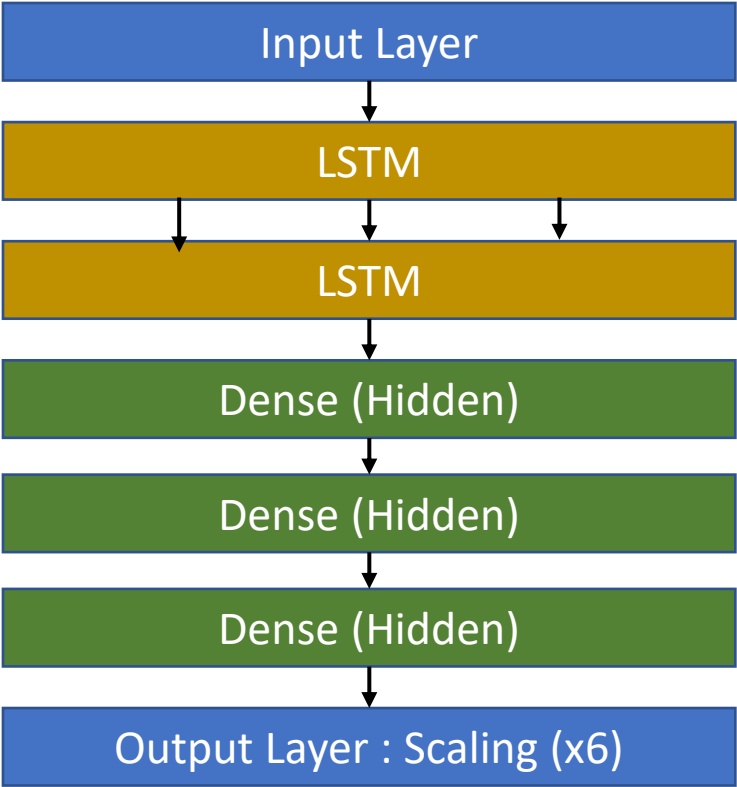


**Temporal Input: [S(1), S(2), S(3)...S(n)]**



**Output: S(n+1)**

**LSTM Node Architecture (x9)**

