|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Inst** | **op** | **func** | **ALUOp** | **MemWr** | **MemtoReg** | **MemRd** | **ALUSrc** | **BType** | **RegDst** | **RegWr** |
| addi | 110000 |  | 000 | 0 | 00 | 0 | 1 | 00 | 00 | 1 |
| and | 100001 | 101000 | 010 | 0 | 00 | 0 | 0 | 00 | 01 | 1 |
| sub | 100001 | 011000 | 001 | 0 | 00 | 0 | 0 | 00 | 01 | 1 |
| sgt | 100001 | 001010 | 110 | 0 | 00 | 0 | 0 | 00 | 01 | 1 |
| lw | 010010 |  | 000 | 0 | 01 | 1 | 1 | 00 | 00 | 1 |
| sw | 010011 |  | 000 | 1 | -1 | 0 | 1 | 00 | -1 | 0 |
| blt | 001110 |  | 111 | 0 | -1 | 0 | 0 | 11 | -1 | 0 |
| jr | 101001 |  | -1 | 0 | -1 | 0 | -1 | 01 | -1 | 0 |
| jal | 001010 |  | -1 | 0 | 10 | 0 | -1 | 10 | 10 | 1 |

(-1 represents x’s)

The highlighted represents the values I changed since last time