

Lab 2: Communicate with your hardware

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Pre-Lab:

N/A

Objective:

The objective of this lab is to familiarize ourselves with the connection between our hardware and software, and create our first design to utilize it. This will allow us to transfer data between the halves and test the hardware.

Circuit Diagrams:

N/A

Outcome Predictions:

At the end of this lab, we should have a settable counter implemented in hardware. When the divisor is set, this counter should increment a register. The software should be able to read this value out of the memory. Then the value of the register will be written to the LEDs on-board.

Equipment:

- Computer
- Digilent Zybo 7000
- Xilinx Vivado 2018

Procedure:

Recalculations and Predictions:

Data and Observations:

Analysis:

Discussion:

Results:

Conclusions: