Digital Design Postlab #3
Charlie Coleman
2016 September 15

    In this lab, we were able to see the delays involved when making a circuit. When initially testing the circuit, the changes in output happened instantly, because the test was only to see the outputs, and to check the delays. When testing the circuit on the board and in the simulation, the time delays were present, and while they were not visible to the human eye, they were present on the graph. The delay for the circuit was about 6ns.

    By doing this lab, the Xilinx software was introduced to us. We were able to practice many of the functions that we will use Xilinx for in the future. Functions such as clock and forced inputs help to test the circuits that we create and therefore verify that we have gotten everything working. The simulations help to see the time delay involved in the calculations and to make sure that those delays to do not affect our circuit in any way.