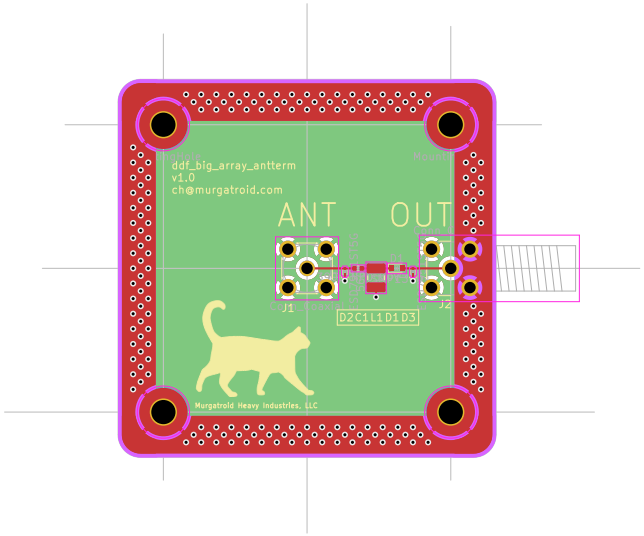

50Z SE for JLC7628:

11.55 mil for microstrip per JLCPCB calculator

Stack up per JLC04161H-7628

layer "F.Silkscreen" type "Top Silk Screen" Color "Not specified" Material "Not specified"
layer "F.Paste" type "Top Solder Paste"
layer "F.Mask" type "Top Solder Mask" Color "Not specified" Thickness 0.0127 mm Material "Not specified" EpsilonR 3.3 LossTg 0
layer "F.Cu" type "copper" Thickness 0.035 mm
layer "Dielectric 1" type "core"
  sublayer "1/1" Thickness 0.2104 mm Material "FR4" EpsilonR 4.6 LossTg 0.02
layer "In1.Cu" type "copper" Thickness 0.0152 mm
layer "Dielectric 2" type "prepreg"
  sublayer "1/1" Thickness 1.065 mm Material "FR4" EpsilonR 4.6 LossTg 0.02
layer "In2.Cu" type "copper" Thickness 0.0152 mm
layer "Dielectric 3" type "core"
  sublayer "1/1" Thickness 0.2104 mm Material "FR4" EpsilonR 4.6 LossTg 0.02
layer "B.Cu" type "copper" Thickness 0.035 mm
layer "B.Mask" type "Bottom Solder Mask" Color "Not specified" Thickness 0.0127 mm Material "Not specified" EpsilonR 3.3 LossTg 0
layer "B.Paste" type "Bottom Solder Paste"
layer "B.Silkscreen" type "Bottom Silk Screen" Color "Not specified" Material "Not specified"
Finish "None" Option "Impedance Controlled"

Sheet:		
File: ddf_big_array_antterm.kicad_pcb		
Title:		
Size: A4	Date:	**Rev:**
KiCad E.D.A. kicad 6.0.2+dfsg-1		Id: 1/1