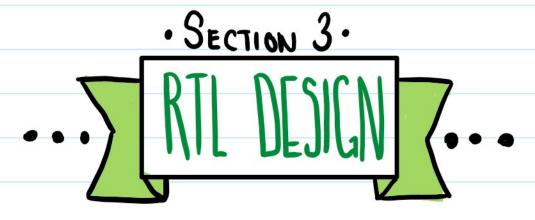
Section 3 - RTL Design

Wednesday, June 19, 2019

12:56 PM



CUSTOM PROCESSOR-CIRCUIT CONSISTING OF A CONTROLLER AND DATAPATH

RTL DESIGN-REGISTER TRANSFER LEVEL DESIGN

RTL DESIGN _____ PROCESS

- 1) CAPTURE BEHAVIOR AS AN HLSM
- 2) MAKE DATAPATH FROM HLSM
- 3) MAKE FSM FROM DATAPATH
 4 USE SAME STATES AS HISM
 4 1 BIT INPUT SAME AS HISM
 4 CONTROL STANDES FOR DATAPATH
- 4) MAKE CONTROLLER FROM FSM

AKE OUTPUTS