· SECTION 3 ·

RTL DESIGN

CUSTOM PROCESSOR- CIRCUIT CONSISTING OF A CONTROLLER AND DATAPATH

RTL DESIGN- REGISTER TRANSFER LEVEL DESIGN

RTL Design Process

1) Capture Behavior as an HLSM

2) Make Datapath from HLSM

3) Make FSM from Datapath
   - ↳ Use same states as HLSM
   - ↳ 1 bit input same as HLSM
   - ↳ Control signals for Datapath are outputs

4) Make Controller from FSM