

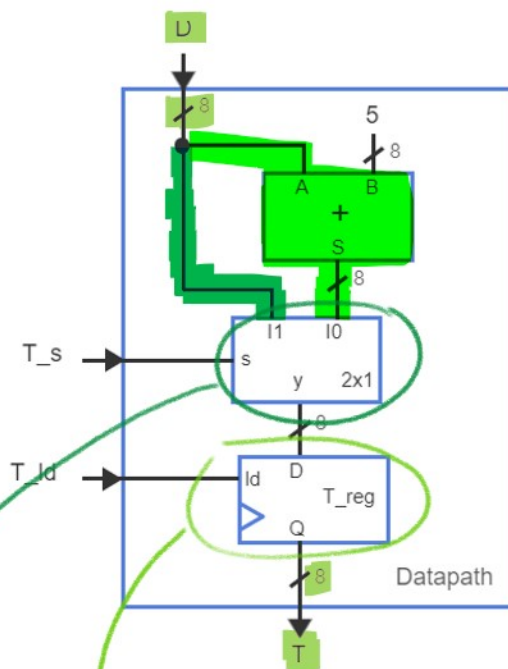
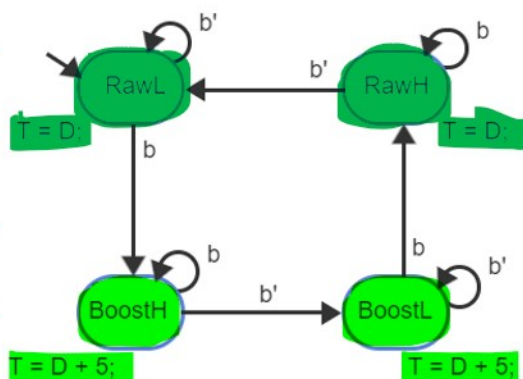
Section 2 - Datapaths

Wednesday, June 19, 2019 12:54 PM

• SECTION 2 • • DATAPATHS •

DATAPATH- A CIRCUIT THAT CAN SUPPORT DATA OPERATION AND DATA STORAGE

Inputs: $b, D(8)$
Outputs: $T(8)$



SINCE T CAN BE 1 OF 2 OPTIONS (D or $D+5$) A MUX IS USED

EACH OUTPUT NEEDS A REGISTER

- A FSM WILL BE USED TO MAKE A CONTROLLER AND MAKE A PROCESSOR

- THE FSM WILL HAVE INPUT b and outputs T_s + T_{ld}