2023 Digital IC Design Homework 5

2023 Digital	ic Design Homew	2023 Digital IC Design Homework 3		
賴姿伶				
Student ID E94091128				
Simulation Result				
Complete	Gate-level	Completed		
n	simulation	Completed		
Simulation completed successfully! *** Simulation completed successfully! *** Note: cfinish : D:/digital ic design/HWS/func/testfixture.v(145) ** Time: 8336580 ns Iteration: 1 Instance: /testfixture *** Simulation completed successfully! *** Note: cfinish : D:/digital ic design/HWS/gate/testfixture.v(145) ** Time: 8336580 ns Iteration: 1 Instance: /testfixture				
Evaluation Results				
25.47	test2.png	24.97		
29.17	test4.png	20.96		
22.26	test6.png	25.27		
Description of your design				
以標準的雙線性插值法為基準,考量到提高畫質的要素之一是邊緣銳利化,以及人眼對綠色的感覺最敏銳,因此針對綠色的計算做優化。如果上下的差大於左右的差,代表邊緣是橫向的使用左右兩像素平均值;反之是縱向的使用上下兩像素平均值。 一開始先把所有輸入值存入 RGB 各自的 memory 每次取 9 個值計算正中間缺少的 RGB 值,除法皆為四捨五入取整數邊以外的 pixel 計算完成後,把 done 拉高				
	照接伶 E94091128 Sin Complete Co	## E94091128 Completed Gate-level simulation Result		

Scoring = average PSNR of the six test images

* PSNR of all interpolation results should meet at least the baseline.