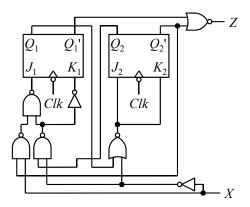
CSIE 2344: Discussion (Unit 13)

1 Analysis of Clocked Sequential Circuits

- 1. Construct a state table and a state graph for the following circuit. Is the circuit a Mealy or Moore circuit? Does the circuit have any unused state? Assume 00 is the initial state.
- 2. What is the output sequence for the input sequence X = 01100.



2 Moore or Mealy?

Given the following timing chart for a sequential circuit, construct as much of the transition table as possible. Is this a Moore or Mealy circuit?

