

# Homework 2

● Graded

## Student

陳姍安

## Total Points

54 / 60 pts

## Question 1

### Question 1

6 / 6 pts

✓ + 6 pts Correct

+ 4 pts A minor mistake

+ 2 pts A major mistake or two minor mistakes

+ 0 pts Totally wrong or empty

## Question 2

### Question 2

12 / 12 pts

✓ + 12 pts Correct

+ 10 pts A minor mistake

+ 8 pts A major mistake or two minor mistakes

+ 6 pts The question has two parts, but only one is correct

+ 4 pts Some reasonable effort

+ 0 pts Totally wrong or empty

## Question 3

### Question 3

6 / 6 pts

✓ + 6 pts Correct

+ 4 pts A minor mistake

+ 2 pts A major mistake or two minor mistakes

+ 0 pts Totally wrong or empty

#### Question 4

#### Question 4

12 / 12 pts

✓ + 12 pts Correct

+ 10 pts A minor mistake

+ 8 pts A major mistake or two minor mistakes

+ 6 pts The question has two parts, but only one is answered

+ 0 pts Totally wrong or empty

#### Question 5

#### Question 5

Resolved 4 / 6 pts

+ 6 pts Correct

✓ + 4 pts A minor mistake

+ 2 pts A major mistake or two minor mistakes

+ 0 pts Totally wrong or empty

C Regrade Request

Submitted on: Apr 01

想詢問錯誤的是哪個部分，謝謝！

你好，你的答案並不是最簡化的結果，我們需要最簡化的結果，最簡化的結果是 $A'D + B'D' + C'D$ ，  
詳細作法請參考解答SOL-02 (0401).pdf，謝謝。

Reviewed on: Apr 01

### Question 6

#### Question 6



Resolved

8 / 12 pts

+ 12 pts Correct

+ 10 pts A minor mistake (including no clear explanation on "why minimum")

✓ + 8 pts A major mistake or two minor mistakes

+ 6 pts more than one major mistakes

+ 4 pts Some reasonable effort

+ 0 pts Totally wrong or empty

Wrong POS

Regrade Request

Submitted on: Apr 01

想詢問錯誤的是哪個部分，謝謝！

已寫在comment中，第二頁的K-map是錯的

Reviewed on: Apr 01

### Question 7

#### Question 7

6 / 6 pts

✓ + 6 pts Correct and minimum (5, 12)

+ 4 pts Correct and non-minimum

+ 2 pts Incorrect function or gate circuit

+ 0 pts Totally wrong or empty

No questions assigned to the following page.

# CSIE 2344, Spring 2024: Homework 2

Due April 1 (Monday) at Noon

There are 60 points in total. Points will be deducted if no appropriate intermediate step is provided.

When you submit your homework on Gradescope, please select the corresponding page(s) of each question.

## 1 Minterm and Maxterm Expansions (6pts)

A combinational circuit has three inputs ( $A, B, C$ ) and two outputs ( $X, Y$ ).  $XY$  represents a binary number whose value equals the number of 1's at the input. For example, if  $ABC = 101$ , then  $XY = 10$ . Find the minterm and maxterm expansions for  $X$  and  $Y$ , respectively.

## 2 BCD Multiplication (12pts)

A combinational circuit has four inputs ( $A, B, C, D$ ), which represent a binary-coded-decimal (BCD) digit. The circuit has two groups of four outputs— $S, T, U, V$  and  $W, X, Y, Z$ . Each group represents a BCD digit. The output digits represent a decimal number which is five times the input number. For example, if  $ABCD = 0111$ , then the outputs are 0011 0101. Assume that invalid BCD digits do not occur as inputs.

1. Construct the truth table.
2. Write down the minimum expressions for the outputs by inspection of the truth table.

## 3 Karnaugh Map I (6pts)

Use Karnaugh maps to find a minimum sum-of-products expression for  $F(A, B, C) = \sum m(1, 4, 6) + \sum d(0, 2, 7)$ .

## 4 Karnaugh Map II (12pts)

1. Find a minimum sum-of-products expression for  $F(A, B, C, D) = \prod M(0, 2, 10, 11, 12, 14, 15) \cdot \prod D(5, 7)$ .
2. Find a minimum product-of-sums expression for  $F(A, B, C, D) = \prod M(0, 2, 10, 11, 12, 14, 15) \cdot \prod D(5, 7)$ .

## 5 Karnaugh Map III (6pts)

A logic circuit realizes the function  $F(A, B, C, D) = A'B' + A'CD + AC'D + AB'D'$ . Assuming that  $A = C$  never occurs when  $B = D = 1$ , find a simplified expression for  $F$ .

No questions assigned to the following page.

## 6 Two-Level Gate Circuits (12pts)

Find eight different minimum two-level gate circuits (draw them) to realize  $F = A'BC' + AB'C' + BC'D$ . You do not need to show the conversions between the eight forms, but you need to explain why they are minimum.

## 7 Multi-Level Gate Circuits (6pts)

Using AND and OR gates, find a circuit (draw it) to realize  $F(A, B, C, D) = \prod M(0, 1, 3, 13, 14, 15)$ . Try to minimize the number gates and gate inputs (fewer gates and gate inputs, more points). How many gates are there? How many gate inputs are there?

Question assigned to the following page: [1](#)

# 1 Minterm and Maxterm Expansions (6pts)

A combinational circuit has three inputs ( $A, B, C$ ) and two outputs ( $X, Y$ ).  $XY$  represents a binary number whose value equals the number of 1's at the input. For example, if  $ABC = 101$ , then  $XY = 10$ . Find the minterm and maxterm expansions for  $X$  and  $Y$ , respectively.

Sol:

A	B	C	XY
0	0	0	00
0	0	1	01
0	1	0	01
0	1	1	10
1	0	0	01
1	0	1	10
1	1	0	10
1	1	1	11

$$X: 011, 101, 110, 111$$

$$X = \Sigma m(3, 5, 6, 7)$$

$$= \prod M(0, 1, 2, 4)$$

$$Y: 001, 010, 100, 111$$

$$Y = \Sigma m(1, 2, 4, 7)$$

$$= \prod M(0, 3, 5, 6)$$

$$\text{Ans: } X = \Sigma m(3, 5, 6, 7)$$

$$= \prod M(0, 1, 2, 4)$$

$$Y = \Sigma m(1, 2, 4, 7)$$

$$= \prod M(0, 3, 5, 6)$$

Question assigned to the following page: [2](#)

## 2 BCD Multiplication (12pts)

A combinational circuit has four inputs ( $A, B, C, D$ ), which represent a binary-coded-decimal (BCD) digit. The circuit has two groups of four outputs— $S, T, U, V$  and  $W, X, Y, Z$ . Each group represents a BCD digit. The output digits represent a decimal number which is five times the input number. For example, if  $ABCD = 0111$ , then the outputs are  $0011\ 0101$ . Assume that invalid BCD digits do not occur as inputs.

1. Construct the truth table.
2. Write down the minimum expressions for the outputs by inspection of the truth table.

**Ans:**

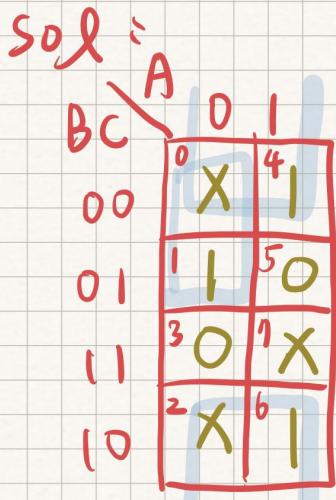
1. <u><math>ABCD</math></u>		<u><math>STUV</math></u>	<u><math>WXYZ</math></u>	
0 0 0 0	(0)	0 0 0 0	0 0 0 0	(0)
0 0 0 1	(1)	0 0 0 0	0 1 0 1	(5)
0 0 1 0	(2)	0 0 0 1	0 0 0 0	(10)
0 0 1 1	(3)	0 0 0 1	0 1 0 1	(15)
0 1 0 0	(4)	0 0 1 0	0 0 0 0	(20)
0 1 0 1	(5)	0 0 1 0	0 1 0 1	(25)
0 1 1 0	(6)	0 0 1 1	0 0 0 0	(30)
0 1 1 1	(7)	0 0 1 1	0 1 0 1	(35)
1 0 0 0	(8)	0 1 0 0	0 0 0 0	(40)
1 0 0 1	(9)	0 1 0 0	0 1 0 1	(45)

2.  $S = O$        $W = O$   
 $T = A$        $X = D$   
 $U = B$        $Y = O$   
 $V = C$        $Z = D$

Question assigned to the following page: [3](#)

### 3 Karnaugh Map I (6pts)

Use Karnaugh maps to find a minimum sum-of-products expression for  $F(A, B, C) = \sum m(1, 4, 6) + \sum d(0, 2, 7)$ .



$$C' + A'B'$$

Ans:  $C' + A'B'$

Question assigned to the following page: [4](#)

#### 4 Karnaugh Map II (12pts)

- Find a minimum sum-of-products expression for  $F(A, B, C, D) = \prod M(0, 2, 10, 11, 12, 14, 15) \cdot \prod D(5, 7)$ .
- Find a minimum product-of-sums expression for  $F(A, B, C, D) = \prod M(0, 2, 10, 11, 12, 14, 15) \cdot \prod D(5, 7)$ .

Sol:

$$F(A, B, C, D) = \prod M(0, 2, 10, 11, 12, 14, 15) \cdot \prod D(5, 7)$$

AB		00	01	11	10
CD	00	0	1	12	0
	01	1	5	X	9
11	3	1	0	X	11
10	2	0	6	14	0

- $A'B + C'D + A'D + AB'C'$
- $(A' + C')(A + B + D)(A' + B' + D)$

$$(F')' = AC + A'B'D' + AB'D'$$

$$F' = (A' + C')(A + B + D)(A' + B' + D)$$

- Ans:
- $A'B + C'D + A'D + AB'C'$
  - $(A' + C')(A + B + D)(A' + B' + D)$

Question assigned to the following page: [5](#)

## 5 Karnaugh Map III (6pts)

A logic circuit realizes the function  $F(A, B, C, D) = A'B' + A'CD + AC'D + AB'D'$ . Assuming that  $A = C$  never occurs when  $B = D = 1$ , find a simplified expression for  $F$ .

Sol:

		AB	00	01	11	10
		CD	00	01	11	10
00	00		1	0	0	1
			1	X	1	1
11	11		1	1	X	0
			1	0	0	1

X : 0101, 1111

$$A'B' + BD + C'D + B'D'$$

Ans:  $F = A'B' + BD + C'D + B'D'$

Question assigned to the following page: [6](#)

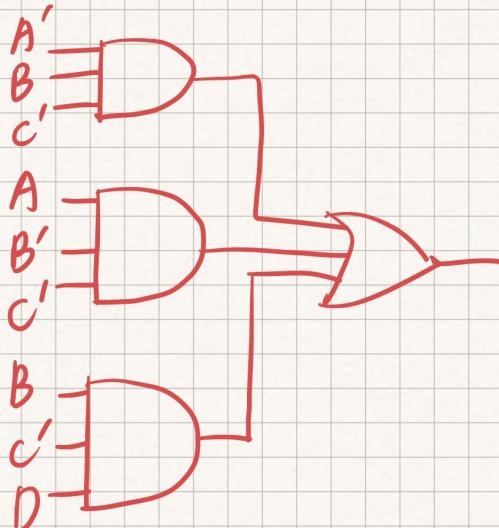
## 6 Two-Level Gate Circuits (12pts)

Find eight different minimum two-level gate circuits (draw them) to realize  $F = A'BC' + AB'C' + BC'D$ . You do not need to show the conversions between the eight forms, but you need to explain why they are minimum.

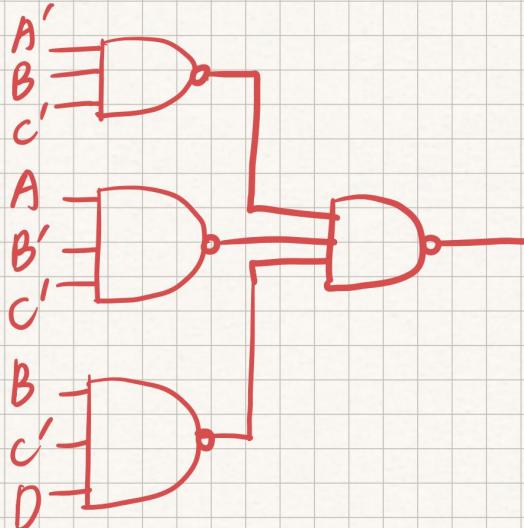
Sol:

AND-OR	$\leftarrow$ SOP
NAND-NAND	
OR-NAND	
NOR-OR	
<hr/>	
OR-AND	$\leftarrow$ POS
NOR-NOR	
AND-NOR	
NAND-AND	

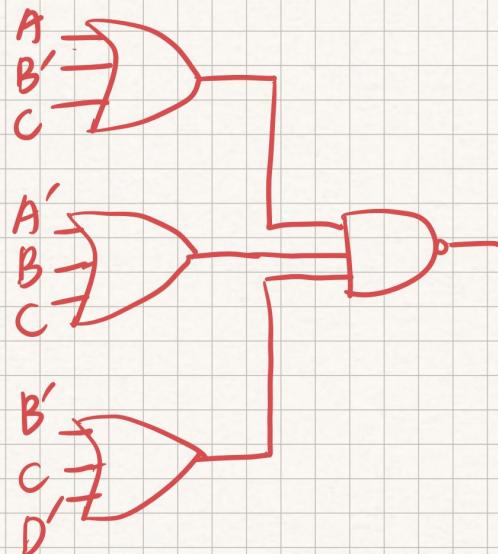
① AND-OR



② NAND-NAND

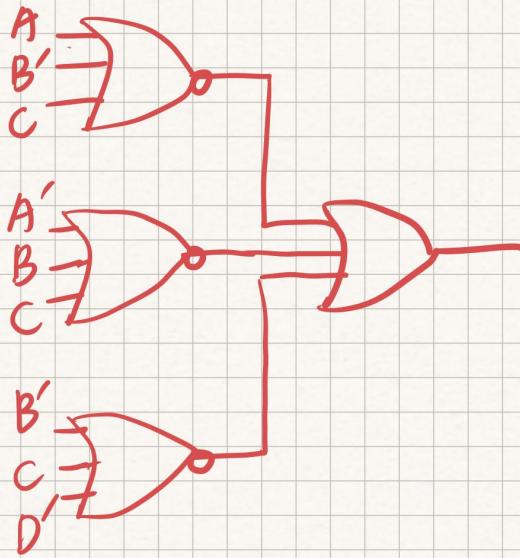


③ OR-NAND



Question assigned to the following page: [6](#)

## ④ NOR-OR



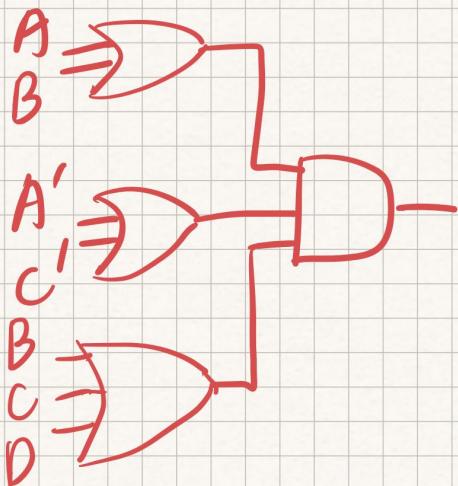
		AB	00	01	11	10
		C0	00	01	11	10
00	01	00	0	0	0	1
		01	0	1	1	1
11	10	00	0	1	0	0
		01	0	1	0	0

$$F = A'BC' + AB'C' + BC'D.$$

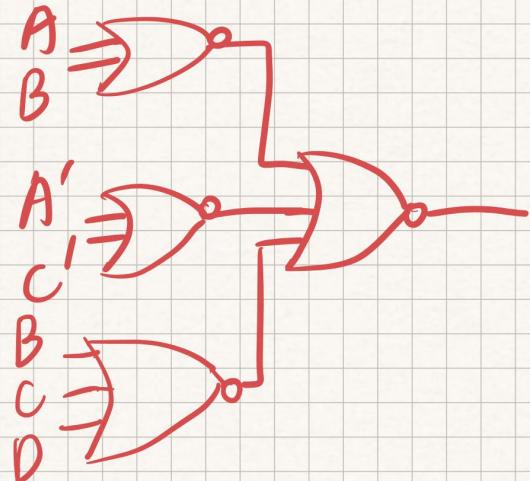
$$\begin{aligned}
 F &= A'B'C' + AB'C' + BC'D \\
 F' &= A'B' + AC + B'C'D' \\
 &= (A+B)(A'+C')(B+C+D)
 \end{aligned}$$

Question assigned to the following page: [6](#)

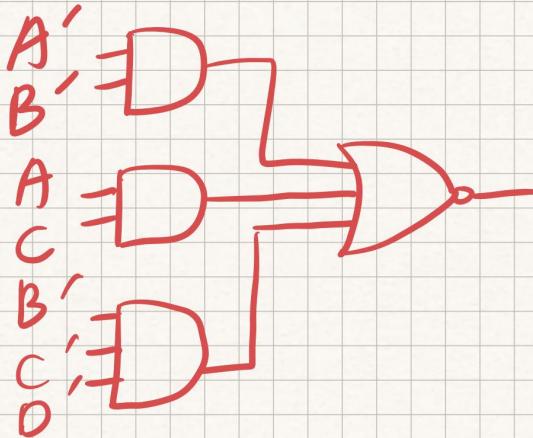
⑤ OR-AND



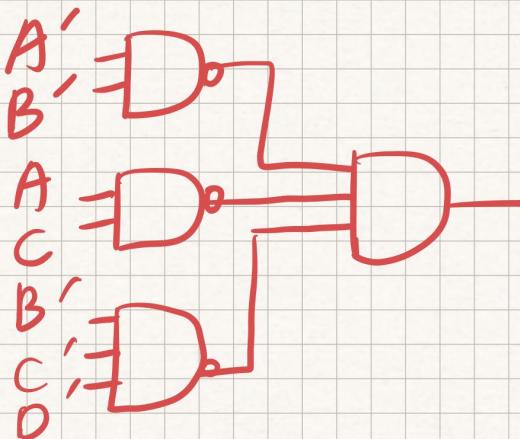
⑥ NOR-NOR



⑦ AND-NOR



⑧ NAND-AND



Question assigned to the following page: [7](#)

## 7 Multi-Level Gate Circuits (6pts)

Using AND and OR gates, find a circuit (draw it) to realize  $F(A, B, C, D) = \prod M(0, 1, 3, 13, 14, 15)$ . Try to minimize the number gates and gate inputs (fewer gates and gate inputs, more points). How many gates are there? How many gate inputs are there?

$$\text{Sol: } F(A, B, C, D) = \prod M(0, 1, 3, 13, 14, 15)$$

$$= \sum m(2, 4, 5, 6, 7, 8, 9, 10, 11, 12)$$

		AB	00	01	11	10		
		CD	00	01	11	10		
		00	0	4	1	12	1	
		01	0	5	1	13	0	
		11	3	0	7	1	15	0
		10	2	1	6	1	14	0
			1	1	1	10	1	

**SOP**  $F = AB' + A'B + AC'D' + A'CD' \rightarrow 5 \text{ gates, } 14 \text{ inputs (1)}$

$$= A(B' + C'D') + A'(B + CD') \rightarrow 7 \text{ gates, } 14 \text{ inputs (2)}$$

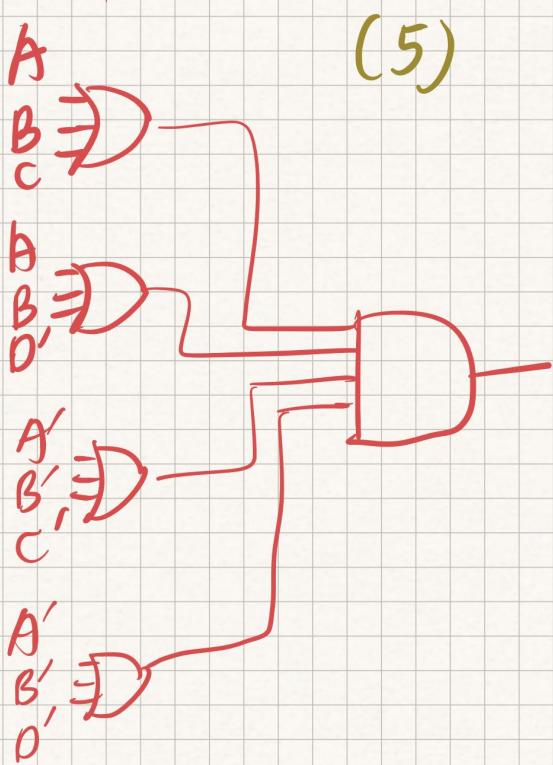
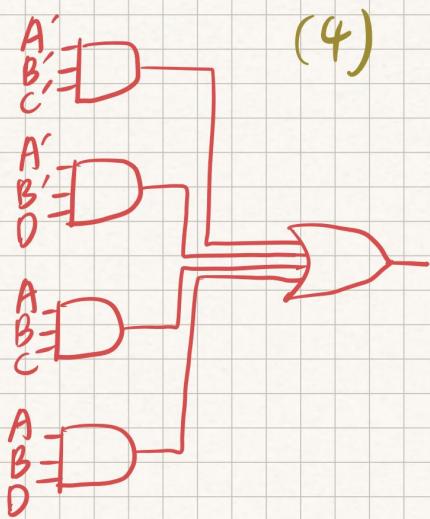
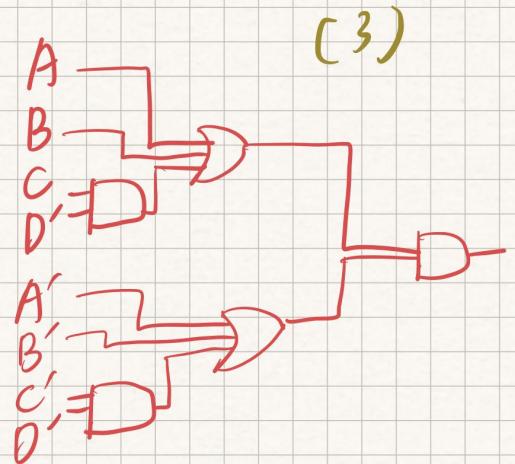
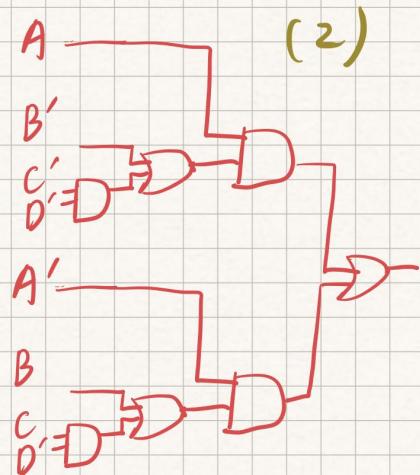
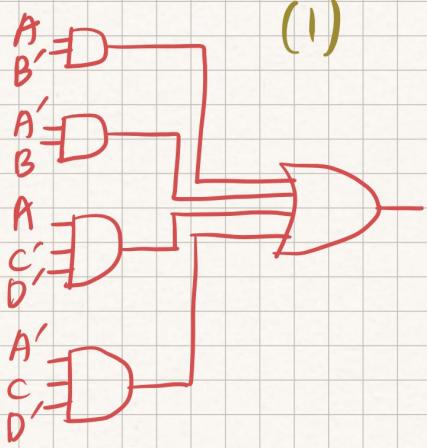
$$= (A + B + CD')(A' + B' + C'D') \rightarrow 5 \text{ gates, } 12 \text{ inputs (3)}$$

**POS**  $F' = A'B'C' + A'B'D + ABC + ABD \rightarrow 5 \text{ gates, } 16 \text{ inputs (4)}$

$$F = (A + B + C)(A + B + D')(A' + B' + C')(A' + B' + D') \rightarrow 5 \text{ gates, } 16 \text{ inputs (5)}$$

$$= (A + B + CD')(A' + B' + C'D') \rightarrow 5 \text{ gates, } 16 \text{ inputs}$$

Question assigned to the following page: [7](#)



Ans: 5 gates  
12 inputs

