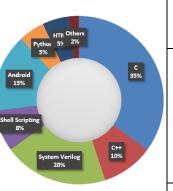
# Changxu "Chauncey"

# Yan

Address: 991 SW 15<sup>th</sup> St, Corvallis, OR 97333

Cellphone: (971) 678-2702



Email: Yanc @oregonstate.edu

Visit me at my website: supacyan.github.io

# **Objective**

Targeting an Internship or Entry-Level Opportunity in embedded systems design, testing and VLSI/FPGA design, layout. Available for software development engineering jobs.

### Education

Oregon State University

Corvallis, OR

Bachelor of Science, Electrical & Computer Engineering

Minor: Computer Science

Bilingual: fluent in Chinese and English in speaking, reading, and writing

Anticipated Graduation: March 2017

# **Courses Completed:**

VLSI System Design(FPGA), Energy-Efficient VLSI Design, Microcontrollers System Design, Computer Architecture, Assembly Language Programming, Multimedia Systems, Electronic Circuits, Signal and system, Operating Systems, Computer Networking, Senior Project.

# **Electrical Engineering Skills**

- o VLSI Layout with Static Free Electric
- o FPGA System Verilog with Quartus II
- o PCB Layout with Eagle CAD
- Microcontroller Programming
- o SPICEs, SMT Soldering

# **Computer Science Skills**

- CS: C/C++, Android Developing, Java
- o Shell Scripting, Python, Perl
- o HTML/CSS Website Design
- o C#, MySQL Database
- o Matlab, 3D Modeling

# **Relevant Experience**

# MECOP internship at Welch Allyn, Inc.

R&D department Computer Engineer Intern

- o Quick prototyping on several exploration projects using uC/Arduino
- o Microcontroller application and programming in Java on PC and Android

### **MECOP** internship at Mentor Graphics Corp

Apr. 2014 to Sep. 2014

Jun. 2015 to Dec. 2015

Caliber PEX team Software Testing Intern

- o Worked on testing Caliber software and database management
- o Linux software development using shell script and Perl

## **Projects & Prototyping**

Dec. 2013 to Present

- o Portable Bluetooth audio amplifier, Android Bluetooth voltmeter with FPGA
- o VLSI 4-bit multiplier and adder using static free Electric
- o Senior Project: Software define radio frequency receiver for Analog Device, Inc.
- o PCB design and layout for Senior Project, Electronics II and other projects
- o Quadcopter building, tuning, aerial shooting
- o Wastewater treatment plant gas data logger and Arduino controlling system
- o Door lock automation with Arduino, Raspberry Pi and Android application.

### **Computer Science Teaching Assistant**

Oct. 2013 to Apr. 2014

 $\circ$  Taught students in Python 3 and C++ during lab sections for half year.

## **Volunteering**

President of Oregon State University Badminton Club

FTC "Ring It Up" Robotic Tournament field operator

Feb. 2013

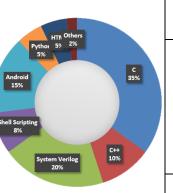
Professional Photo Editor at Youcan LLC., China Jan. 2011 to Aug. 2011

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Targeting an Internship or Entry-Level Opportunity in software development engineering. Available for hardware development engineering jobs.

## **Education**

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Corvallis, OR

Bachelor of Science, Electrical & Computer Engineering

Minor: Computer Science

Bilingual: fluent in Chinese and English in speaking, reading, and writing

Anticipated Graduation: March 2017

# **Courses Completed:**

Operating Systems I/II (C), Assembly Language Programming, Computer Networking, Computer Introduction (C++), VLSI System Design (System Verilog), Energy-Efficient VLSI Design, Microcontrollers System Design (C), Computer Architecture, Multimedia Systems (Linux), Electronic Circuits, Signal and system, Senior Project.

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