

Nucleo-144_Q (int-SMPS)

MB1549

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Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

△ Notes to generate the board layout.

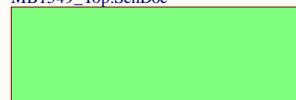
OPEN PLATFORM LICENSE AGREEMENT

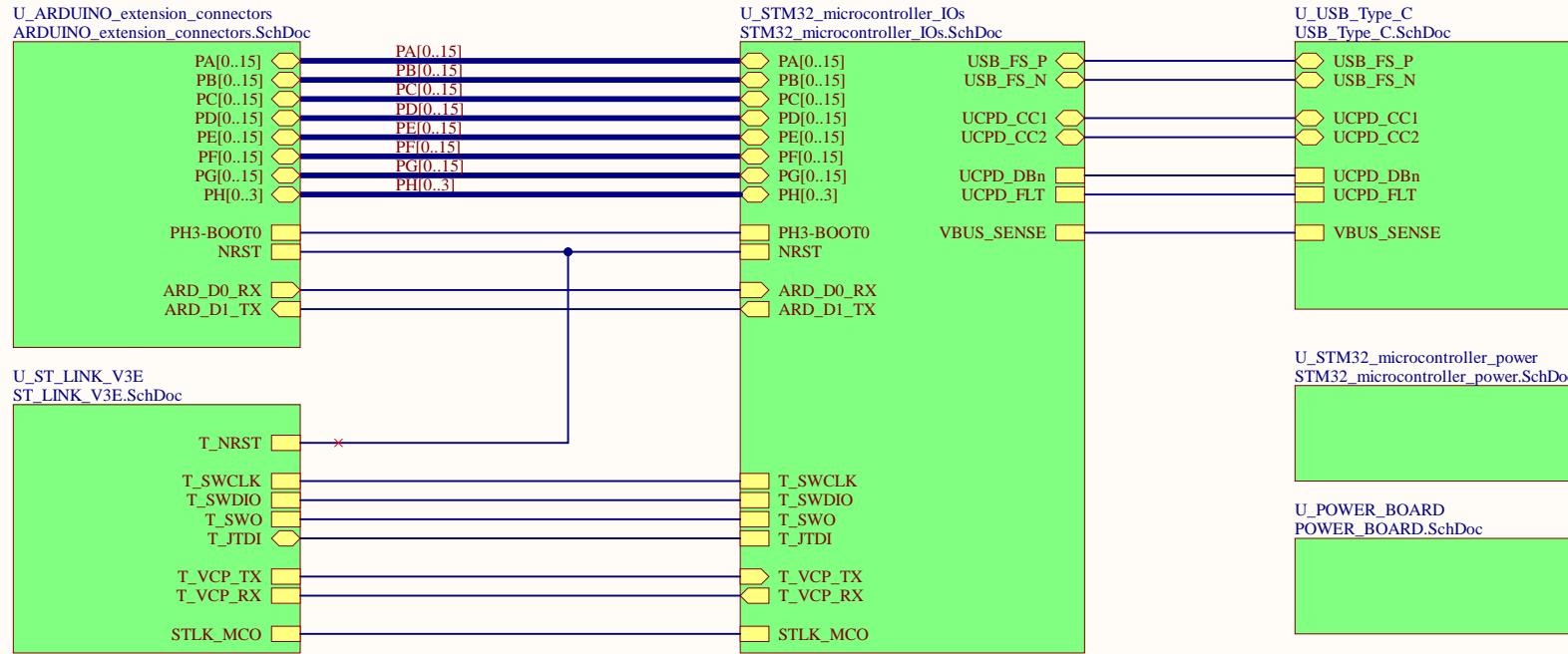
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U_MB1549_Top
MB1549_Top.SchDoc



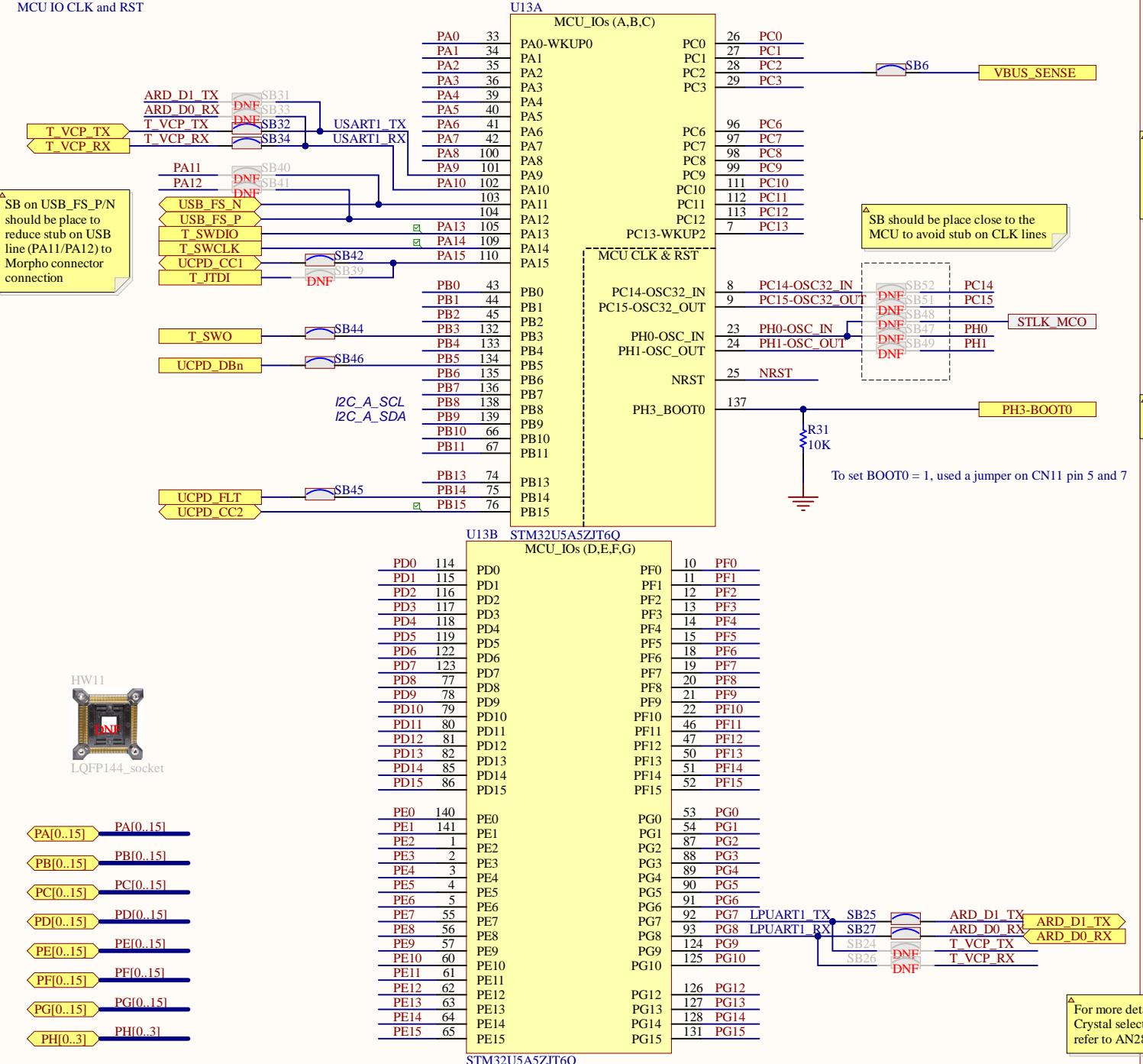


MECHANICAL PARTS

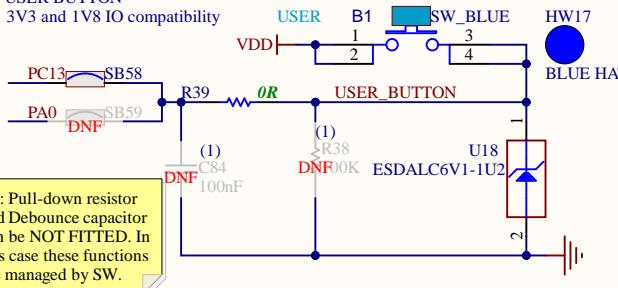
HW101
BOARD REF
MBxxxxy-zz sywxxxxx

HW102
BOARD CPN
Board CPN

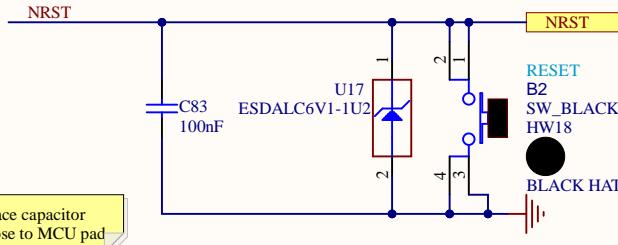
MCU IO CLK and RST



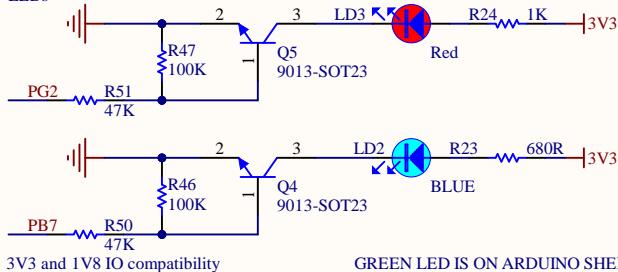
USER BUTTON



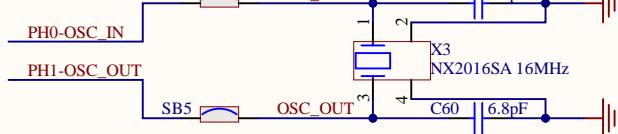
RESET FUNCTION



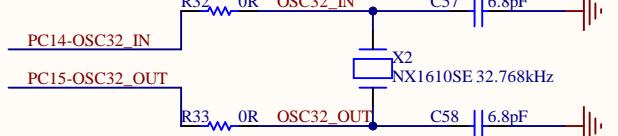
LEDs



EXTERNAL HSE CLK OPTIONAL



EXTERNAL LSE CLK



Title: STM32_microcontroller_IOS

Project: Nucleo-144_Q(int-SMPs)

Variant: U5A5ZJQ

Revision: C-04

Size: A4

Date: 27-Sept-22

Reference: MB1549



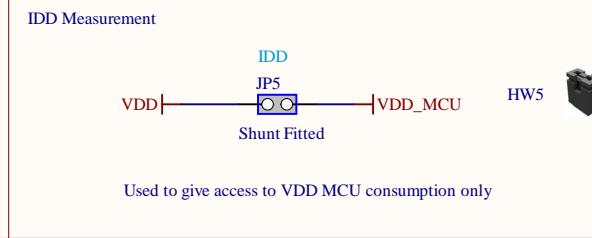
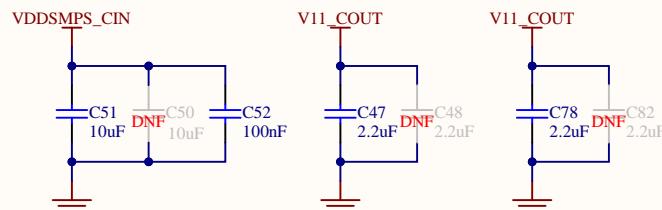
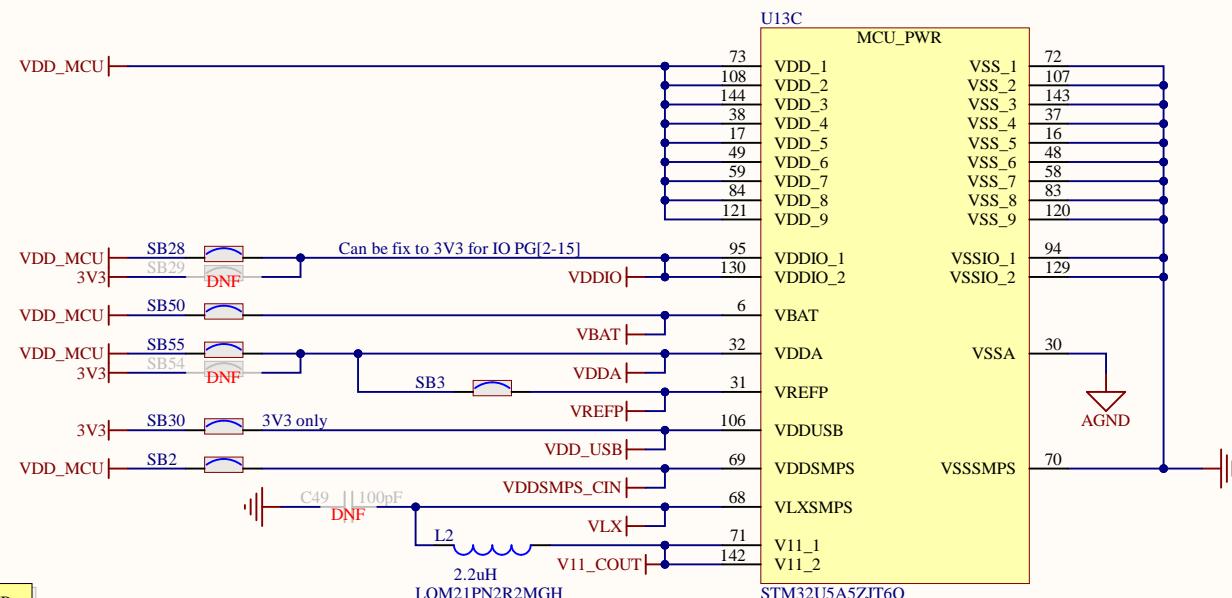
life.augmented

Sheet: 3 of 8

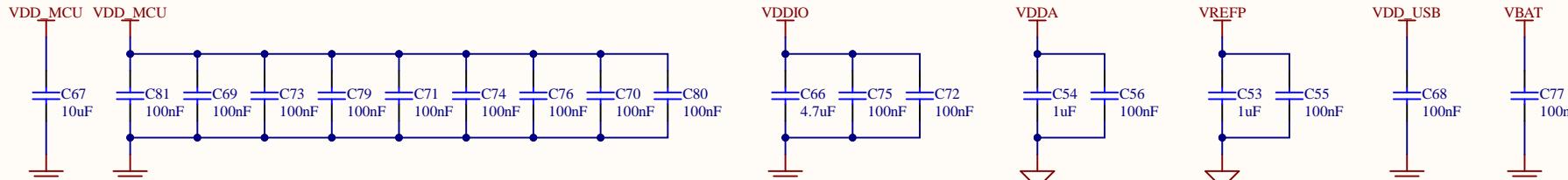
MCU PWR SUPPLIES

Operating range: $1V71 < VDD < 3V6$
 Operating range: $1V08 < VDDIO < 3V6$ (only for IO PG[2:15])
 Operating range: $1V65 < VBAT < 3V6$
 Operating range: $1V62 < VDDA < 3V6$
 Operating range: $1V62 < VREFP < 3V6$ (depend of VDDA)
 Operating range: $1V71 < VDDSMPS < 3V6$

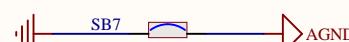
Operating range: $3V0 < VDDUSB < 3V6$



MCU DECAPS Ceramic capacitor (Low ESR, ESR<1ohm)



For more detail about decap, please refer to AN5373



Title: STM32_microcontroller_power

Project: Nucleo-144_Q(int-SMPs)

Variant: U5A5ZJQ

Revision: C-04

Size: A4 | Date: 27-Sept-22

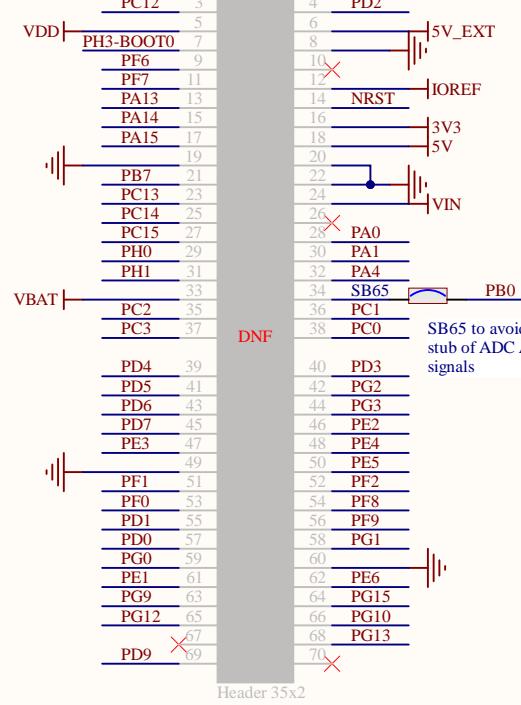
Reference: MB1549



life.augmented



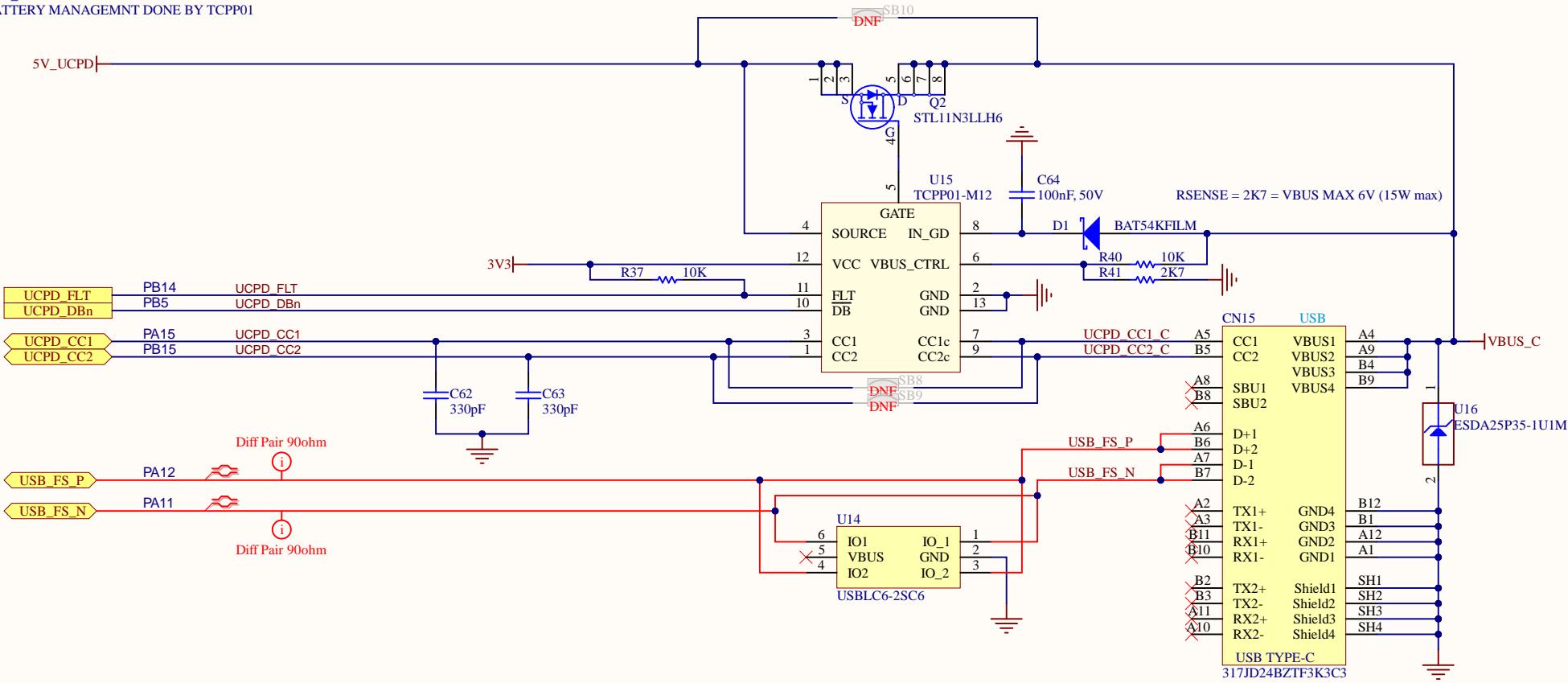
MORPHO CONNECTOR LEFT SIDE



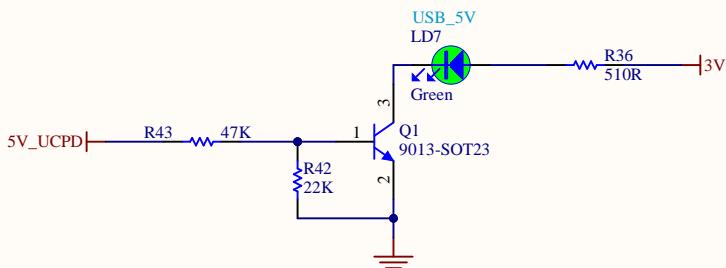
USB_TYPE_C CONNECTOR

USB_TYPE_C SINK ONLY

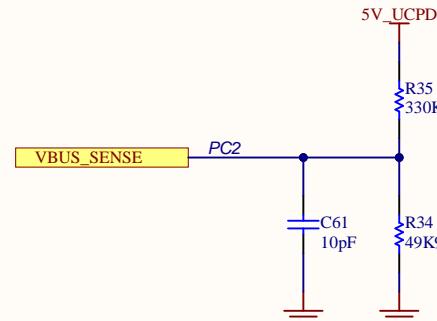
DEAD BATTERY MANAGEMENT DONE BY TCPP01



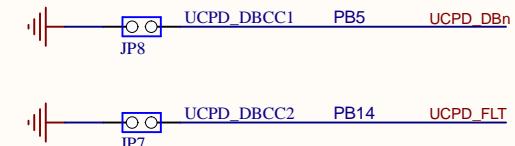
5V_USB_LED



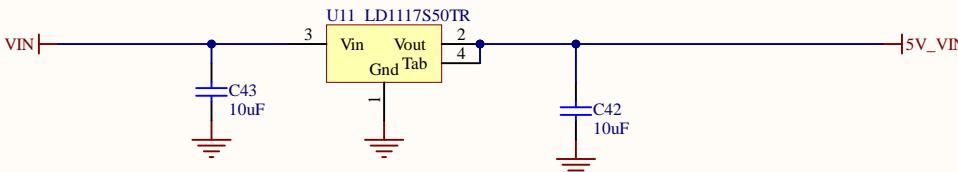
VBUS SENSE



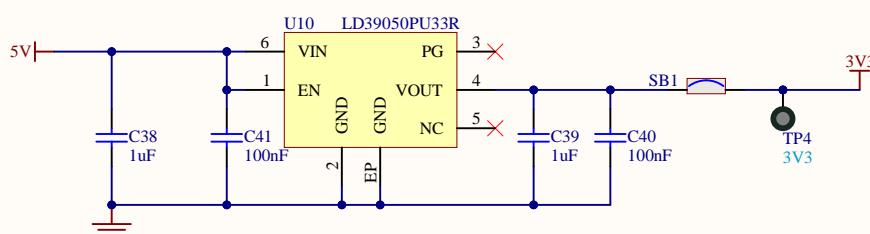
RESERVED FOR DEBUG DEAD BATTERY MANAGEMENT BY MCU



BOARD PWR: VIN / 5V (output current depend of VIN range)

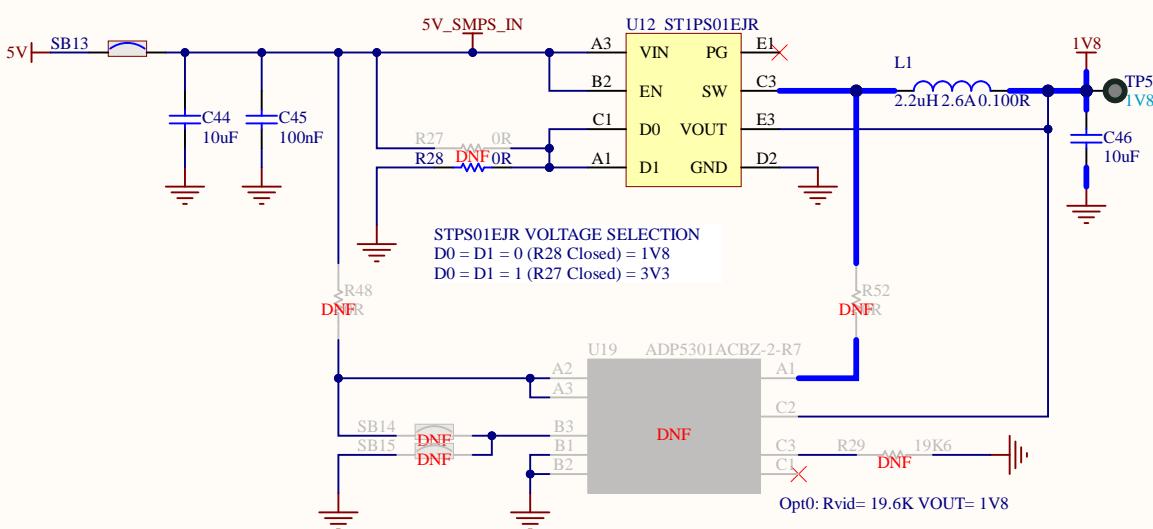


3V3 POWER SOURCE / 500mA



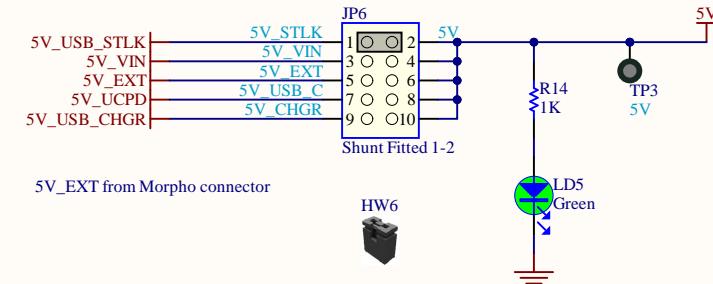
1V8 POWER SOURCE / 400mA

ONLY ONE DEVICE TO BE USED U12 or U19

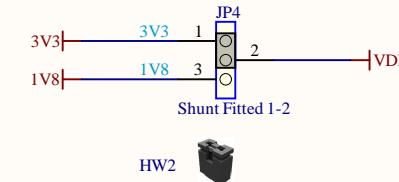


ADP5301 MODE SELECTION
SYNC/MODE = 1 (SB14 Closed) = HYSTERESIS MODE 50mA
SYNC/MODE = 0 (SB15 Closed) = PWM MODE 500mA

5V PWR SELECTION
MAX CURRENT 500mA WHEN USING POWER INPUT 5V_USB_STLK

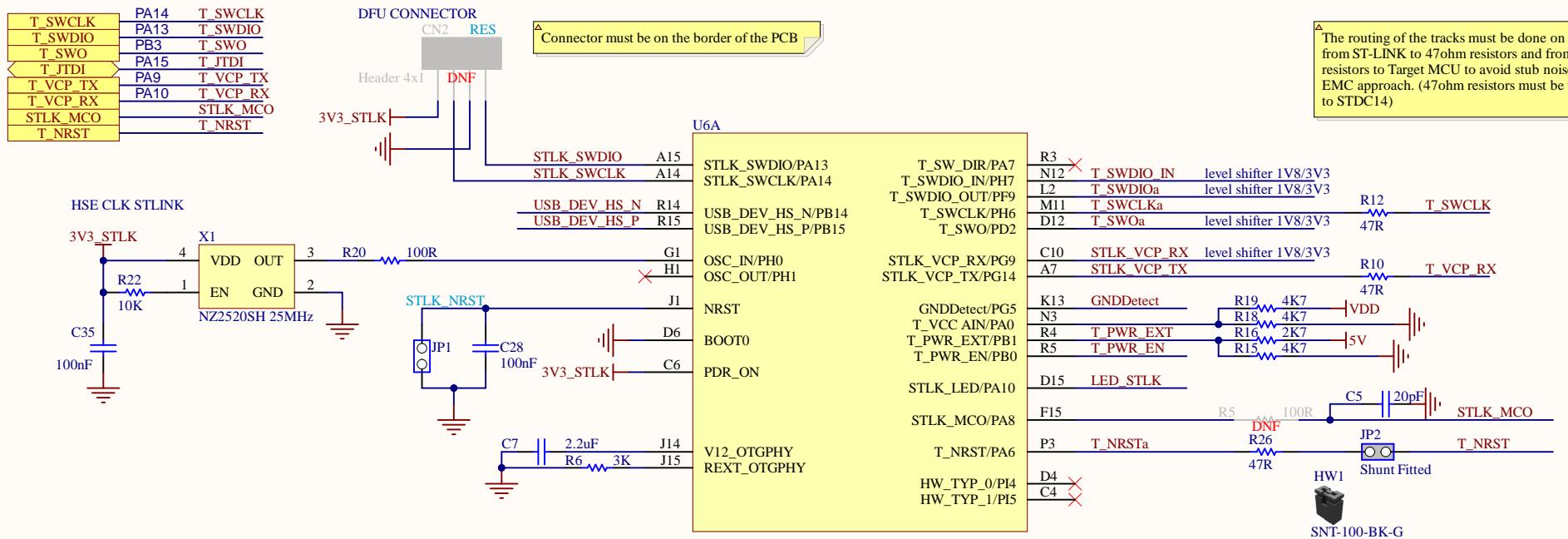


VDD MCU SELECTION AND CURRENT MEASUREMENT

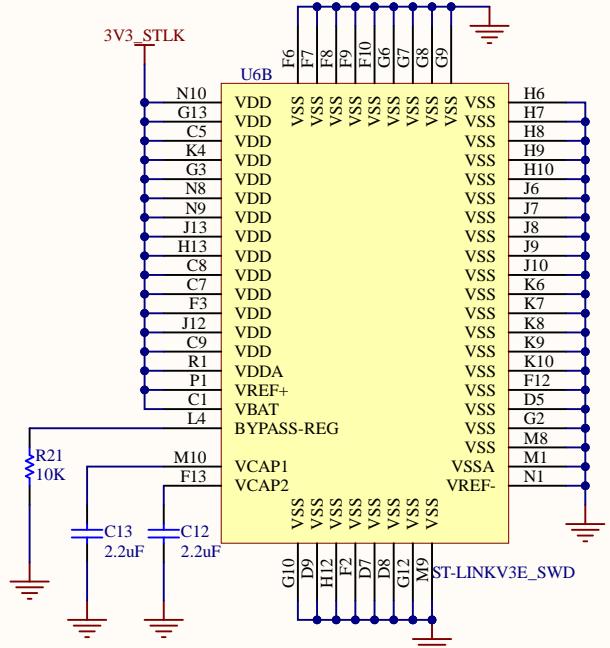


VDD table	JP4
VDD = 3V3	[1-2]
VDD = 1V8	[2-3]

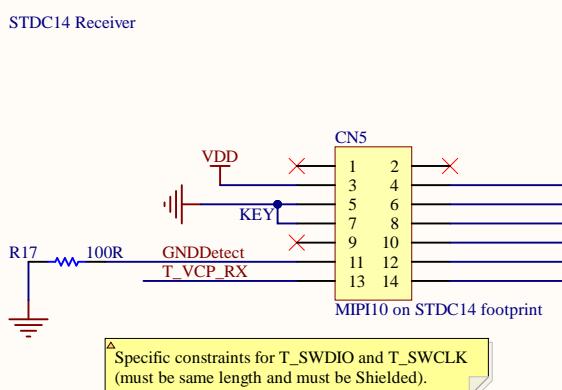
STLINK MCU



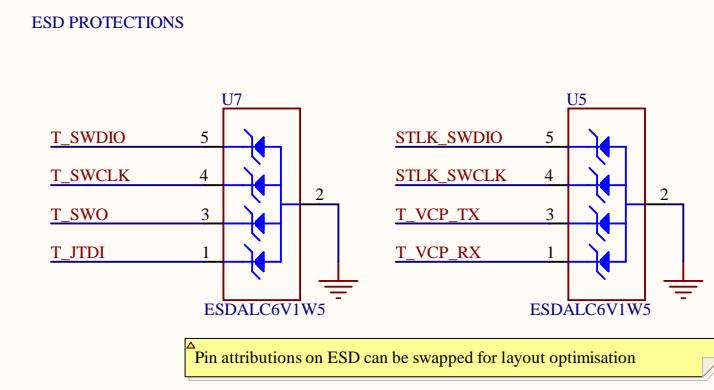
ST-LINK POWER



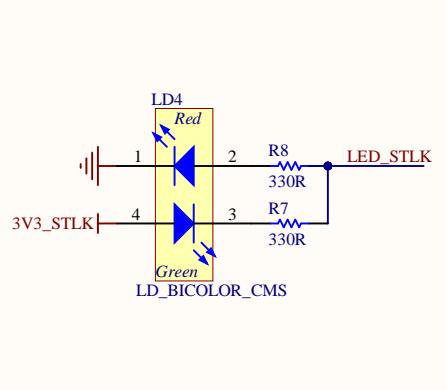
STDC14 Receiver



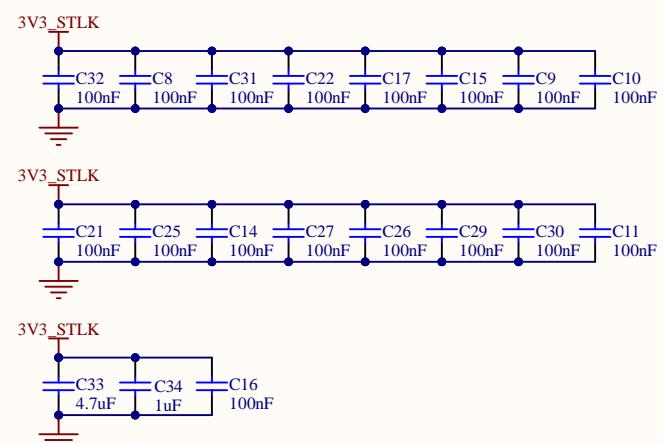
ESD PROTECTIONS



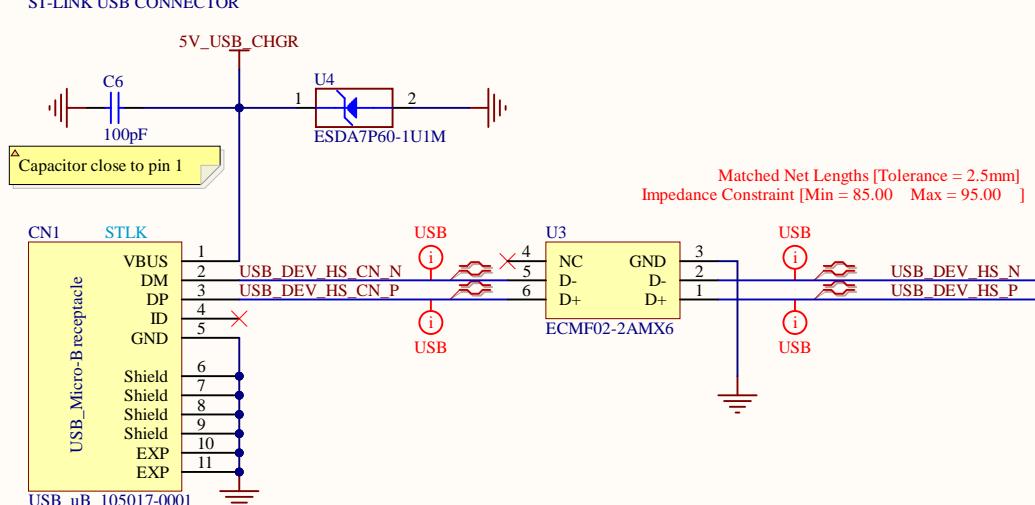
LED STLK



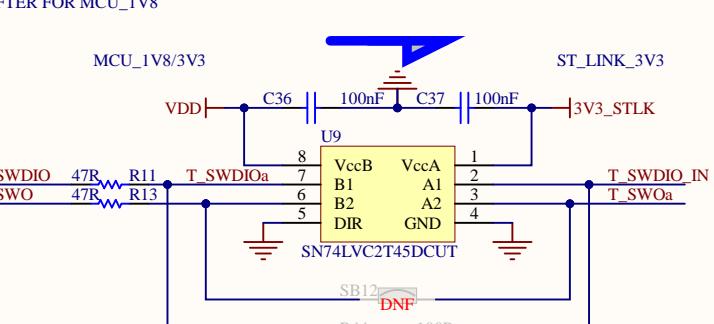
3V3_STLK



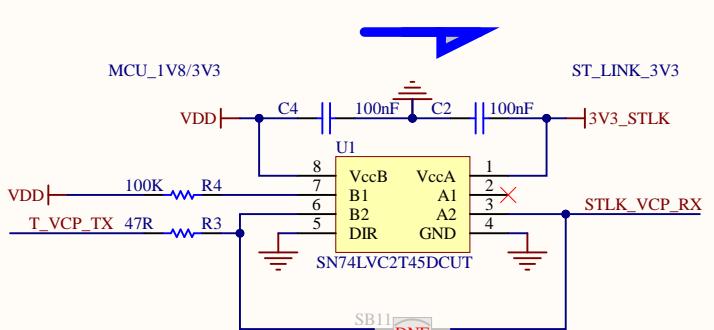
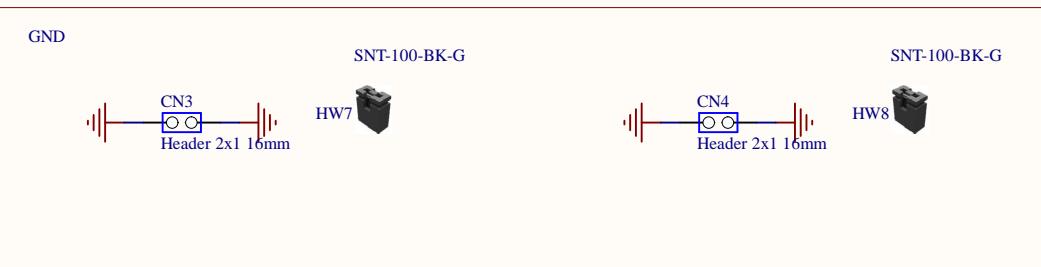
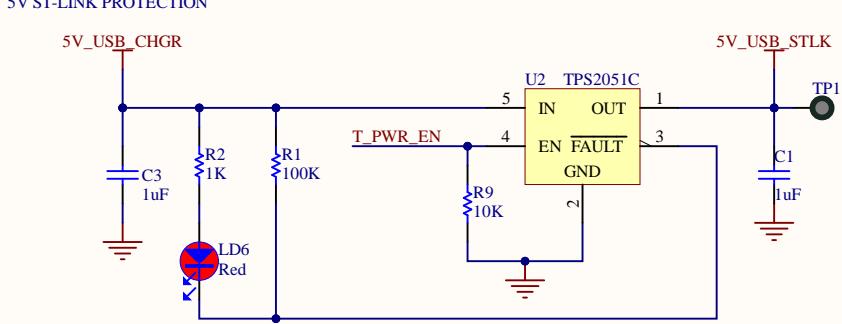
ST-LINK USB CONNECTOR



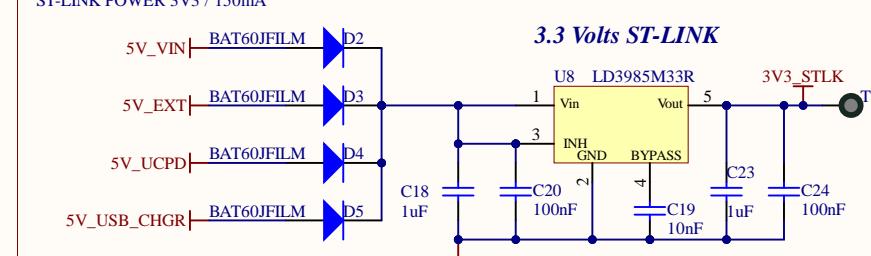
SW LEVEL SHIFTER FOR MCU_1V8

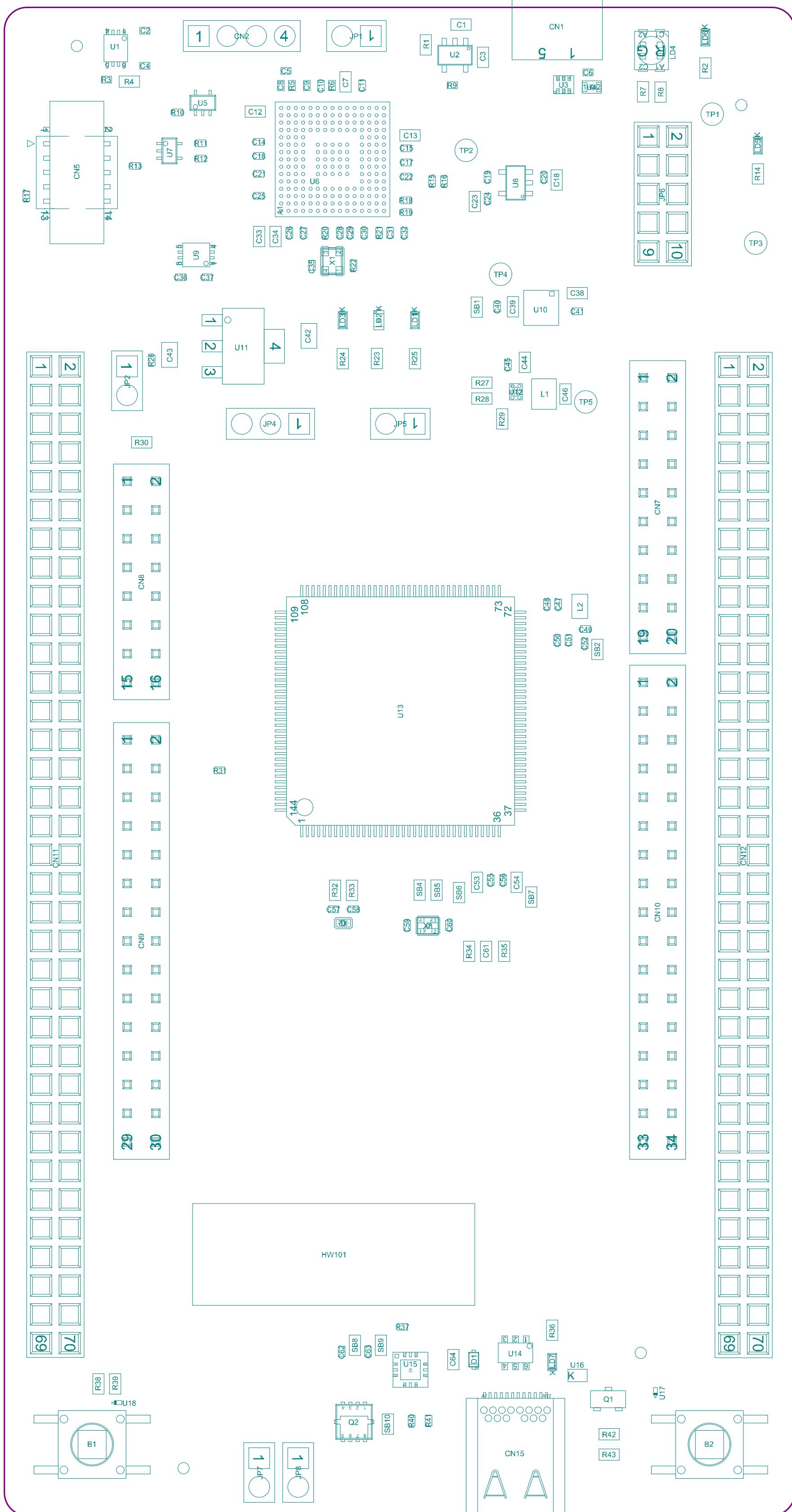


5V ST-LINK PROTECTION



3.3 Volts ST-LINK





Project: Nucleo-144_Q (int-SMPS)

Layer: M14-Top Assembly

Gerber: .GM14

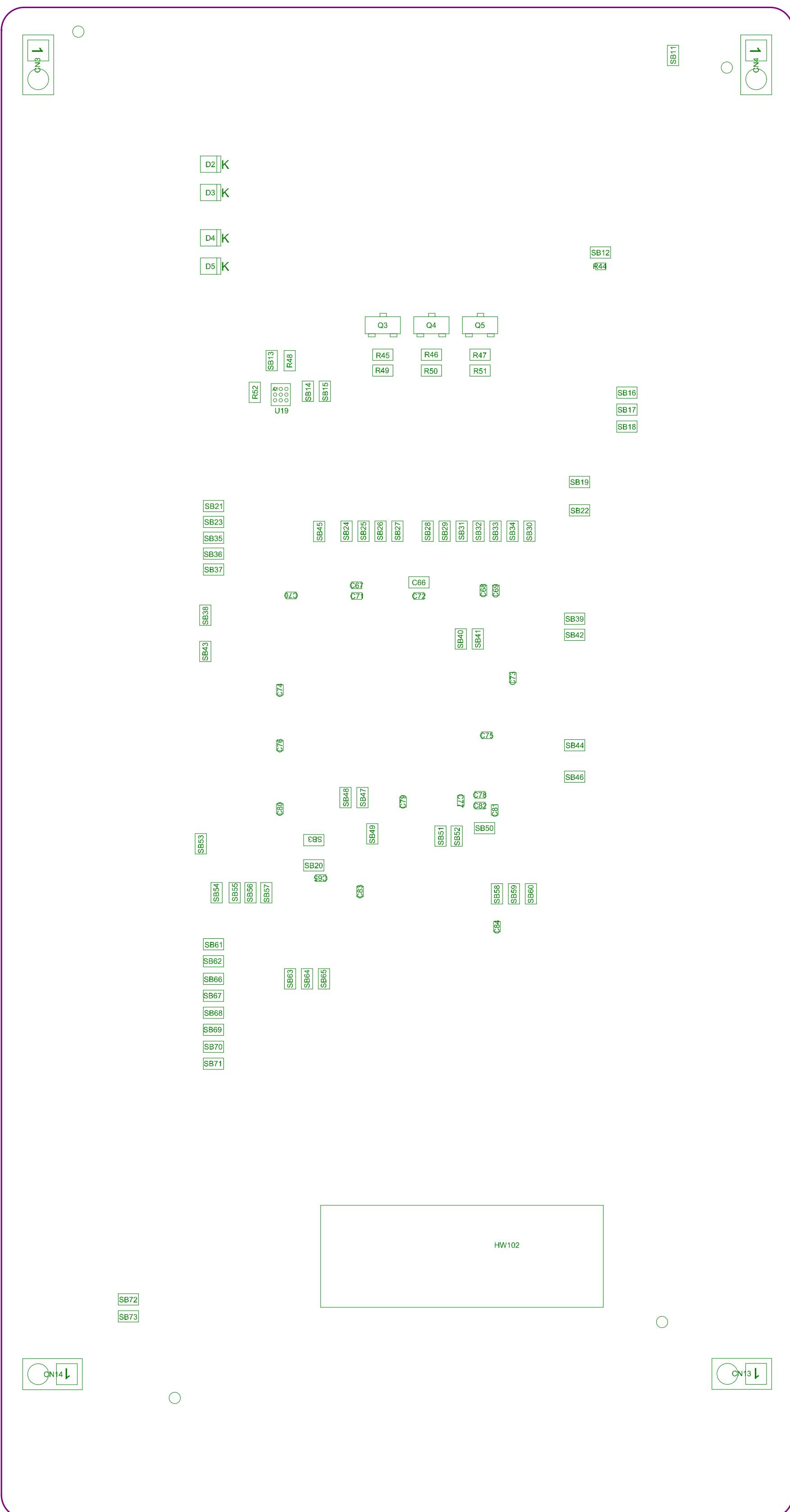
Variant: [No Variations]

Ref: MB1549

Date: 27-Sept-22

Rev: C





Project: Nucleo-144_Q (int-SMPS)

Layer: M15-Bottom Assembly	Gerber:.GM15
Variant: [No Variations]	Ref: MB1549
Date: 27-Sept-22	Rev: C

