

DATE	REV	BY	DESCRIPTION
2024-07-03	0.1		First Revision

6

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D

C

B

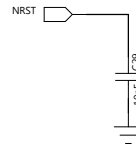
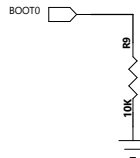
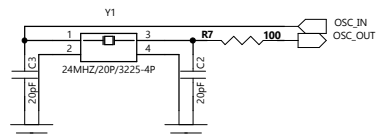
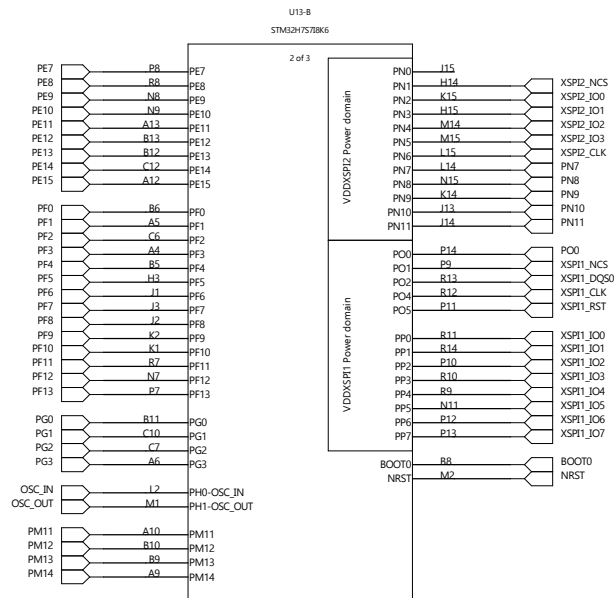
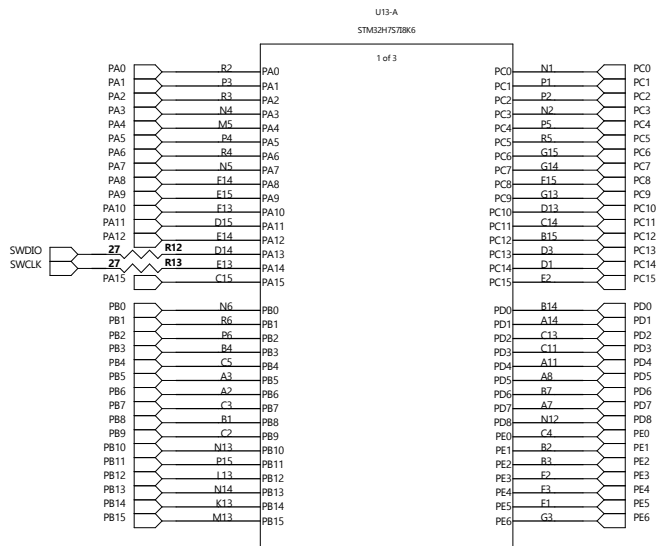
A

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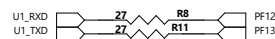
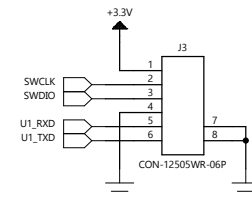
C

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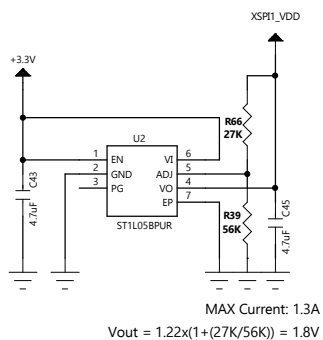


DEBUG

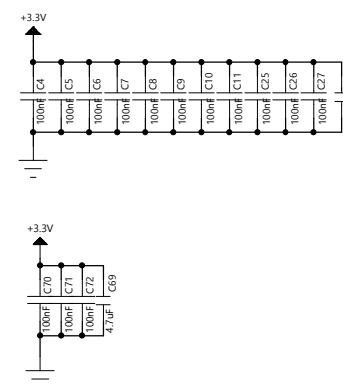
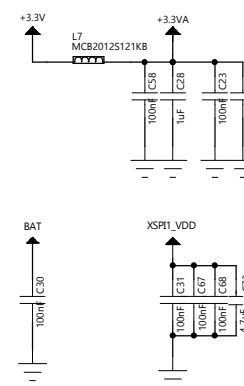
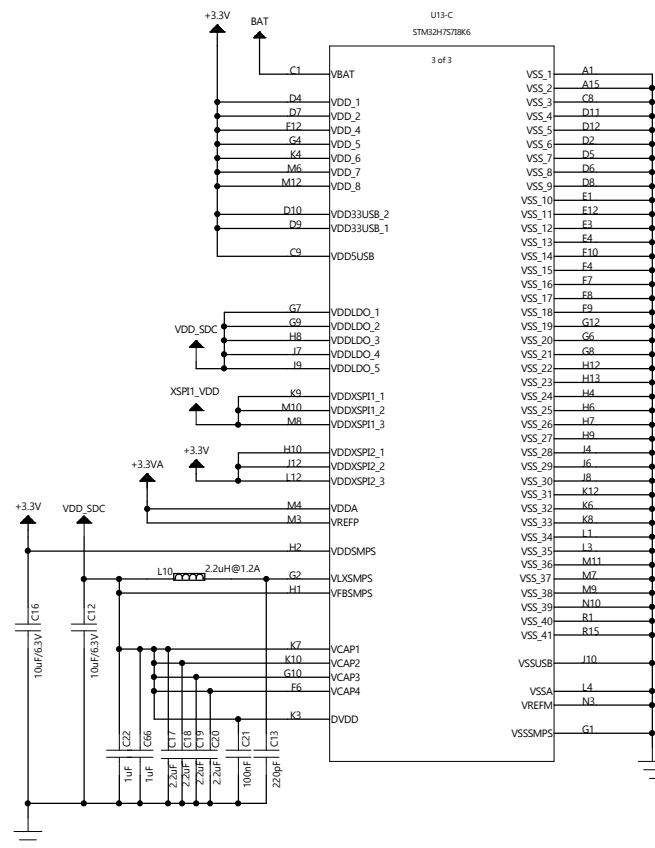


NAME:	HG_H7S_SOM	
SHEET:	MCU	
REV:	0.1	SHEET: 2 OF 4

PSRAM POWER



LED



6

5

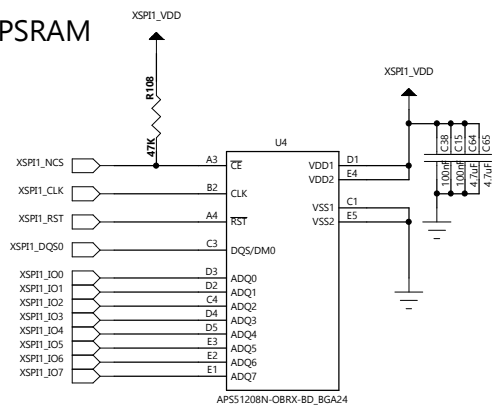
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OSPI PSRAM



- DQS0 with IO0..7 (Byte 0), and CLK, all in 50 mils
- NCS length can be in the range of up to CLK length +/-750 mils

QSPI FLASH

