

Mehthab Saheba Shaik

CONTACT

MOBILE No.: 8187087168

ALTERNATE CONTACT No.: 9701084980

Email id: mehthabsaheba.s@students.iiit.ac.in

EDUCATION

INTERNATIONAL INSTITUTE OF INFORMATION TECHNOLOGY, HYDERABAD
BTECH. IN ELECTRONICS AND COMMUNICATIONS ENGINEERING

Current Semester : 6th semester, 3rd year

Cum. GPA: 8.23

COURSEWORK

Computer programing ,Data Structures and Algorithms,Discrete Mathematics(Mathematics 1),Linear Algebra and Group Theory(Mathematics 2),Probability and Random Processes, Statistical Methods in AI,Mobile Robotics,Topics in Applied Optimization, Digital Signal Processing,Signals and Systems,Communication Theory 1, Computer System Organisation,Digital Logic and Processors,Embedded Hardware Design,Electronic Circuits and Electrical Science

SKILLS

PROGRAMMING

C/C++ - STL and OOP structure ,some meta programming experience and can use few boost libraries Python - can implement algorithms ,can use packages and libraries for ml and data handling,basic tensorflow,understand the oop structure Matlab - .mat and .m scripting , familiar with robotics,signal processing and vision tool boxes,familiar with searching and using in-built functions as needed Basic Java- oop structure, inbuilt libraries,JS(basic)

HONORS

CENTRE FOR SECURITY,THEORY AND ALGORITHMIC RESEARSH - CSTAR

Advisor : Dr. Pawan Kumar

Hons. Project 1 - studied kernels for machine learning (ANOVA kernel etc, kernel conjugate gradient.) and some iterative methods

Hons. Project 2 - ONGOING : will me assisting in developing a better solver for bundle adjustment algorithm.

COURSE PROJECT WORK

Paper presentation and implementation - Nonsmooth Nonnegative Matrix Factorization(nsNMF) Paper presentation and implementation - Hogwild!(abridged version) Paper Presentation and implementation - ADAM Paper presentation and Implementation - PNAS - Acoustic Echoes reveal room shape Implementation of basic algorithms -PCA,etc,LM algorithm, trajectory planning,newton descent on manifold algorithm, data structures and graph algorithms Implementation of program for fpga to deal with floating point arithmetic Designed an op-amp meeting set constraints Hardware - ambient light controlled LED, active mobile phone detector, simple audio amplifier

LANGUAGES

English(fluent)

WORK EXPERIENCE

TA for Digital Systems and Microcontollers Course (current)