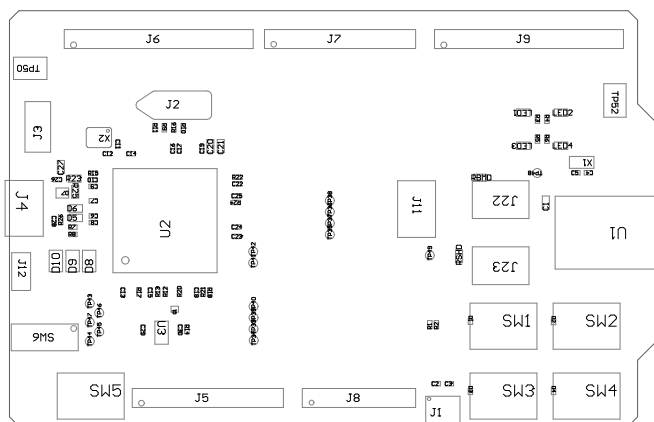


REVISION

REV	DESCRIPTION	DATE	BY
v2	Added connectivity and Atmel prog	12/11/2015	PJB
vA	Updated, inverted LED polarity	2/10/2016	DCN
vB	Updated, fixed Q1, U2.97 to GND	3/31/2016	PJB



LAYER USAGE

Board Outline
Assembly Top

RIGADO
For Rigado
CONFIDENTIAL

ENGINEER: DCN

DATE: 3/31/2016

DWG NO: 320-00050

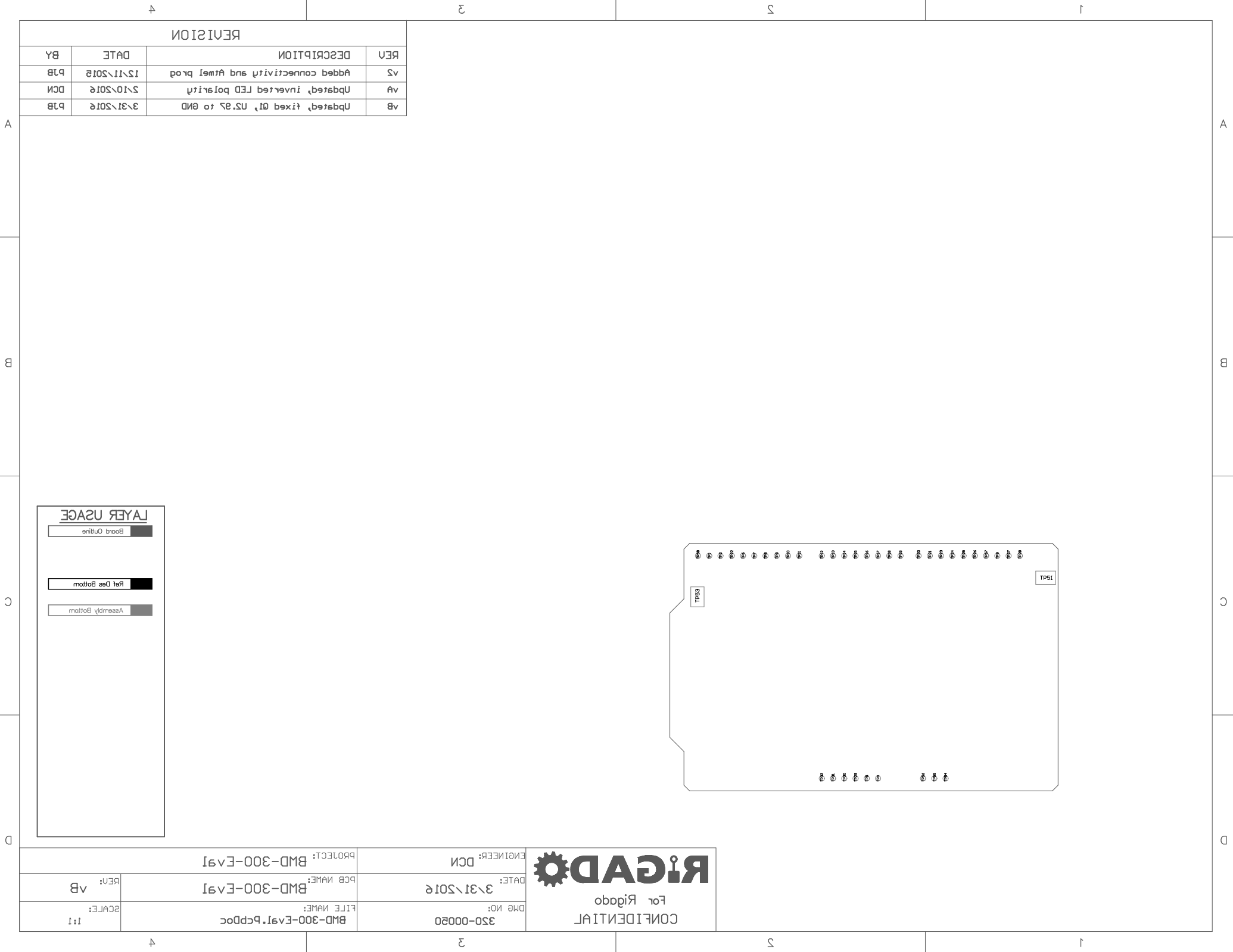
PROJECT: BMD-300-Eval

PCB NAME: BMD-300-Eval

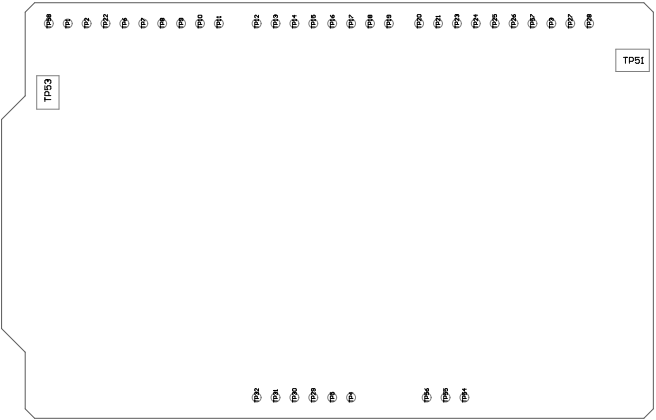
FILE NAME: BMD-300-Eval.PcbDoc

REV: vB

SCALE: 1:1



<div>CONFIDENTIAL</div> <div>For Rigado</div> <div></div>		ENGINEER: DCN	
DWG NO: 320-00080		DATE: 3\31\2018	
BMD-300-Ev1.PcbDoc		PCB NAME: BMD-300-Ev1	
FILE NAME:		REV: vB	
SCALE: 1:1		PROJECT: BMD-300-Ev1	



LAYER USAGE

Board Outline

Ref Des Bottom

Assembly Bottom

REVISION			
REV	DESCRIPTION	DATE	BY
v2	Added connectivity and A7we1 prod	12\11\2018	p1B
v1	Updated, inverted LED polarity	2\10\2018	DCN
vB	Updated, fixed Q1 U2 to GND	3\31\2018	p1B