Shreenivas Bharadwaj

https://shreenibhar.github.io
vshreenivasbharadwaj@gmail.com

EDUCATION

NIT TRICHY
B.Tech. in Computer Science
Expected June 2018 | TamilNadu, India
Cum. GPA: 8.29/10
University domain

VIDYA MANDIR MYLAPORE
Grad. May 2014| TamilNadu, India
Aggr. Marks: 482/500 (AISSCE)

LINKS

Github: shreenibhar
Gmail: vshreenivasbharadwaj

COURSEWORK

UNDERGRADUATE
Data Warehouse and Data Mining
Artificial Intelligence
Machine Learning
Data Structures and Algorithms
Design and Analysis of Parallel algorithms
Operating Systems
Automata theory
Microprocessors (MASM and 8086)

SKILLS

PROGRAMMING
Over 5000 lines:
•C/C++ •Python •Android •Cuda
•Matlab •LaTeX •Tensorflow • Java
Over 1000 lines:
•HTML •CSS • Javascript • Jquery
•OpenCV •MySQL •Bash •Scikit-learn
•Scikit •Numpy
Familiar:
•PHP •NodeJS •Flask •Assembly •NS2 simulator

EXPERIENCE

AMAZON Summer Internship
May 2017 – July 2017 | Chennai, TamilNadu
- Created a connection service to link multiple services run by Kindle Digital Commerce Platform using Coral framework and Spring.
- Built the user interface for the connection service.

DELTA | The university web and app team
June 2015 – June 2018 | Trichy, TamilNadu
- Developed multi threading drivers and AI logic for a Battle Code like simulator platform in C/C++ for an AI event in the university tech fest Pragyan .

RESEARCH

LANGUAGE TECHNOLOGIES RESEARCH CENTER IIIT HYDERABAD | Natural Language Processing
May 2016 – July 2016 | Hyderabad, Telangana
Improved the accuracy of Named Entity Recognition task for languages with sparse labels. English task achieved 90% and Dravidian languages like Hindi achieved 78% accuracy in related datasets. The project was implemented in Python Tensorflow framework. Github project has achieved 290 plus stars. Paper was published as part of the proceedings in ICON-2016 conference. Links: Conference, Paper, Github

NETWORKS SIMULATOR LAB NIT TRICHY | Networking Optimization
July 2016 – Dec 2016 | Trichy, TamilNadu
Optimized the MAC 802.11 wireless network Contention Window mechanism with a dynamic regressive learning algorithm based on Markov models. The project was implemented in NS2 simulator. Publication was published in Vol.7 No.2 IJDIWC(SDIWC) journal. Links: Journal Paper

PARALLEL COMPUTING IIT DELHI | Parallel Computing
Acceleration of Sparse Vector Autoregressive Modeling of granger causality using GPUs. Achieved 650x speedup than the regular CPU code in the context of fMRI Brain imaging. The project was implemented in Nvidia CUDA platform involving parallelization of existing algorithms. BLAS algorithms were extensively optimized. Paper is applied to the flagship IPDPS IEEE conference. Links: Conference

AWARDS

2014 AISSCE School topper in Physics and School 3rd overall and Best CS project.
2014 NSO(SOF), IMO(SOF), NCO(SOF) Top 25 in State, Top 350 in country.
2014 High distinction in Australian National Chemistry quiz.
2016 2nd best publication in Vortex 2016 CSE symposium for Audio separation.

SOCIETIES

2015-18 Pragyan team . Festember team .
2015-18 Delta Web team of NIT Trichy.