Shreenivas Bharadwaj

https://shreenibhar.github.io
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EDUCATION

NIT TRICHY
B.Tech. in Computer Science
Expected June 2018 | TamilNadu, India
Cum. GPA: 8.28/10
University domain

VIDYA MANDIR MYLAPORE
Grad. May 2014| TamilNadu, India
Aggr. Marks: 482/500 (AISSCE)

LINKS

Github: **shreenibhar**
Gmail: **vshreenivasbharadwaj**

COURSEWORK

UNDERGRADUATE
Data Warehouse and Data Mining
Artificial Intelligence
Machine Learning
Data Structures and Algorithms
Design and Analysis of Parallel algorithms
Operating Systems
Automata theory
Microprocessors (MASM and 8086)

SKILLS

PROGRAMMING
Over 5000 lines:
•C/C++ •Python •Android •Cuda
•Matlab •LaTeX •Tensorflow • Java
Over 1000 lines:
•HTML •CSS • Javascript • Jquery
•OpenCV •MySQL •Bash •Scikit-learn
•Scikit •Numpy
Familiar:
•PHP •NodeJS •Flask •Assembly •NS2 simulator

EXPERIENCE

AMAZON Summer Internship
May 2017 – July 2017 | Chennai, TamilNadu
- Worked in Digital Commerce Platform team part of the Kindle umbrella.
- Implemented an interface to access multiple services run by Amazon. Coral framework and Spring was used to power the interface and Amazon's UI framework for the user interface.
- Improved existing service monitoring system by adding more metrics.

DELTA | The university web and app team
June 2015 – June 2018 | Trichy, TamilNadu
- Developed multi threading drivers and AI logic for a Battle Code like simulator platform in C/C++ for an AI event in the university tech fest **Pragyan** .

RESEARCH

LANGUAGE TECHNOLOGIES RESEARCH CENTER IIIT HYDERABAD | Natural Language Processing
May 2016 – July 2016 | Hyderabad, Telangana
Created an end to end interface performing Named Entity Recognition task for languages with sparse labels using Recurrent Neural Networks. The project was implemented in Python Tensorflow framework. Github project has achieved 250 plus stars. Publication was published as part of the proceedings in ICON-2016 conference.
Links: **Conference**, **Paper**, **Github**

NETWORKS SIMULATOR LAB NIT TRICHY | Networking Optimization
July 2016 – Dec 2016 | Trichy, TamilNadu
Optimized the MAC 802.11 wireless network Contention Window mechanism with a dynamic learning algorithm based on Markov models. The project was implemented in NS2 simulator. Publication was published in Vol.7 No.2 IJDIWC(SDIWC) journal.
Links: **Journal** **Paper**

PARALLEL COMPUTING IIT DELHI | Parallel Computing
Improved performance of Granger causality analysis of human brain using fMRI. The project was implemented in Nvidia CUDA C platform involving parallelization of existing algorithms. BLAS concepts were extensively used.

AWARDS

2014 AISSCE School topper in Physics and School 3rd overall and Best CS project.
2014 NSO(SOF), IMO(SOF), NCO(SOF) Top 25 in State, Top 350 in country.
2014 High distinction in Australian National Chemistry quiz.
2016 2nd best publication in Vortex 2016 CSE symposium for Audio separation.

SOCIETIES

2015-18 **Pragyan team** . **Festember team** .
2015-18 Delta Web team of NIT Trichy.