Shreenivas Bharadwaj

https://cheeni666.github.io vshreenivasbharadwaj@gmail.com

EDUCATION

NIT TRICHY

B.TECH. IN COMPUTER SCIENCE Expected June 2018 | TamilNadu, India Cum. GPA: 8.3/10 University domain

VIDYA MANDIR MYLAPORE

Grad. May 2014 TamilNadu, India Aggr. Marks: 482/500 (AISSCE)

LINKS

Github: cheeni666 Facebook: ShreenivasBharadwajV Gmail: vshreenivasbharadwaj

COURSEWORK

UNDERGRADUATE

Data Warehouse and Data Mining Machine Learning Data Structures and Algorithms Design and Analysis of Parallel algorithms Operating Systems Automata theory Microprocessors (MASM and 8086)

SKILLS

PROGRAMMING

Over 5000 lines:

C/C++ • Python • Android • Django •

Matlab • LATEX • Tensorflow

Over 1000 lines:

HTML • CSS • JavaScript • OpenCV •

Java • MySQL • Bash • Scikit-learn, Scikit and Numpy

Familiar:

PHP • NodeJS • Assembly • NS2

simulator

EXPERIENCE

AMAZON SUMMER INTERNSHIP

Expected May 2017 - July 2017 | Chennai, TamilNadu

DELTA | THE UNIVERSITY WEB AND APP TEAM

June 2015 - June 2018 | Trichy, TamilNadu

- Created and deployed campus communication app.
- Created and deployed numerous apps for university tech fests.
- Part of the team which created a Battle Code simulator like platform in C/C++ for an Al event for the university tech fest. Major work was done on multi threading and Al logic.

RESEARCH

LANGUAGE TECHNOLOGIES RESEARCH CENTER IIIT HYDERABAD | Research Intern

May 2016 - July 2016 | Hyderabad, Telangana

Worked with the LTRC research lab under **Prof Manish** to create, a tool which performs Named Entity Recognition task for languages with sparse labels using Recurrent Neural Networks. The project was implemented in Python Tensorflow framework. **Click here** for the github link to the project. Publication was published as part of the proceedings of **ICON-2016** conference.

Design and Analysis of Parallel algorithms **NETWORKS SIMULATOR LAB NIT TRICHY | RESEARCH ASSISTANT**

July 2016 - Dec 2016 | Trichy, TamilNadu

Worked with the Networks Research lab under **Prof Nithya** on optimizing the MAC 802.11 wireless network Contention Window mechanism with a dynamic learning algorithm based on Markov models. The project was implemented in NS2 simulator. Publication was published in **SDIWC journal**.

IIT DELHI RESEARCH INTERN | RESEARCH ASSISTANT

Worked with **Prof Rahul Garg** on Granger causality analysis of human brain using fMRI. The project was implemented in Nvidia CUDA C platform involving parallelization of existing algorithms.

INSTRUMENT RECOGNITION | Personal project

Worked on Instrument Recognition in polyphonic audio using Recurrent Neural Networks. Publication submitted.

AWARDS

2014 AISSCE School topper in Physics and Best CS project.

2016 2nd best publication in Vortex 2016 CSE symposium.

2016 CodeJam round 1 qualified.

2017 Hackerrank Week of Code 30 Silver medal.

SOCIETIES

2015-18 Pragyan Web ops team.
2015-18 Festember Web ops team.
2015-18 Delta Web team of NIT Trichy.